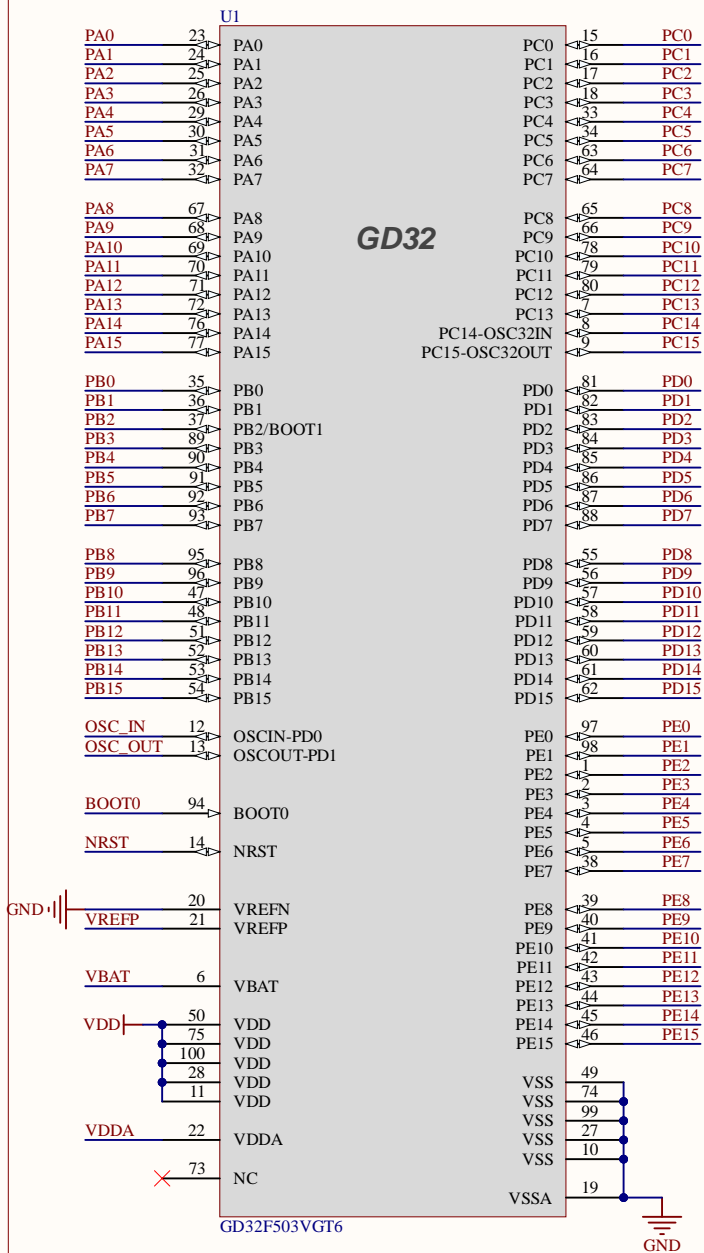
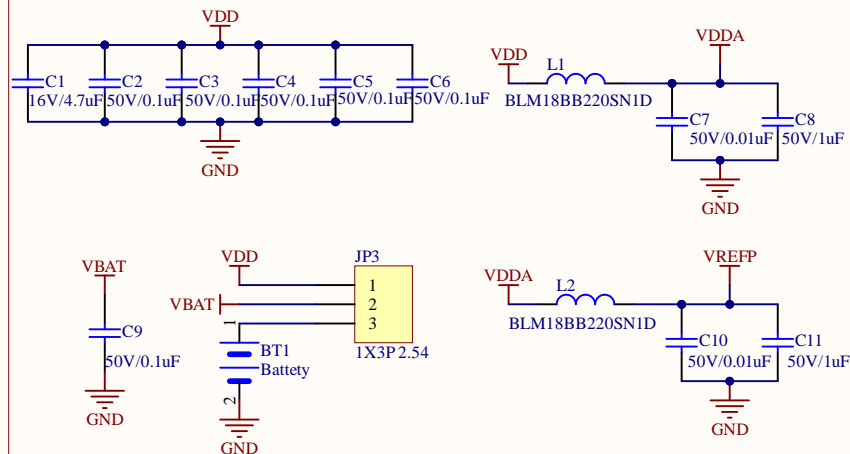


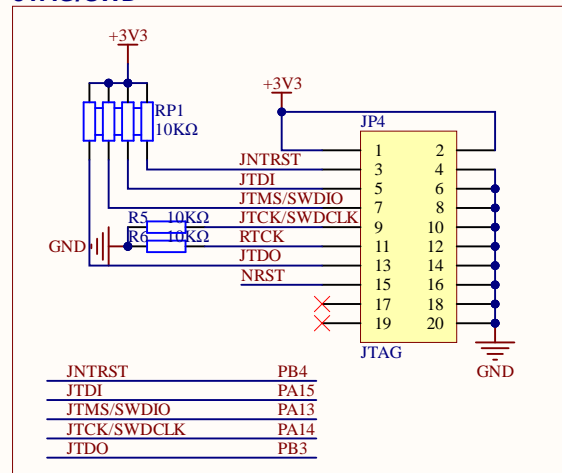
MCU



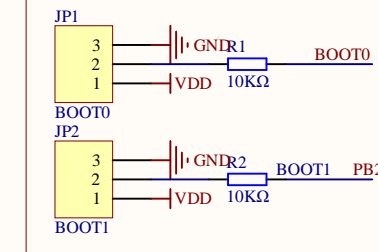
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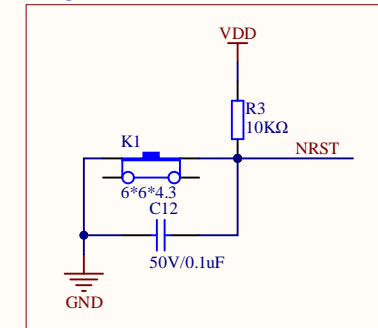
JTAG/SWD



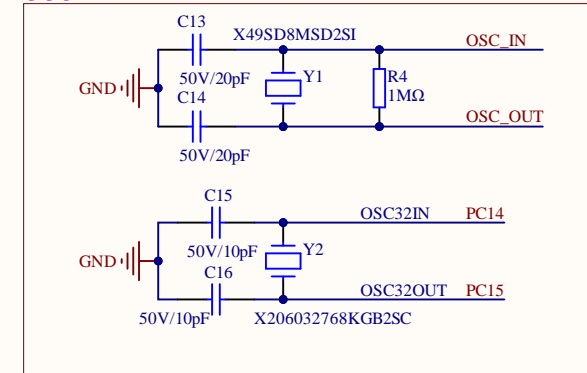
BOOT



NRST



OSC



Project Title: GD32F503V-EVAL

Sheet Title: MCU

Size:
A4

Designer: bruce.wang

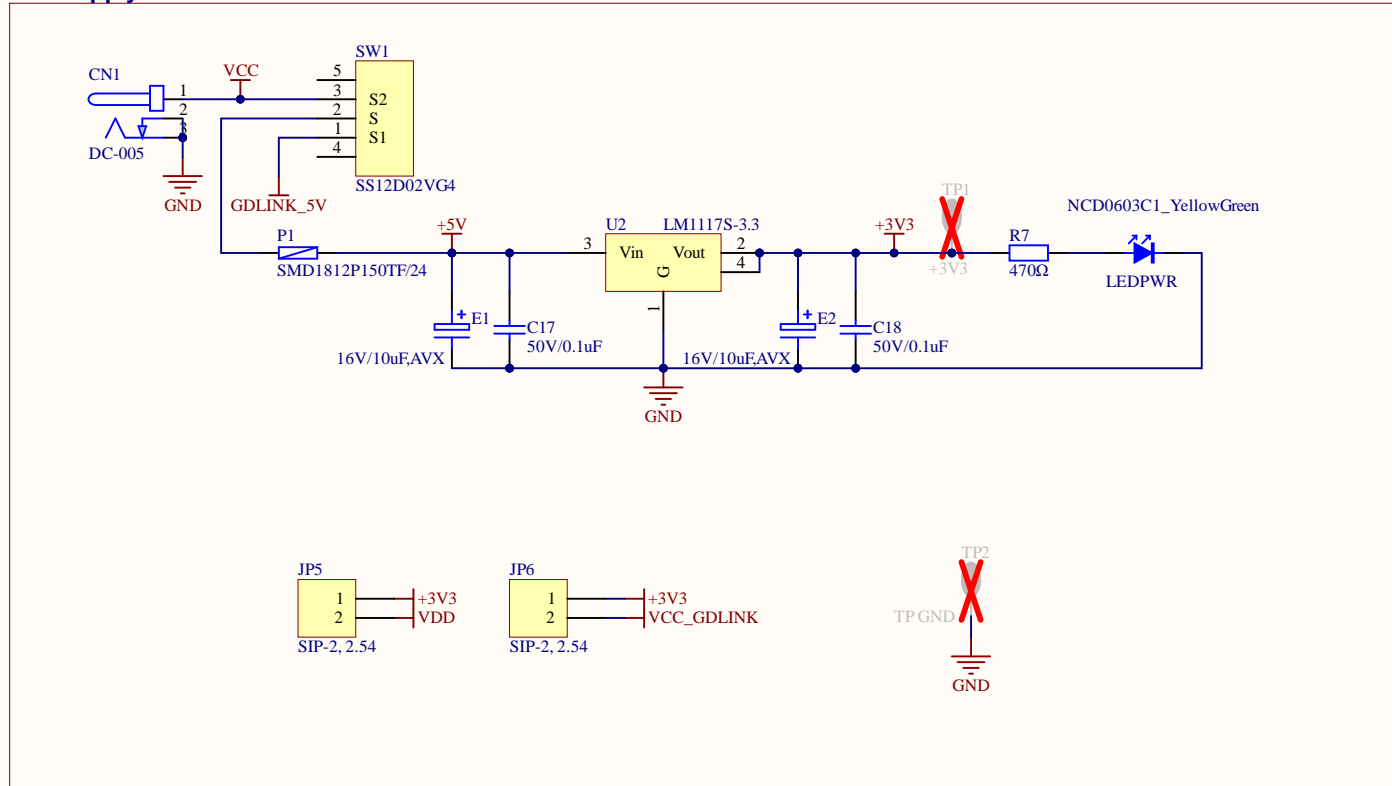
Revision: 1.1


Sheet: 1 of 14

Date: 2025.07

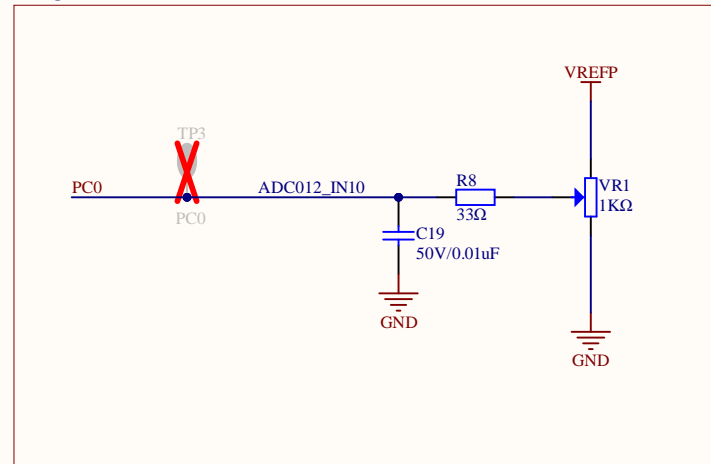


3V3 Supply

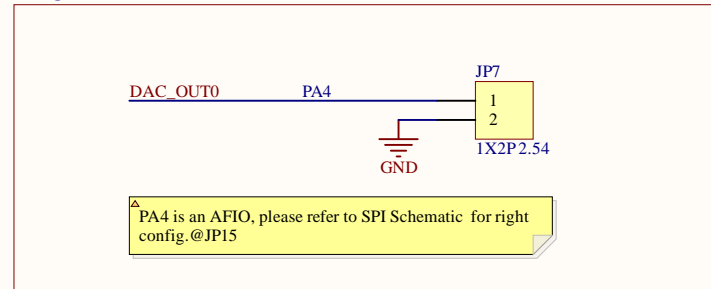


Project Title: GD32F503V-EVAL			
Sheet Title: Power			Size: A4
Designer: bruce.wang	Revision: 1.1		
Sheet: 2 of 14	Date: 2025.07		

ADC



DAC



Project Title: **GD32F503V-EVAL**

Sheet Title: **ADC and DAC**

Size:
A4

Designer: **bruce.wang**

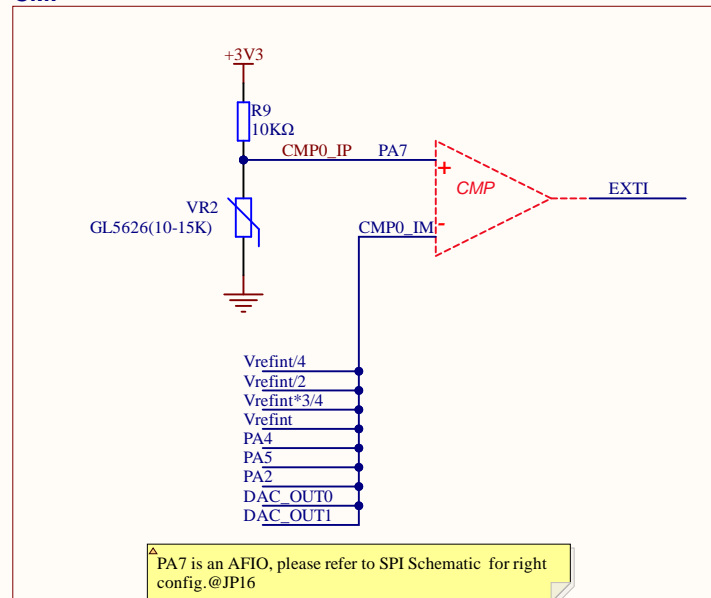
Revision: **1.1**


Sheet: **3 of 14**

Date: **2025.07**

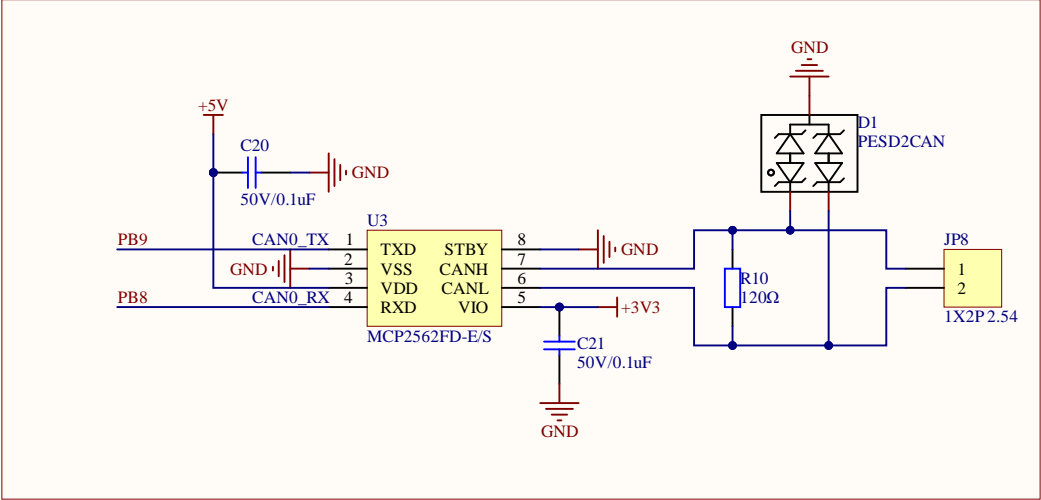



CMP



Project Title: GD32F503V-EVAL			
Sheet Title: COMP			Size: A4
Designer: bruce.wang	Revision: 1.1		
Sheet: 4 of 14	Date: 2025.07		

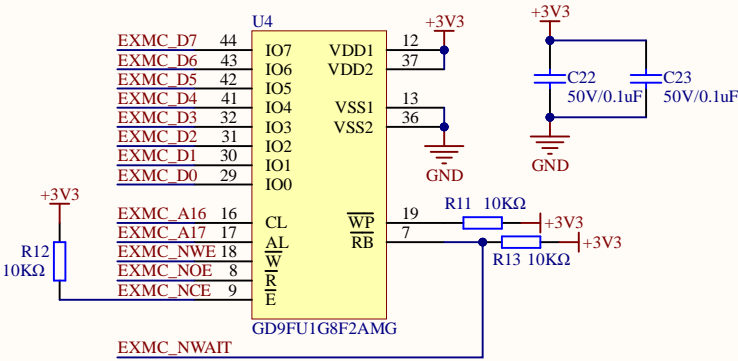
CAN



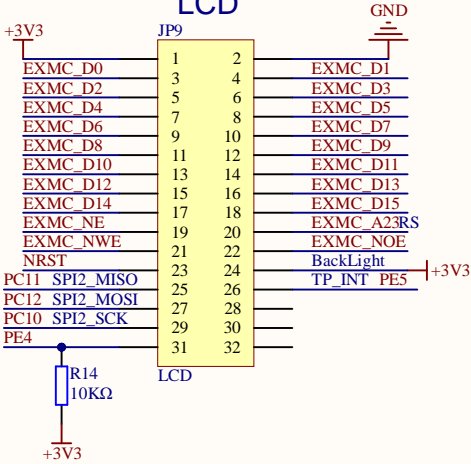
Project Title: GD32F503V-EVAL			
Sheet Title: CAN			Size: A4
Designer: bruce.wang	Revision: 1.1	 GigaDevice	
Sheet: 5 of 14	Date: 2025.07		

EXMC

Nand Flash



LCD



PD14 EXMC_D0
PD15 EXMC_D1
PD0 EXMC_D2
PD1 EXMC_D3
PE7 EXMC_D4
PE8 EXMC_D5
PE9 EXMC_D6
PE10 EXMC_D7
PE11 EXMC_D8
PE12 EXMC_D9
PE13 EXMC_D10
PE14 EXMC_D11
PE15 EXMC_D12
PD8 EXMC_D13
PD9 EXMC_D14
PD10 EXMC_D15

PD11 EXMC_A16
PD12 EXMC_A17
PE2 EXMC_A23

PD4 EXMC_NOE
PD5 EXMC_NWE
PD6 EXMC_NWAIT

PD7 EXMC_NCE
PC4 EXMC_NE

Project Title: **GD32F503V-EVAL**

Sheet Title: **EXMC**

Size:
A4

Designer: **bruce.wang**

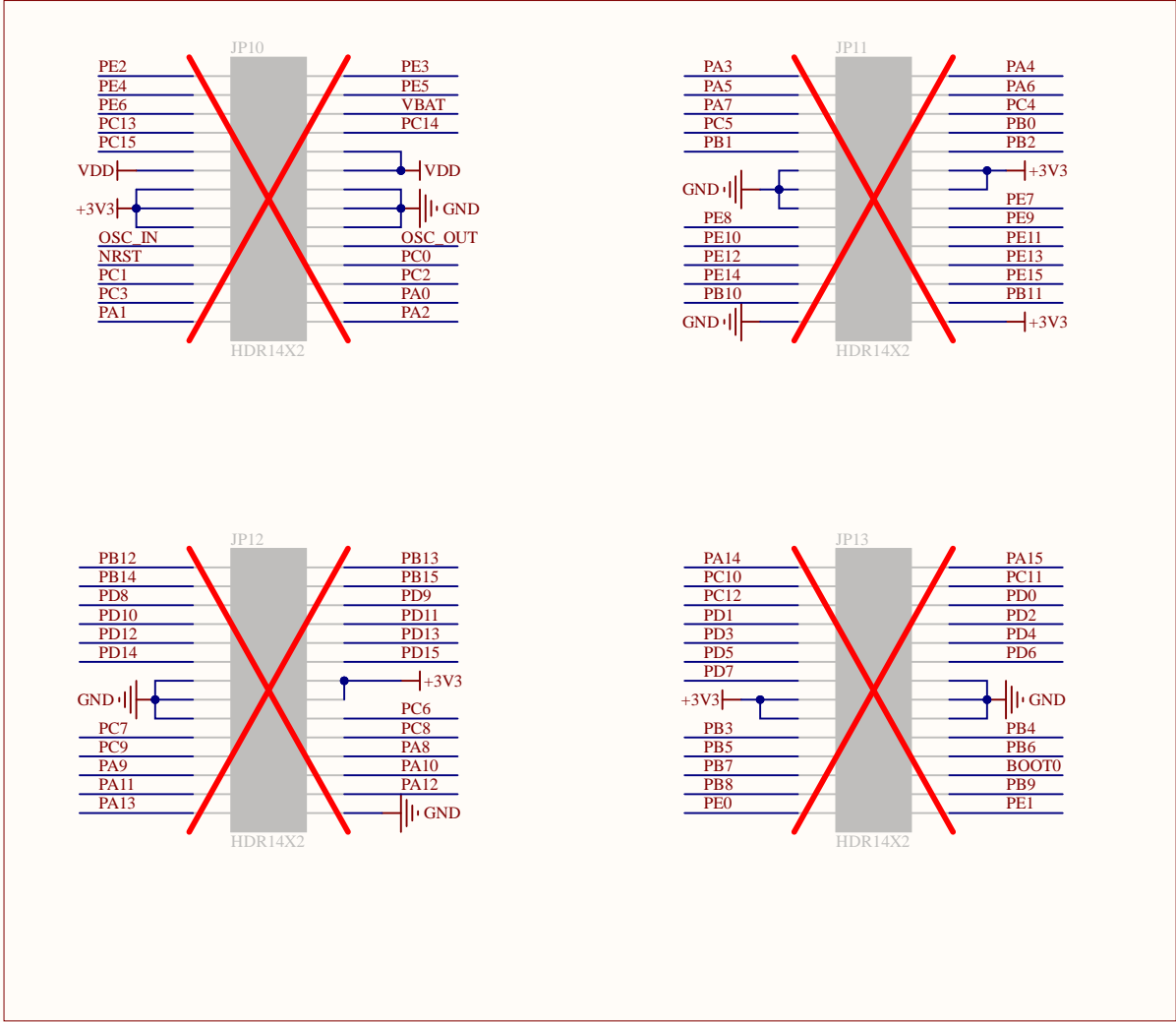
Revision: **1.1**

Sheet: **6 of 14**

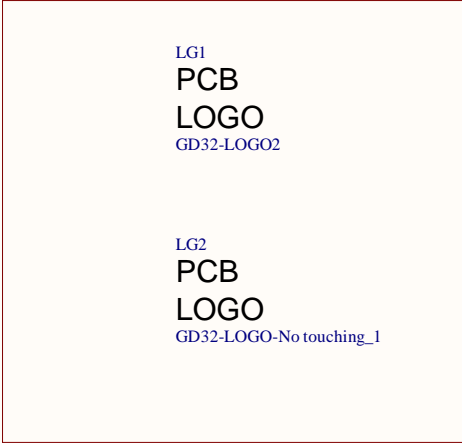
Date: **2025.07**



Extension

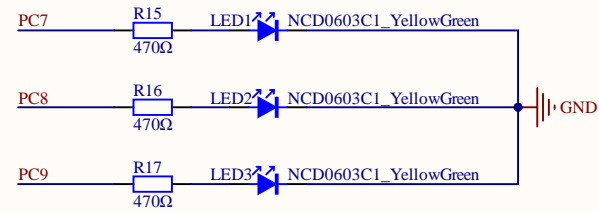


LOGO

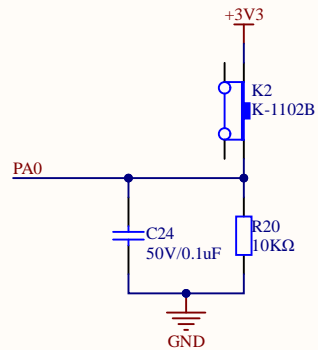


Project Title: GD32F503V-EVAL			
Sheet Title: Extension			Size: A4
Designer: bruce.wang	Revision: 1.1		
Sheet: 7 of 14	Date: 2025.07		

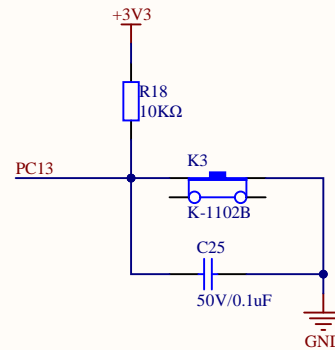
LED



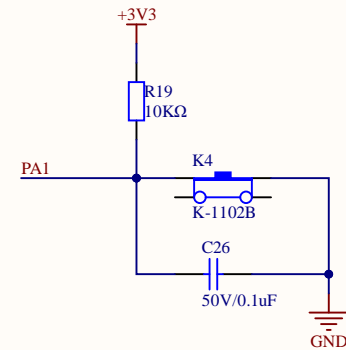
Wakeup



Tamper



User



Project Title: GD32F503V-EVAL

Sheet Title: LED and KEY

Size:
A4

Designer: bruce.wang

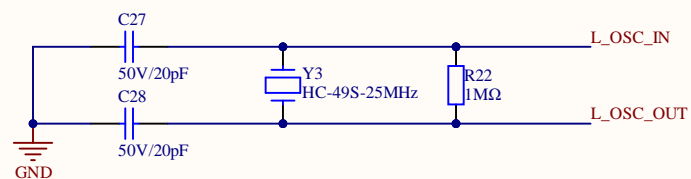
Revision: 1.1

Sheet: 8 of 14

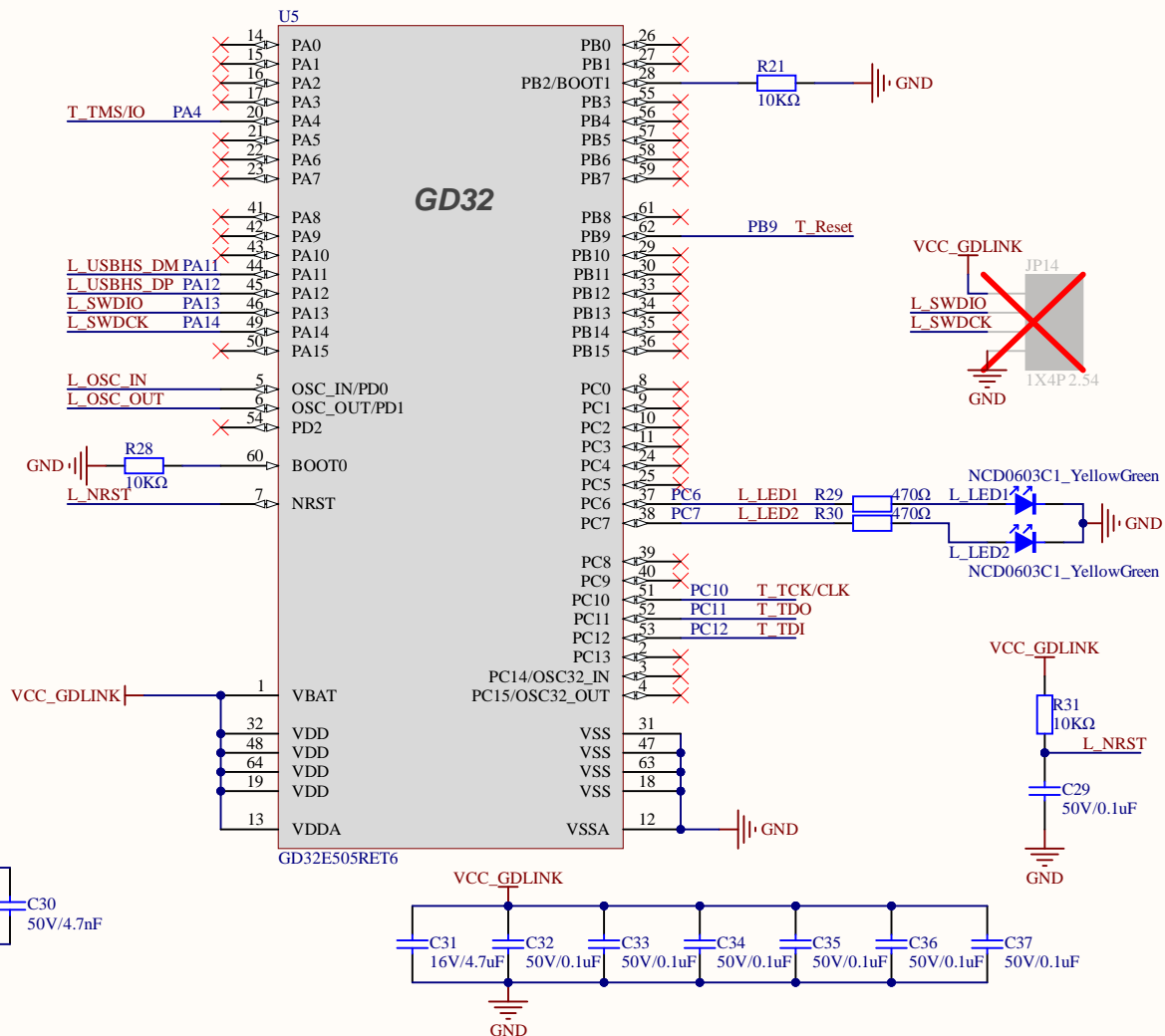
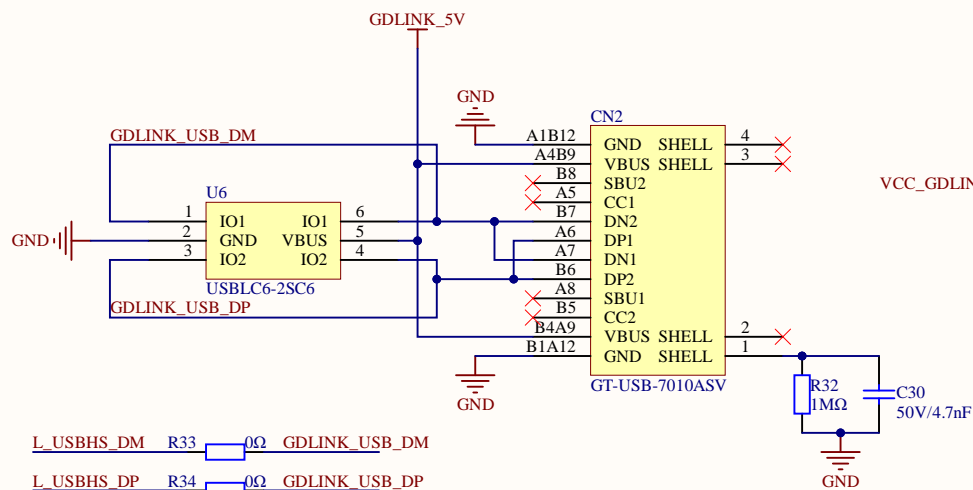
Date: 2025.07



GDLINK



T_TDI	R23	0Ω	PA15
T_TMS/IO	R24	0Ω	PA13
T_TCK/CLK	R25	0Ω	PA14
T_TDO	R26	0Ω	PB3
T_Reset	R27	0Ω	NRST



Project Title: GD32F503V-EVAL

Sheet Title: **GDLINK**

Size:
A4

Designer: bruce.wang

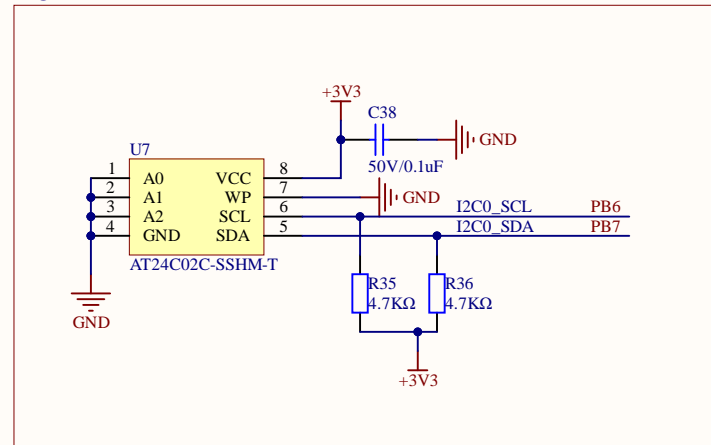
Revision: 1.1

Sheet: 9 of 14

Date: 2025.07



I2C



Project Title: **GD32F503V-EVAL**

Sheet Title: **I2C**

Size:
A4

Designer: **bruce.wang**

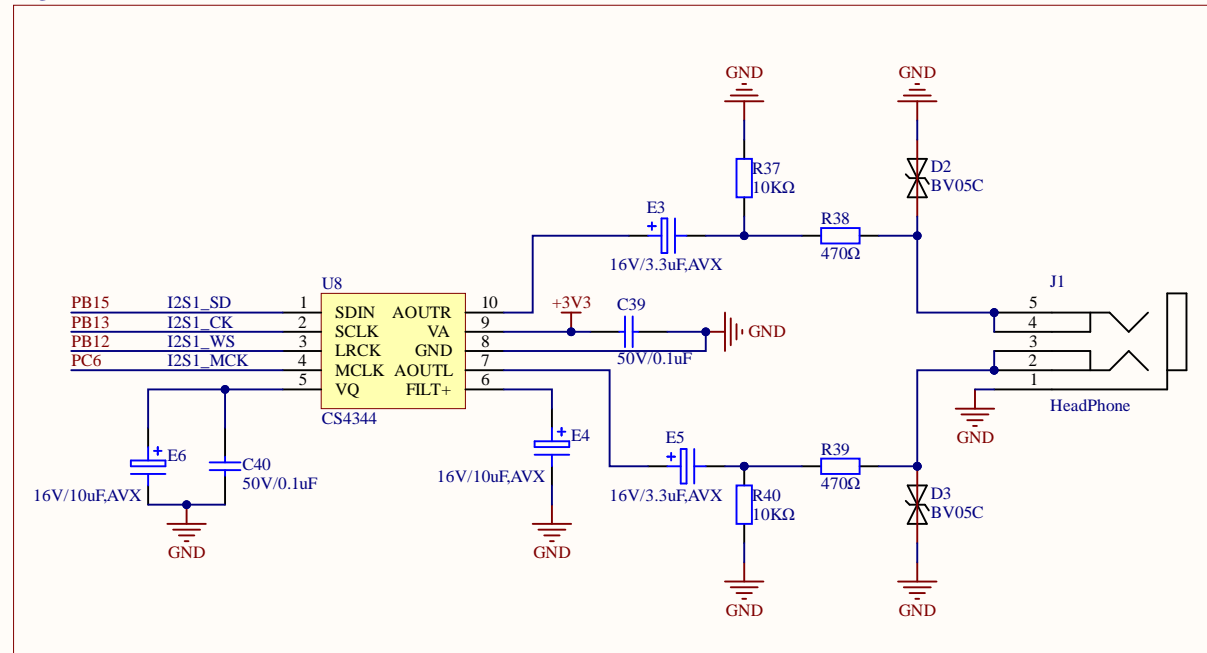
Revision: **1.1**

Sheet: **10 of 14**

Date: **2025.07**



I2S



Project Title: **GD32F503V-EVAL**

Sheet Title: **I2S**

Size:
A4

Designer: **bruce.wang**

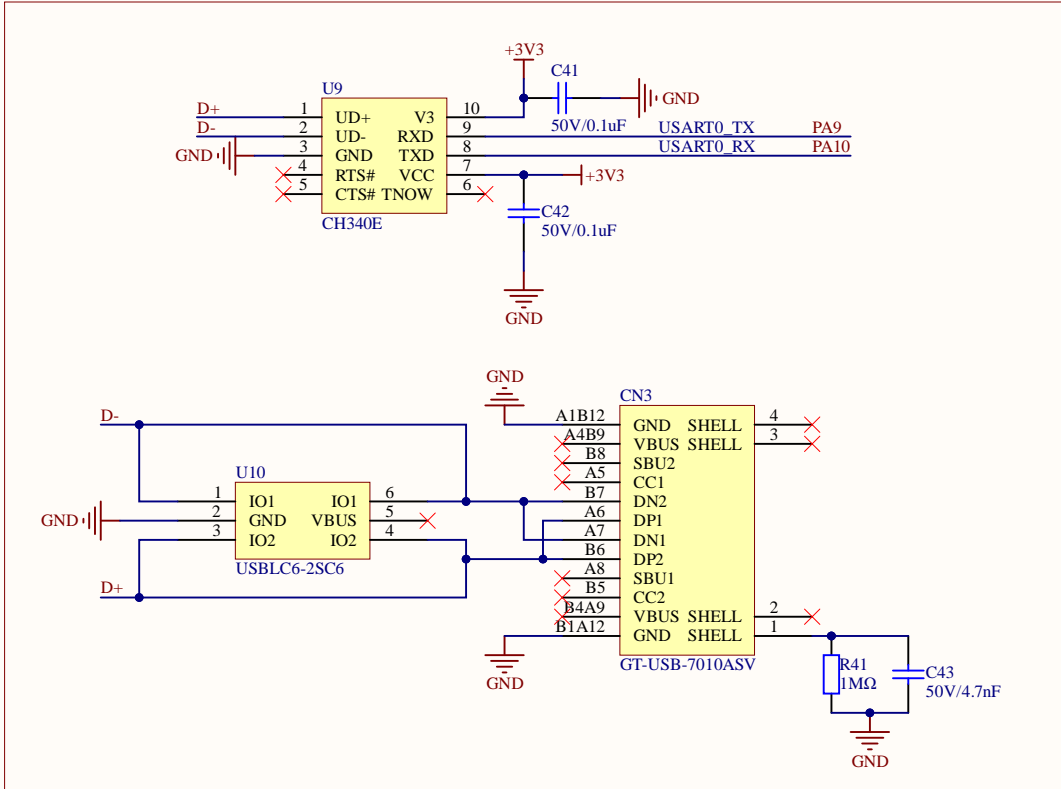
Revision: **1.1**


Sheet: **11 of 14**

Date: **2025.07**

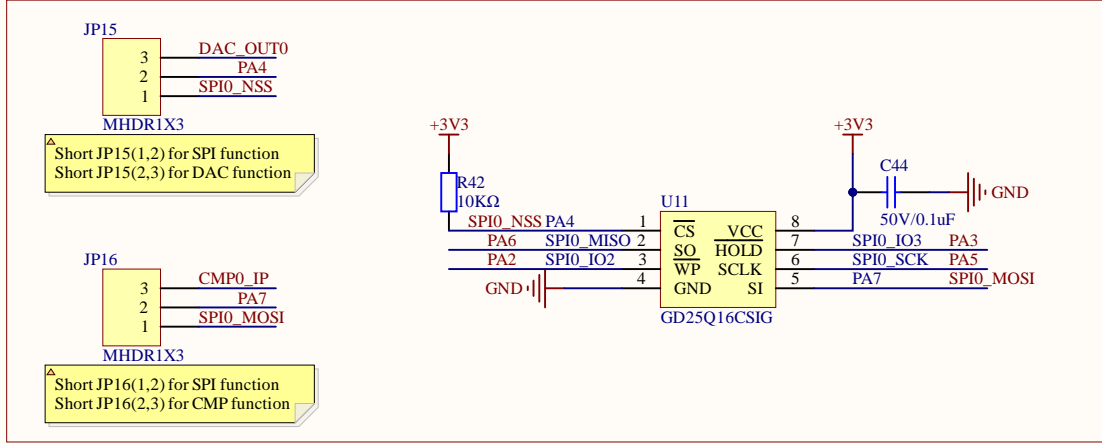


USART



Project Title: GD32F503V-EVAL			
Sheet Title: USART			Size: A4
Designer: bruce.wang	Revision: 1.1	 GigaDevice	
Sheet: 12 of 14	Date: 2025.07		

SPI



Project Title: **GD32F503V-EVAL**

Sheet Title: **SPI**

Size:
A4

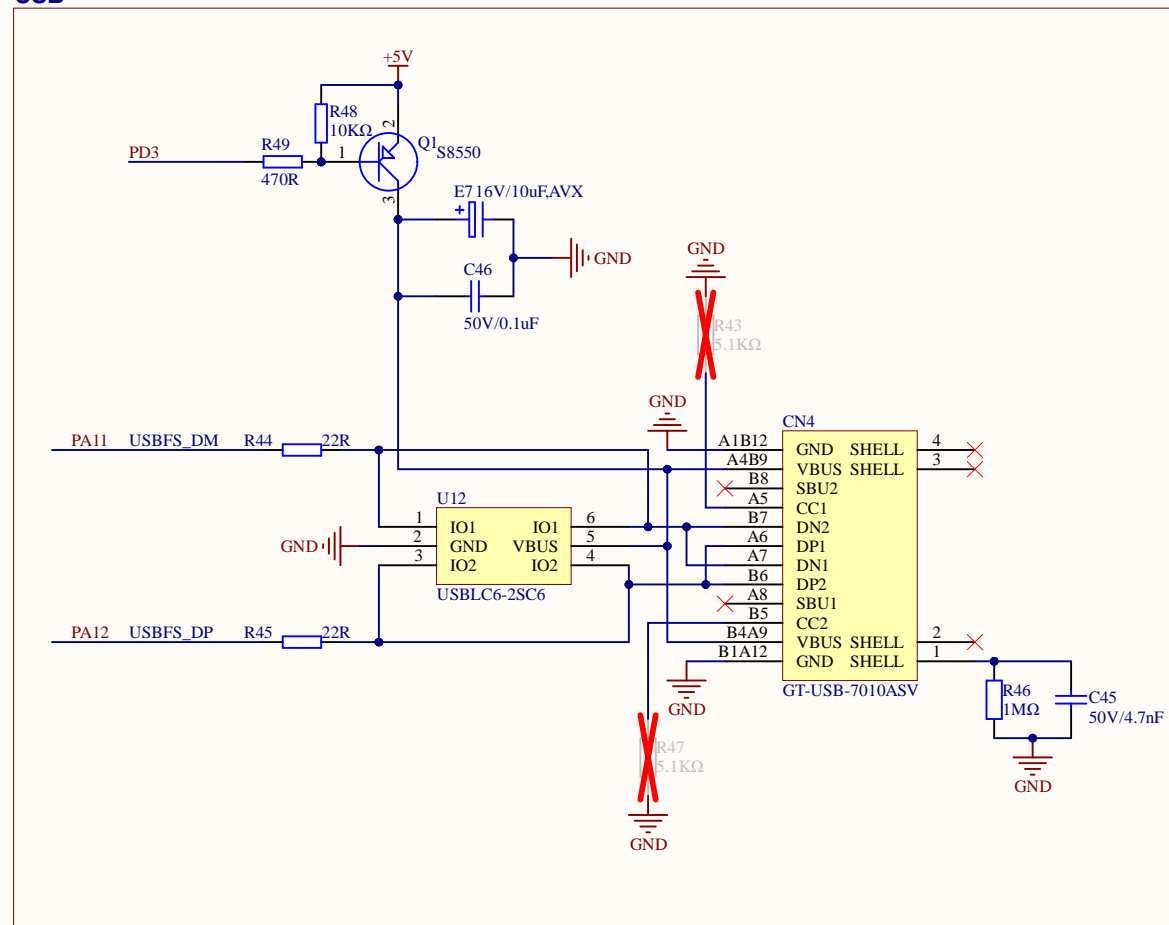
Designer: **bruce.wang**

Revision: **1.1**

Sheet: **13 of 14**

Date: **2025.07**





Size:
A4

