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**GigaDevice Semiconductor Inc.**

**GD32F50x\_Firmware\_Library**

## **Release Note**

Revision 1.0.4

(Feb. 2026)

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## 1. Release content

### 1.1. Version overview

The main updates of GD32F50x\_Firmware\_Library\_V1.0.4 are as follows:

1. EB project FPU configuration related updates
2. Startup file bug fixes
3. DAC, FWDGT, CAN and TIMER module driver modification

### 1.2. Code & Documentation

- GD32F50x Standard Peripheral Firmware
- GD32F50x Peripheral Examples
- GD32F50x Utilities
- GD32F50x Template
- GD32F50x\_Firmware\_Library\_User\_Guide
- GD32F503V-EVAL-V1.1

### 1.3. Reference Document

- GD32F50x\_User\_Manual
- GD32F50x\_Datasheet

## 2. Version information

Table 2-1. Version Information

<b>Name</b>	GD32F50x_Firmware_Library.7z
<b>Version</b>	V1.0.4
<b>Release date</b>	2026.02.25
<b>Applicable chip models</b>	GD32F502xx GD32F503xx GD32F505xx
<b>Supported evaluation boards</b>	GD32F503V-EVAL-V1.1
<b>Compilation environment</b>	■ IAR Embedded Workbench 8.32.1

- ARM Keil 5.29
- GD32EmbeddedBuilder\_v1.5.5\_Rel

### 3. Release version function description

For more details, please refer to "GD32F50x\_Firmware\_Library\_User\_Guide".

### 4. Release version change list

For more details, please refer to "change log.txt".

### 5. Release version quality report

**Table 5-1. Release version quality report**

Item	Report item	Description
1	MISRA report acquisition	Yes, the MISRA-C:2012 report can be obtained.
2	Number of MISRA deviations (Required rules)	0
3	Number of MISRA deviation explanations (Required rules)	0
4	Number of warnings in project compilation results (Keil5)	None
5	Number of warnings in project compilation results (IAR)	None
7	Number of warnings in project compilation results (GD32EmbeddedBuilder)	None

### 6. Release version notes

**Table 6-1. Release version notes**

Item	Module	Problem description	Solution/Workaround
1	-	<ol style="list-style-type: none"> <li>1. Boot mode selection and configuration</li> <li>2. SRAM ECC configuration description</li> <li>3. FLASH configuration description</li> <li>4. RCU clock configuration limitation description</li> <li>5. TIMER usage instructions</li> <li>6. CMP usage instructions</li> <li>7. PMU usage instructions</li> </ol>	See "AN270 GD32F50x Software Development Guide" for details.
2	-	<ol style="list-style-type: none"> <li>1. Hardware design: power supply,</li> </ol>	See "AN278 GD32F50x Hardware Development Guide"

Item	Module	Problem description	Solution/Workaround
		clock, startup configuration, typical peripheral modules, download debugging circuit, reference schematic design 2. PCB Layout design: voltage decoupling capacitors, clock circuit, reset circuit, USB circuit 3. Package description	for details.

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