

GigaDevice Semiconductor Inc.

GDSCN832xx 芯片使用限制

勘误手册

1.0 版本

（2025 年 8 月）

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1. 前言

该文档适用于 GDSCN832xx 产品，具体参考[表 1-1. 适用产品](#)。该文档提供了在使用 GDSCN832xx 过程中需要注意的技术细节，以及相关问题的解决方案。

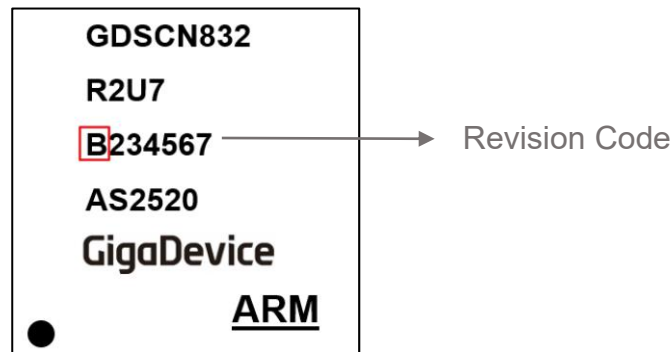
表 1-1. 适用产品

类型	产品系列
ASIC	GDSCN832xx 系列

1.1. 芯片版本定义

可通过芯片丝印上的标记来确定当前芯片的版本。丝印上第 3 行的第 1 个编码表示芯片当前版本，如[图 1-1. GDSCN832xx 的芯片修订版本](#)所示。

图 1-1. GDSCN832xx 的芯片修订版本



1.2. 芯片使用限制总览

GDSCN832xx 芯片使用限制参考[表 1-2. 芯片使用限制](#)。关于芯片使用限制的更多详细信息请参考第 2 章。

表 1-2. 芯片使用限制

模块	使用限制	解决方案
		修订版本 B
GPIO	默认配置下，外部 MCU (I2C 主机) 无法通过 ESC 芯片的 EESDA / EESCL 引脚访问 EEPROM	Y

注意：

Y = 存在使用限制且存在解决方案

N = 存在使用限制但不存在解决方案

'-' = 使用限制已修复

2. 芯片使用限制描述

2.1. GPIO

2.1.1. 默认配置下，外部 MCU（I2C 主机）无法通过 ESC 芯片的 EESDA / EESCL 引脚访问 EEPROM

描述与影响

当使用外部 MCU 访问 EEPROM 时，由于 ESC 芯片的 EESDA/EESCL 引脚默认输出为推挽模式，会导致外部 MCU 无法读出 EEPROM 数据。以上情况下，ESC 芯片依然可正常访问 EEPROM，不影响 ESC 功能使用。

解决方案

外部 MCU 通过 PDI 接口配置 ESC 芯片的 EESDA / EESCL 引脚为输出开漏模式后，外部芯片才可以访问 EEPROM。

3. 版本历史

表 3-1. 版本历史

版本号.	说明	日期
1.0	首次发布	2025 年 8 月 8 日

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