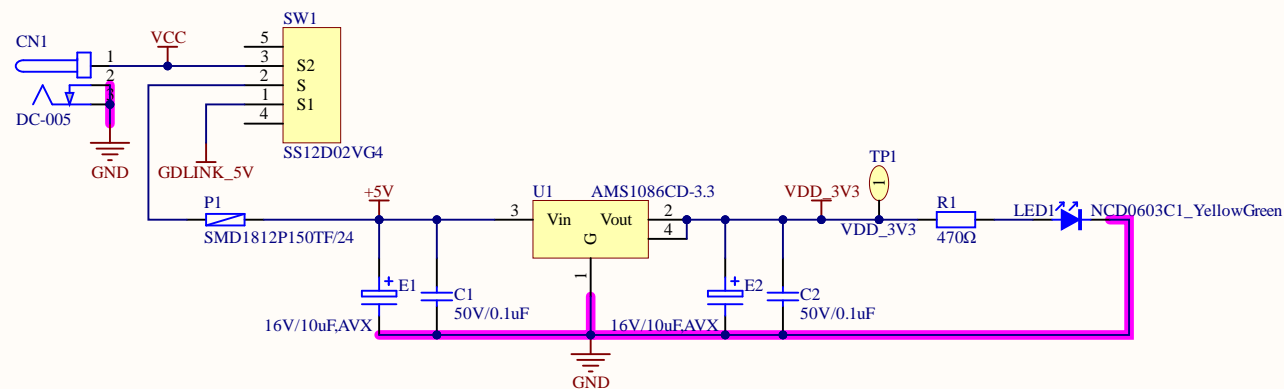
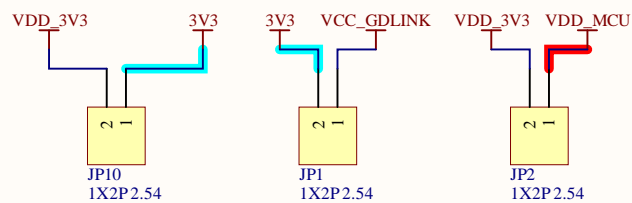


3V3 Supply

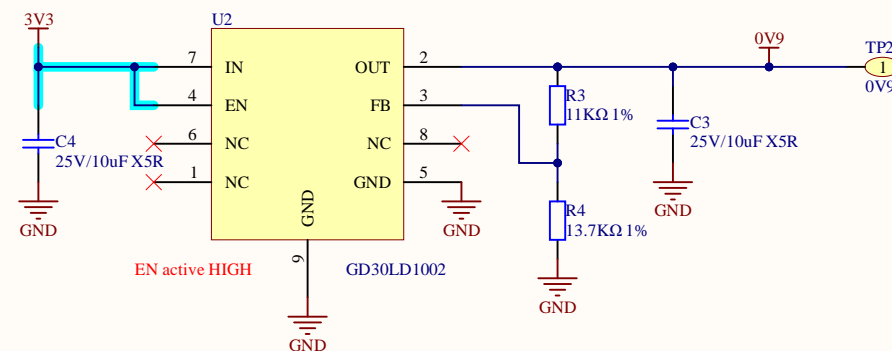


Power Consumption



Notice: If you want to test the MCU's power consumption, connect the multimeter in series to JP2.

0V9 Supply



Project Title: **GD32H75EY-EVAL**

Sheet Title: **Power**

Size:
A4

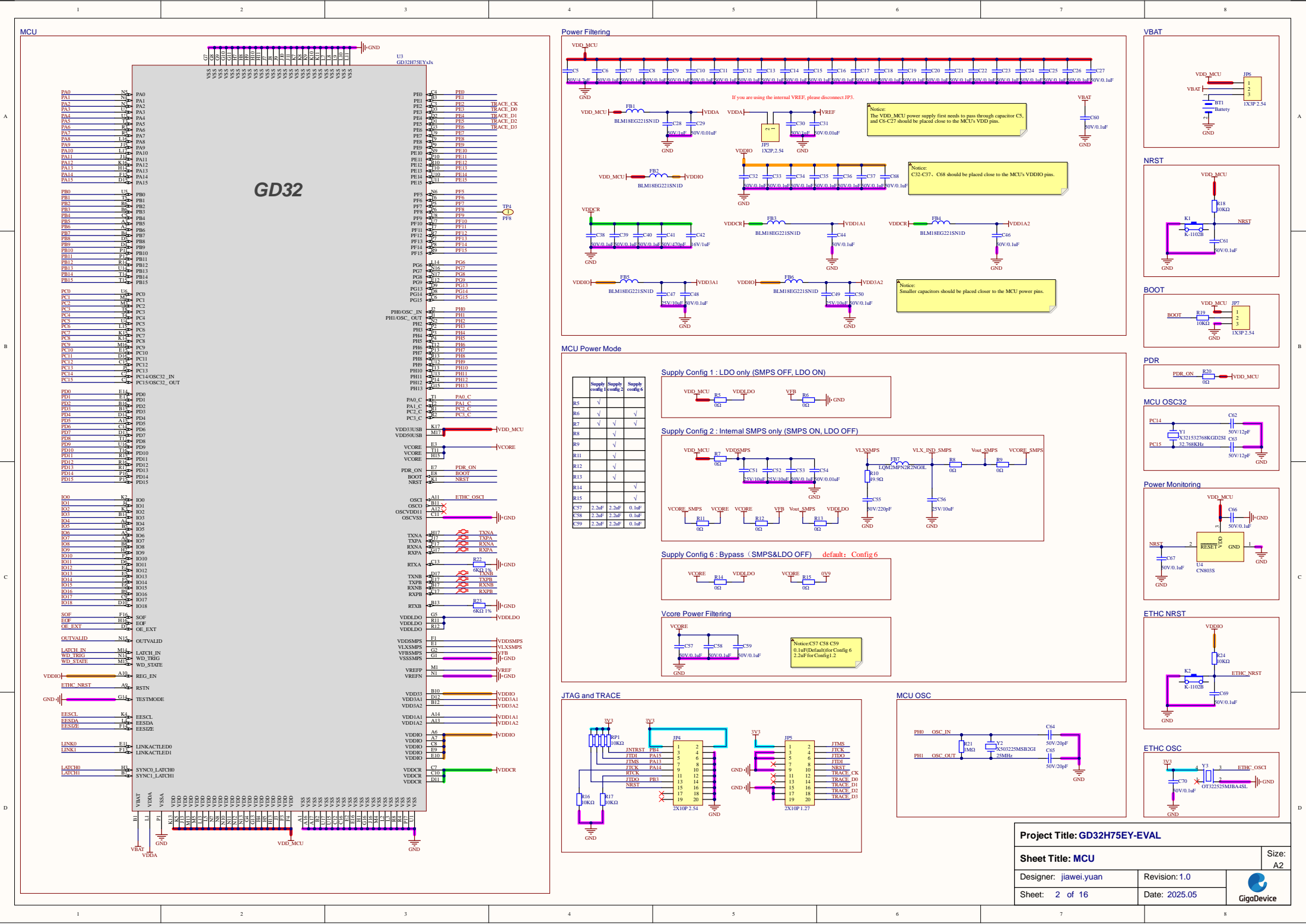
Designer: **jiawei.yuan**

Revision: **1.0**

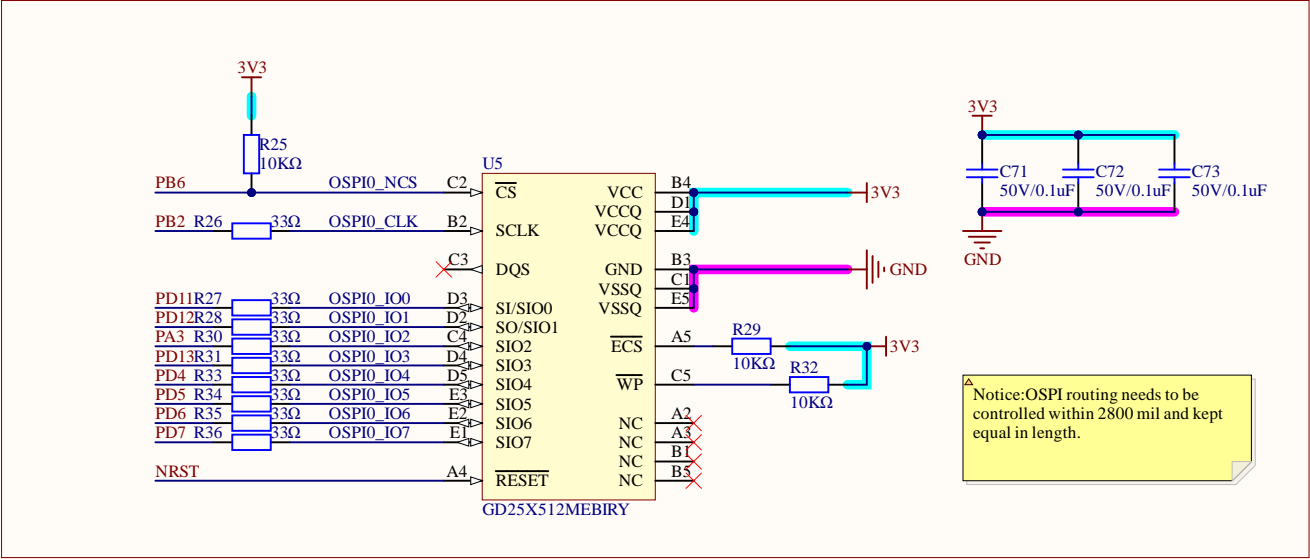
Sheet: **1 of 16**


Date: **2025.05**



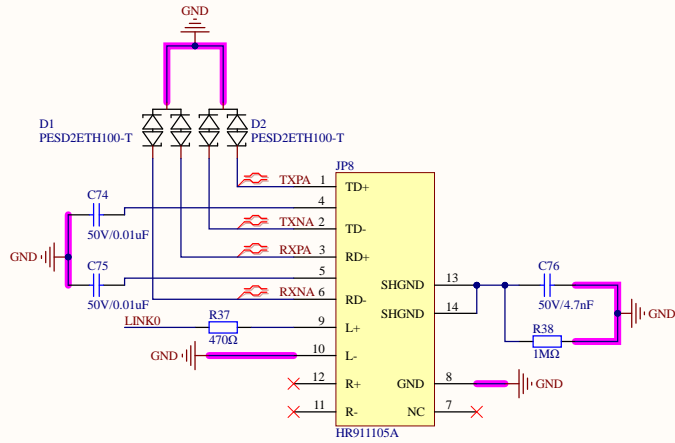


OSPI

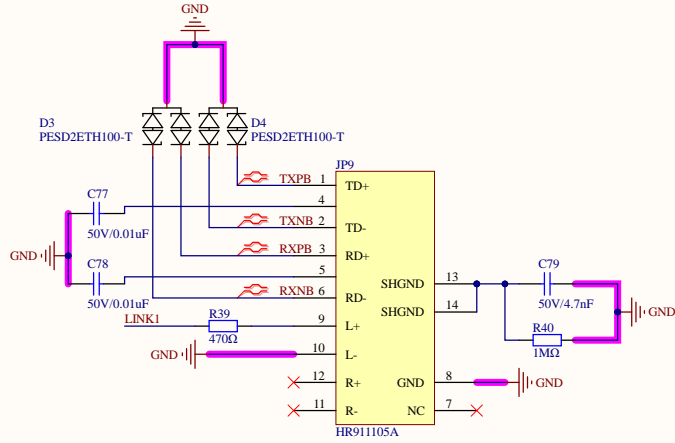


Project Title: GD32H75EY-EVAL			
Sheet Title: OSPI			Size: A4
Designer: jiawei.yuan		Revision: 1.0	
Sheet: 3 of 16		Date: 2025.05	

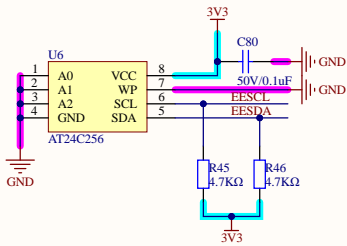
PHY A



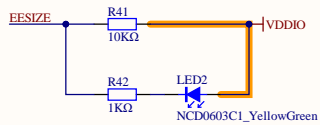
PHY B



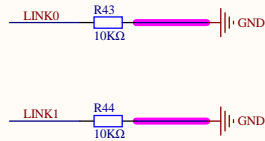
ETHC Eeprom



ETHC Eesize

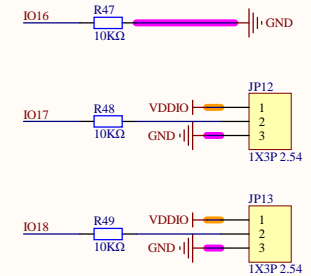


ETHC Linkctld



Notice: LINK0 and LINK1 are set to low by default, at which point ETHC operates in two-port mode. Port 0 and Port 1 are connected to internal PHY A and PHY B.

ETHC Strap



Signal Name	Logic	Connector	Strap
IO17	1	JP12(1&2)	LDO Bypass
	0	JP12(2&3)	LDO no Bypass default
IO18	1	JP13(1&2)	Open Reset delay
	0	JP13(2&3)	No Reset delay default

Project Title: **GD32H75EY-EVAL**

Sheet Title: **ETHC**

Designer: **jiawei.yuan**

Sheet: **4 of 16**

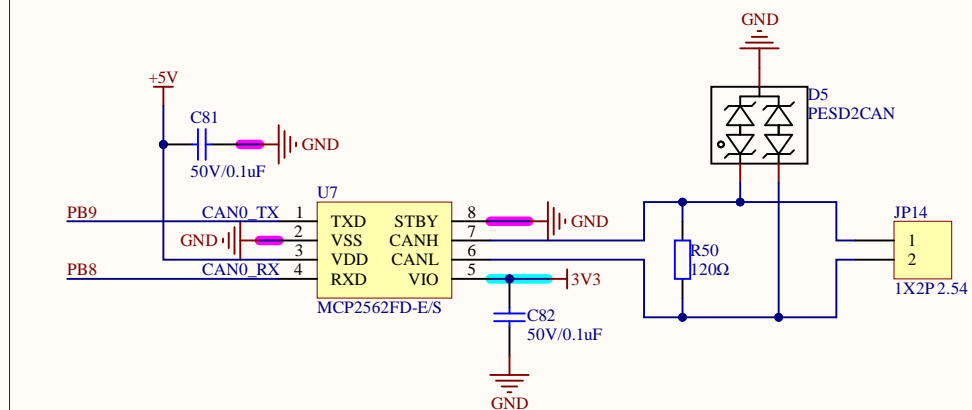
Revision: **1.0**

Date: **2025.05**

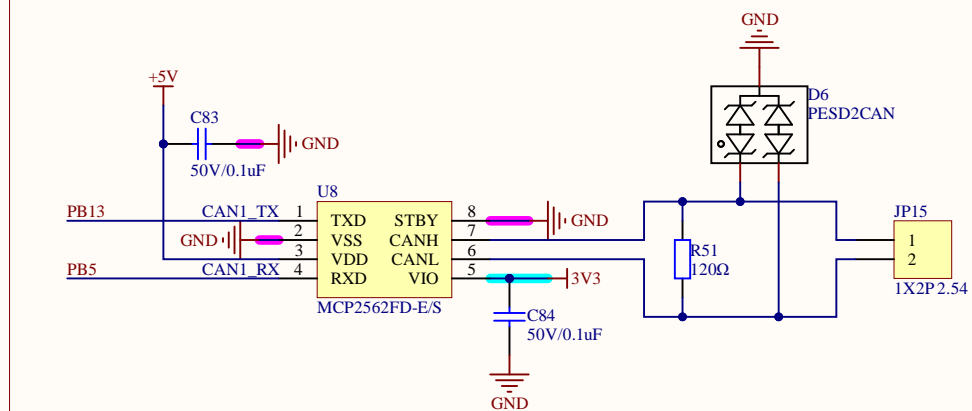
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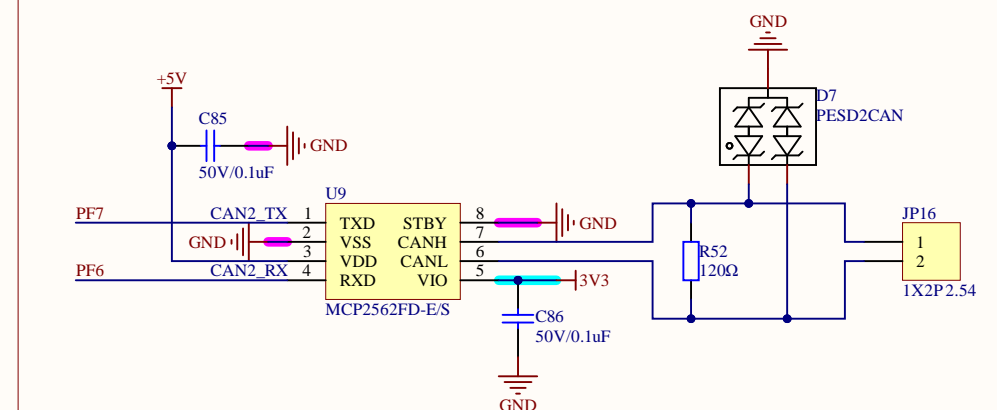
CAN0



CAN1



CAN2



Project Title: **GD32H75EY-EVAL**

Sheet Title: **CAN**

Size:
A4

Designer: **jiawei.yuan**

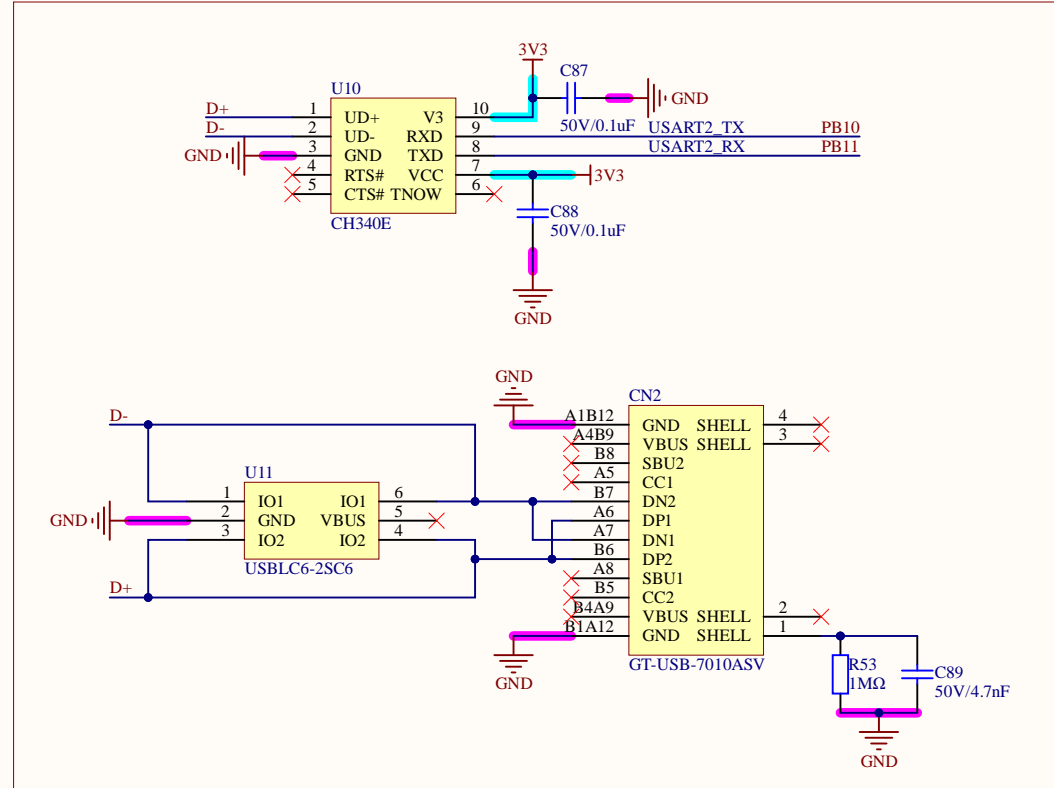
Revision: **1.0**

Sheet: **5 of 16**

Date: **2025.05**



USART



Project Title: **GD32H75EY-EVAL**

Sheet Title: **USART**

Size:
A4

Designer: **jiawei.yuan**

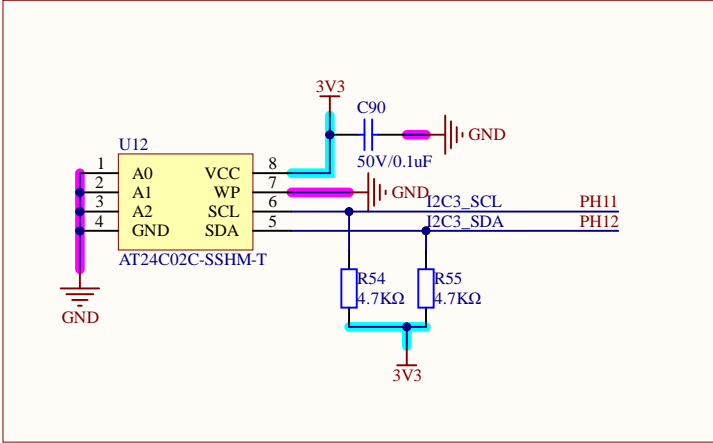
Revision: **1.0**


Sheet: **6 of 16**

Date: **2025.05**

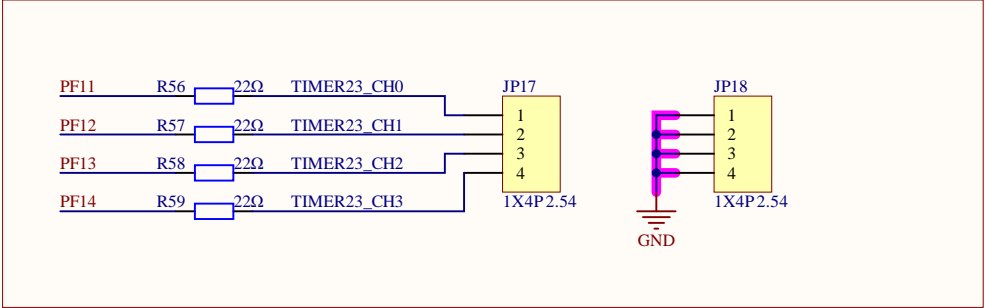


I2C

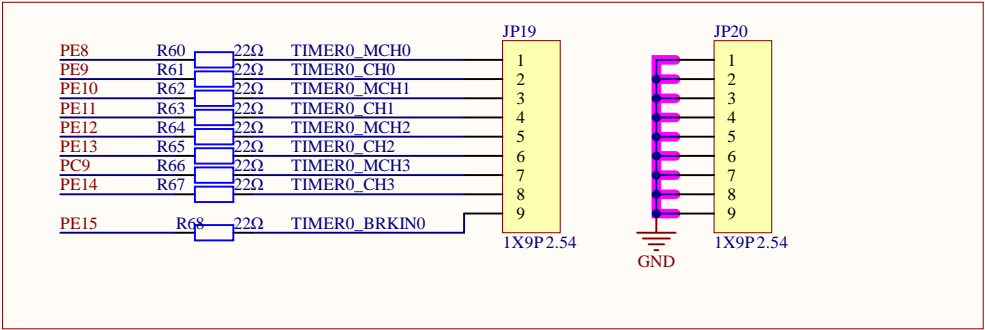



Project Title: GD32H75EY-EVAL			
Sheet Title: I2C			Size: A4
Designer: jiawei.yuan		Revision: 1.0	 GigaDevice
Sheet: 7 of 16		Date: 2025.04	

General TIMER

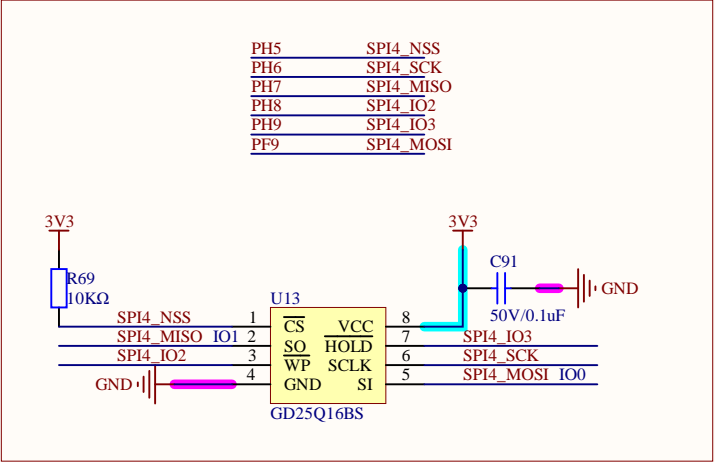



Advanced TIMER



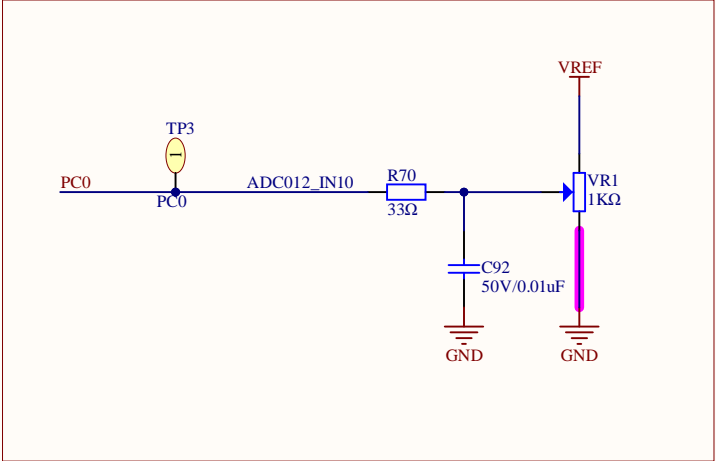
Project Title: GD32H75EY-EVAL			
Sheet Title: TIMER			Size: A4
Designer: jiawei.yuan		Revision: 1.0	 GigaDevice
Sheet: 8 of 16		Date: 2025.04	

SPI

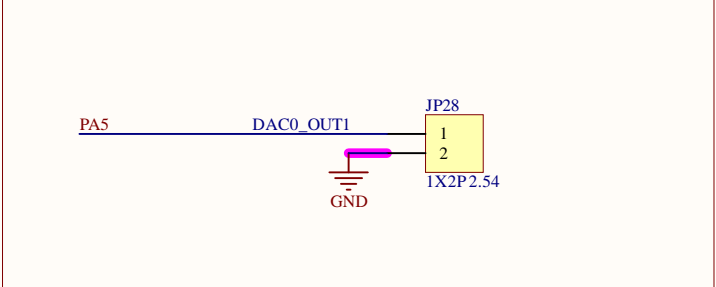



Project Title: GD32H75EY-EVAL			
Sheet Title: SPI			Size: A4
Designer: jiawei.yuan	Revision: 1.0	 GigaDevice	
Sheet: 9 of 16	Date: 2025.05		

ADC

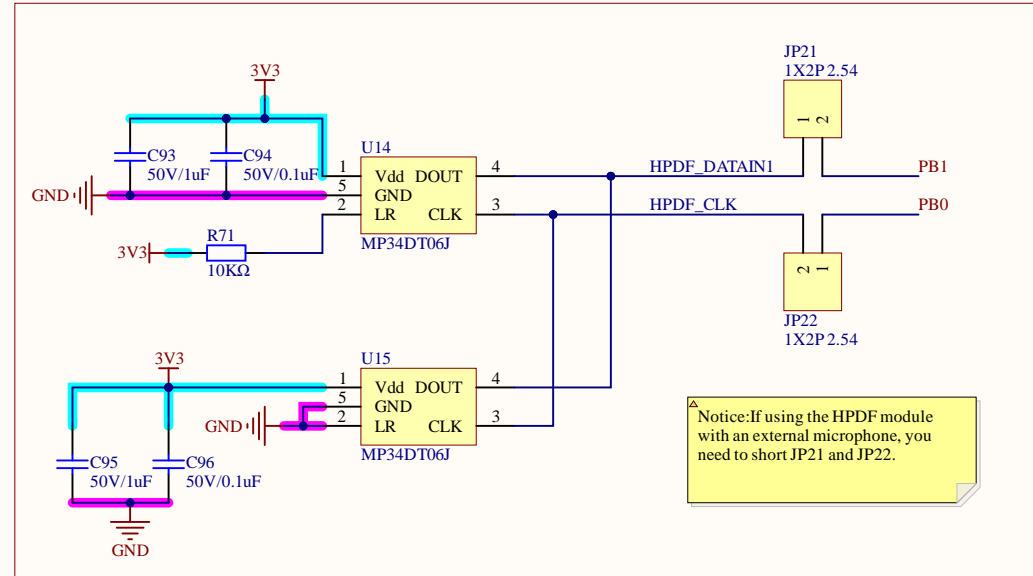


DAC



Project Title: GD32H75EY-EVAL			
Sheet Title: ADC and DAC			Size: A4
Designer: jiawei.yuan		Revision: 1.0	
Sheet: 10 of 16		Date: 2025.05	

HPDF and PDM



Project Title: GD32H75EY-EVAL

Sheet Title: HPDF and PDM

Size:
A4

Designer: jiawei.yuan

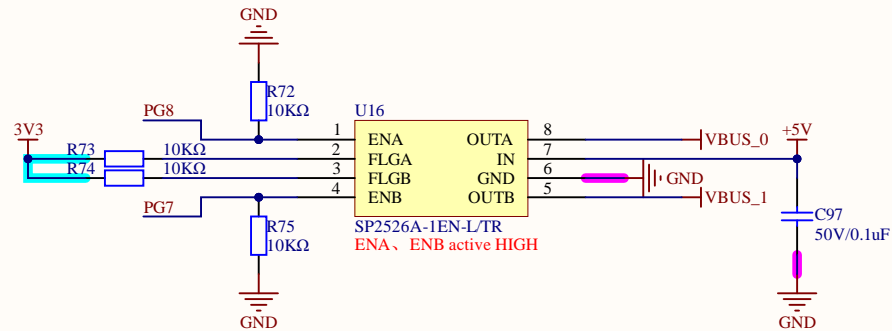
Revision: 1.0

Sheet: 11 of 16

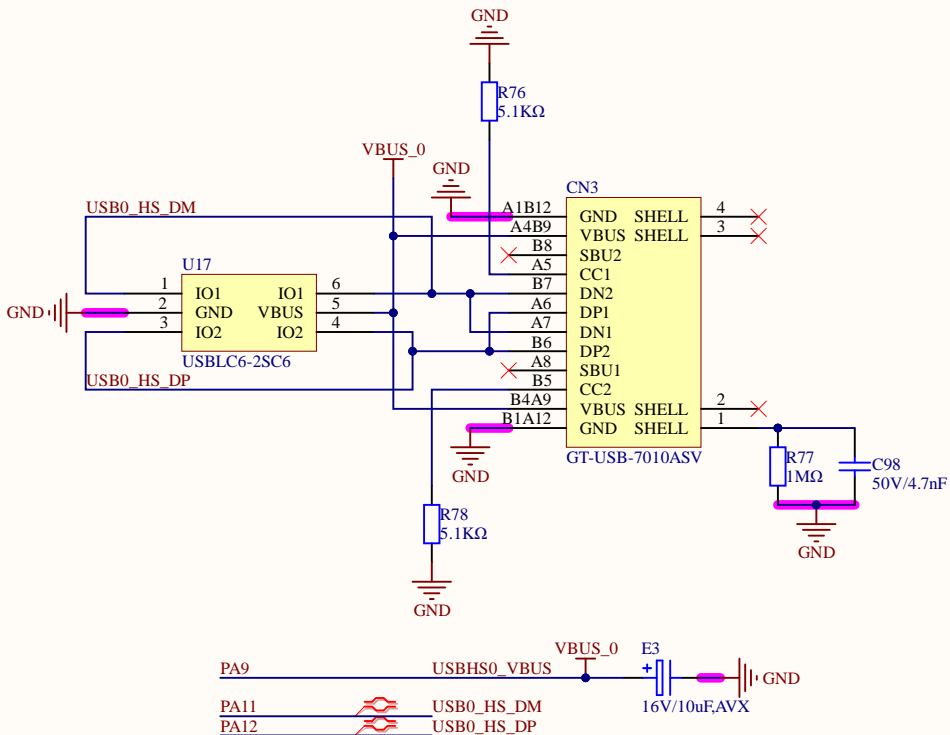
Date: 2025.05



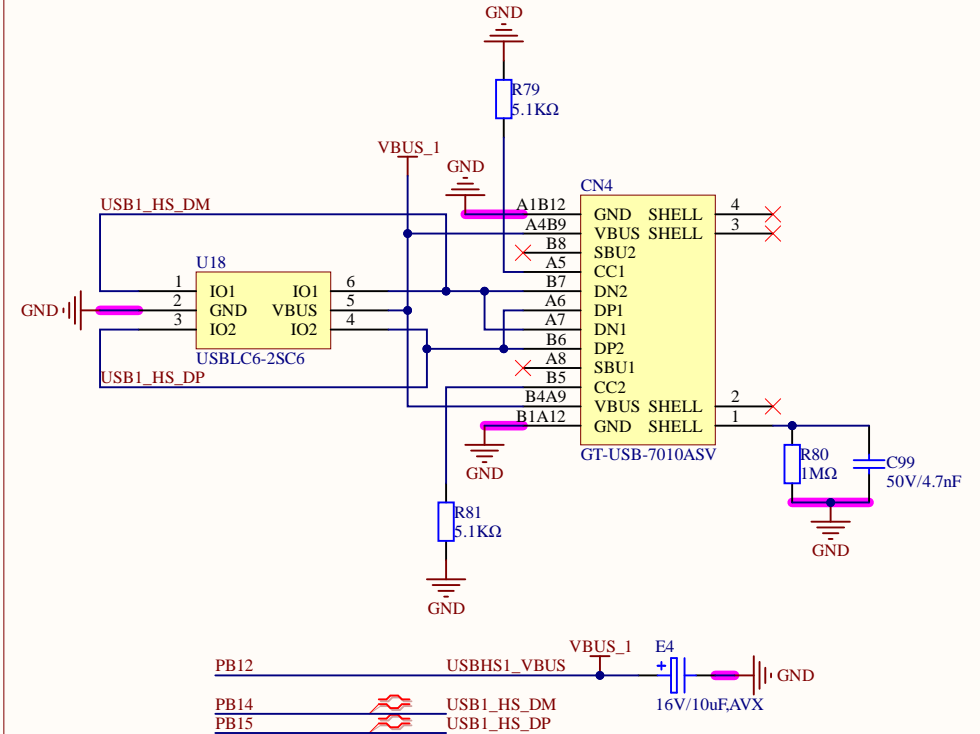
USB Power Distribution Switch



USB0



USB1



Project Title: **GD32H75EY-EVAL**

Sheet Title: **USB**

Size:
A4

Designer: **jiawei.yuan**

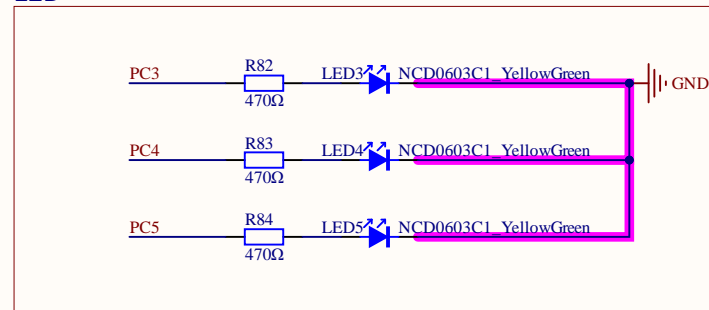
Revision: **1.0**

Sheet: **12 of 16**

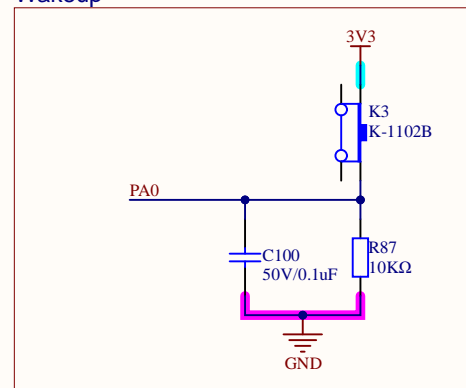
Date: **2025.05**



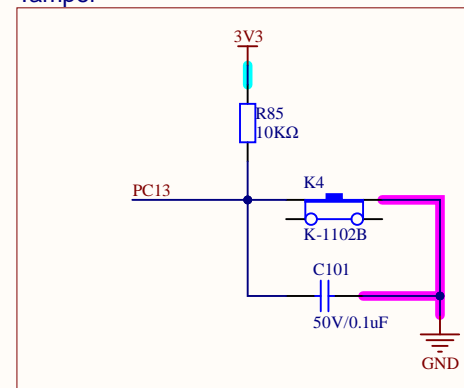
LED



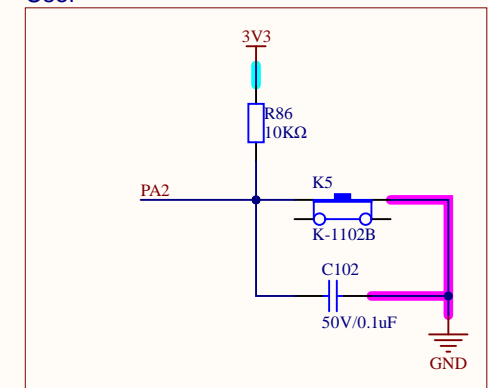
Wakeup



Tamper



User



Project Title: **GD32H75EY-EVAL**

Sheet Title: **LED and KEY**

Size:
A4

Designer: **jiawei.yuan**

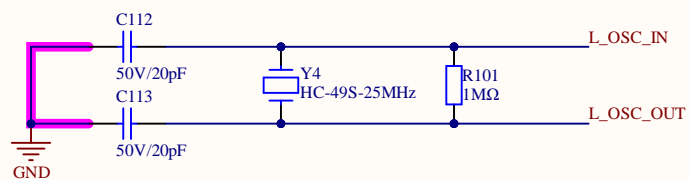
Revision: **1.0**

Sheet: **13 of 16**

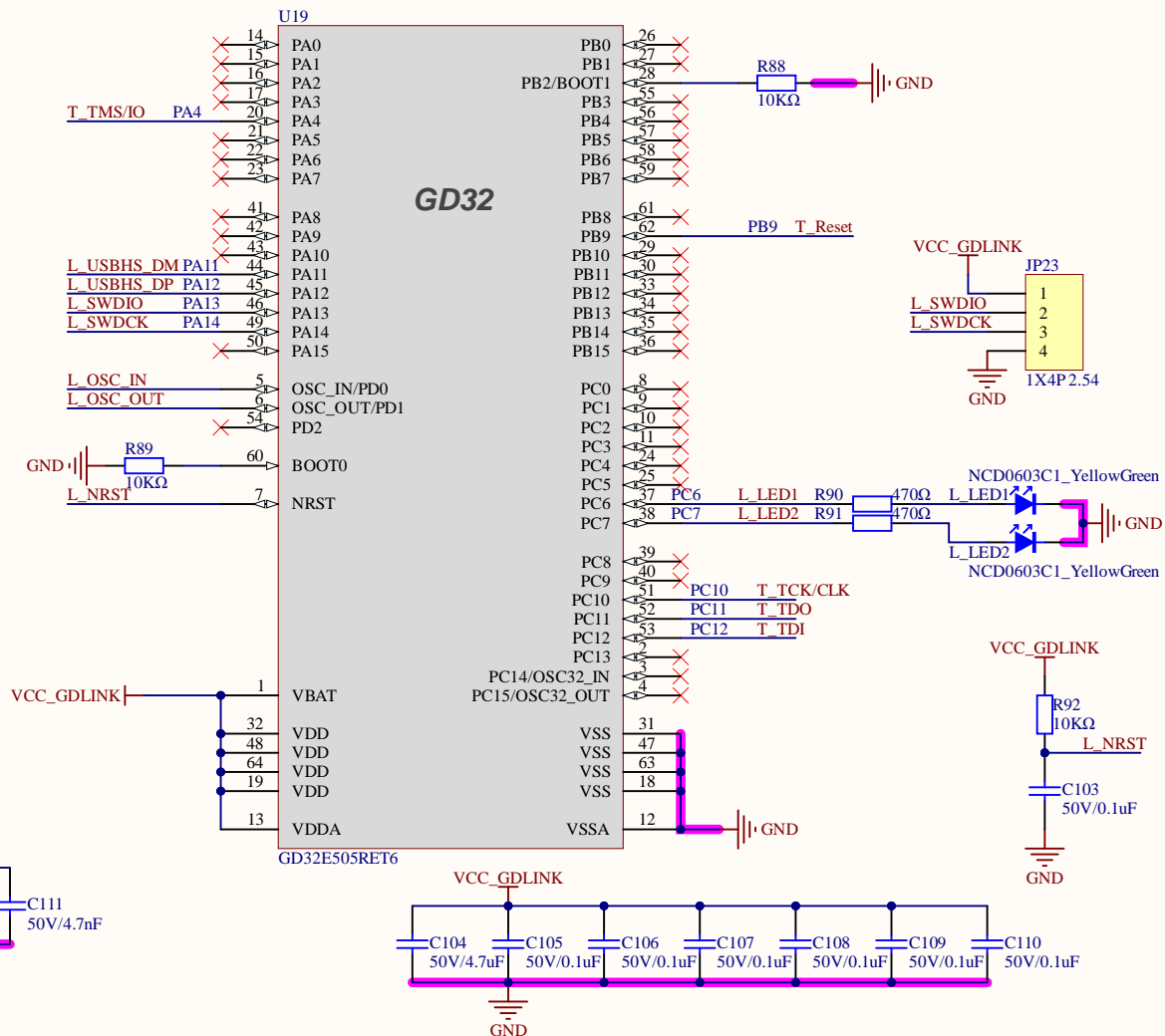
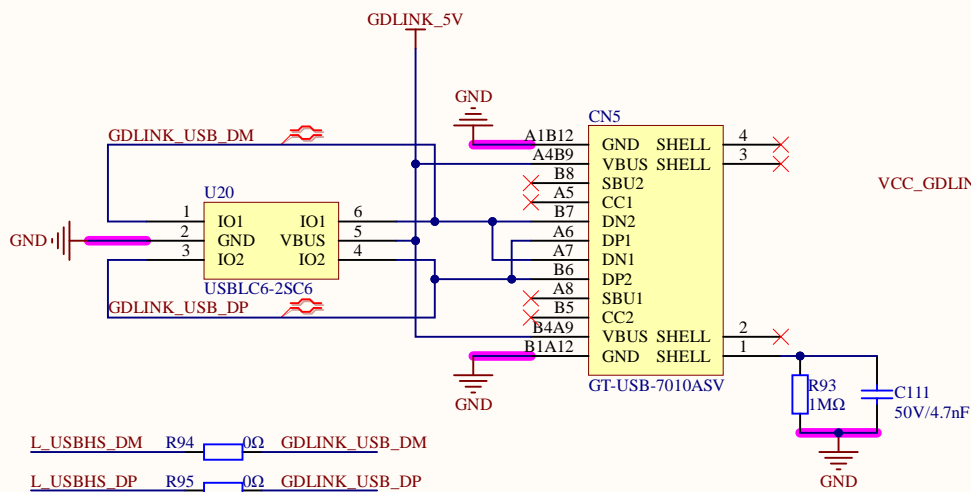
Date: **2025.05**



GDLINK



T_TDI	R96	0Ω	PA15
T_TMS/IO	R97	0Ω	PA13
T_TCK/CLK	R98	0Ω	PA14
T_TDO	R99	0Ω	PB3
T_Reset	R100	0Ω	NRST



Project Title: GD32H75EY-EVAL

Sheet Title: **GDLINK**

Size:
A4

Designer: [jiawei.yuan](#)

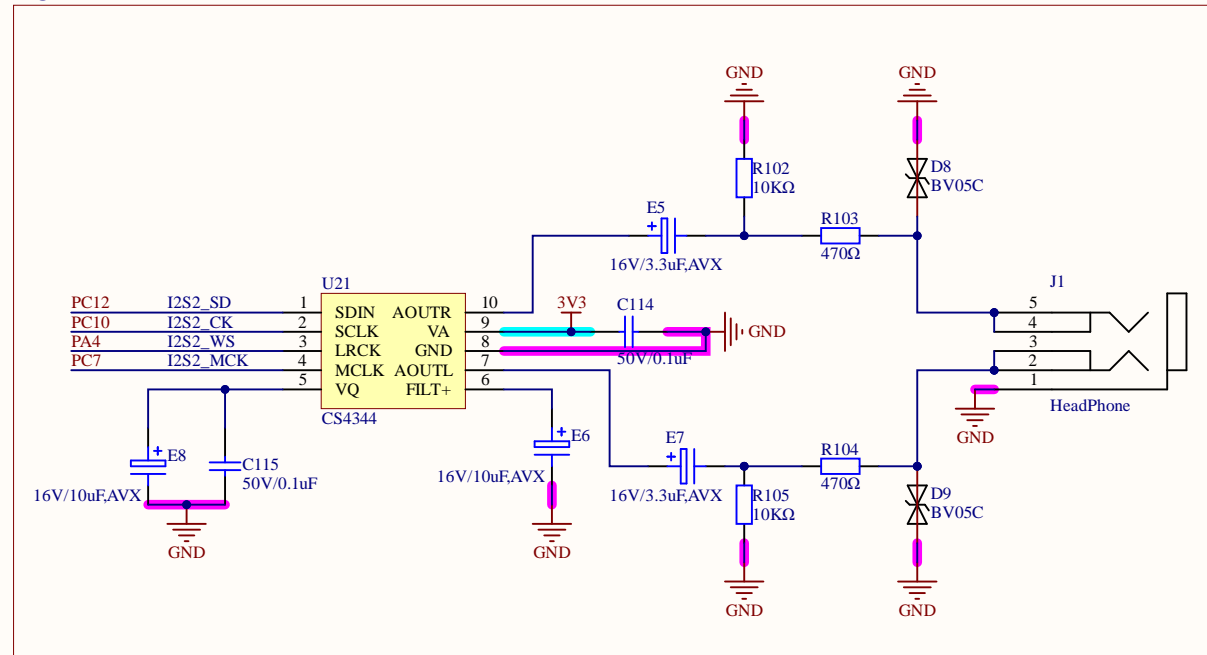
Revision: 1.0

Sheet: 14 of 16

Date: 2025.05



I2S



Project Title: **GD32H75EY-EVAL**

Sheet Title: **I2S**

Size:
A4

Designer: **jiawei.yuan**

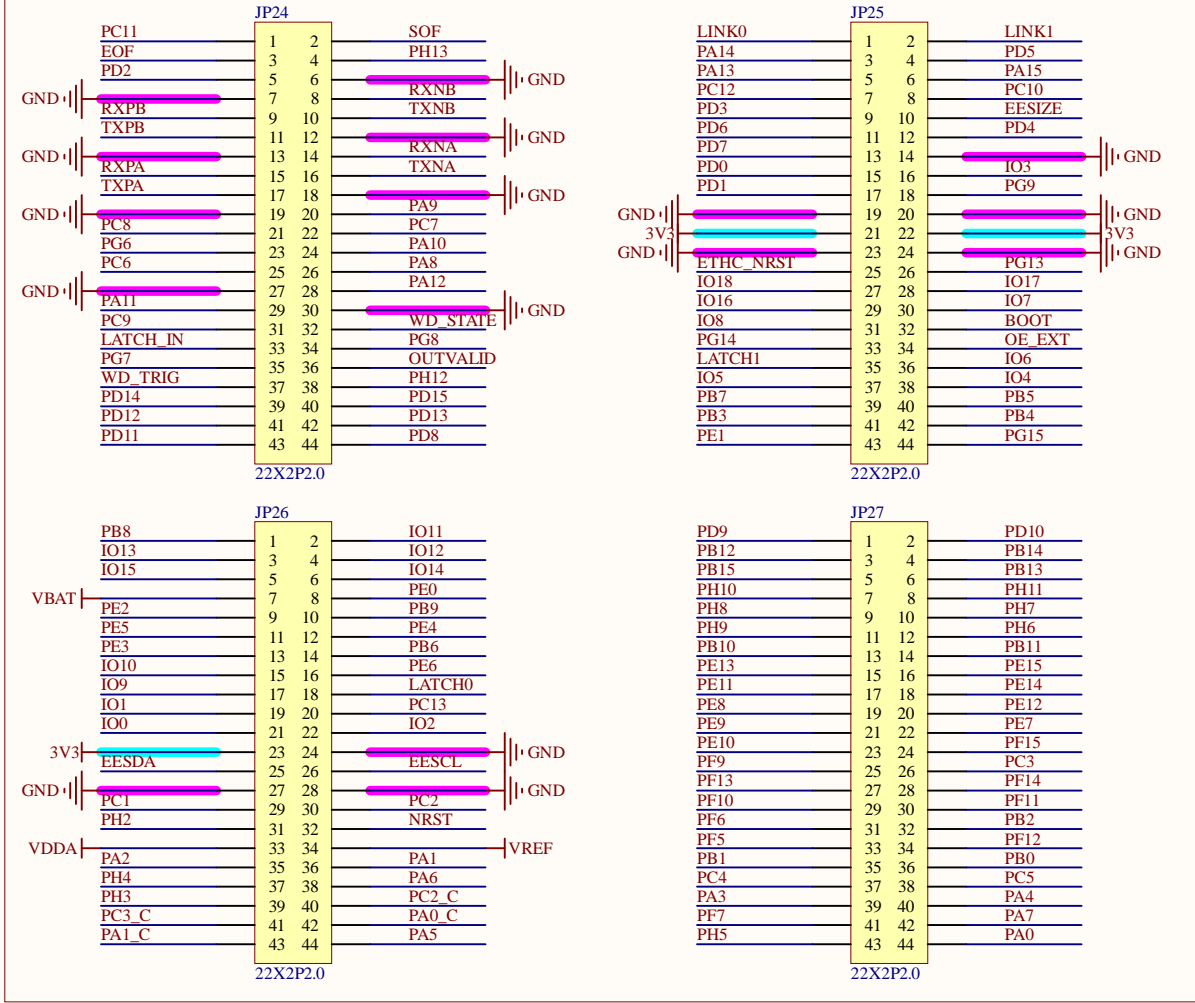
Revision: **1.0**

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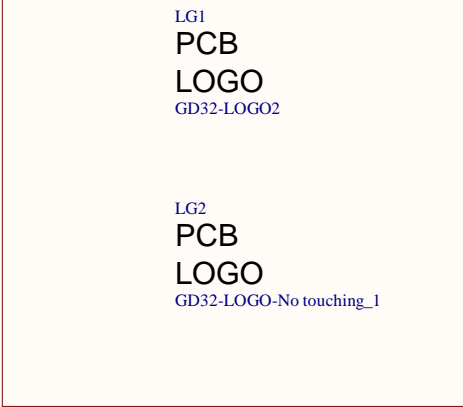
Date: **2025.05**



Extension



LOGO



Project Title: GD32H75EY-EVAL			
Sheet Title: Extension			Size: A4
Designer: jiawei.yuan	Revision: 1.0		
Sheet: 16 of 16	Date: 2025.05		

