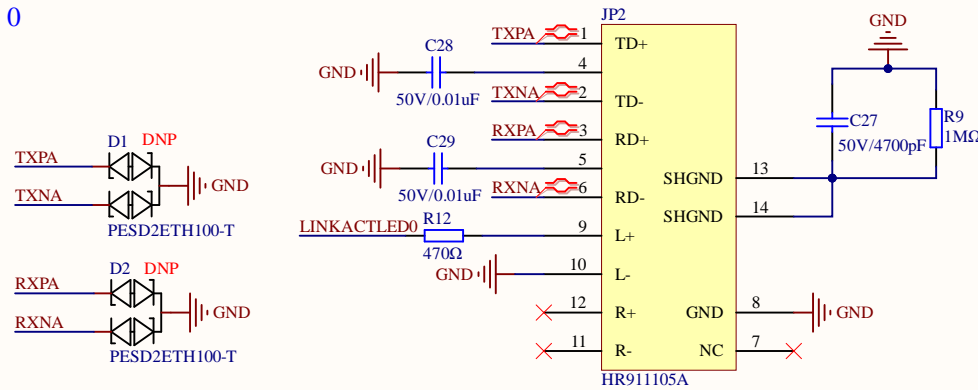
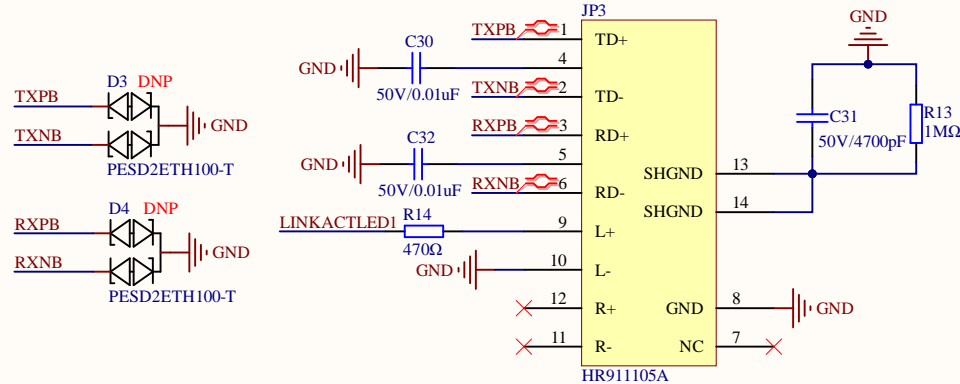


Ethernet

Port 0



Port 1



Network Precautions

Please note that the internal PHY of GDETHC is a voltage-type PHY.

Layout Precautions

The TXP/TXN and RXP/RXN traces should always be as short as possible. The individual trace impedance of TXP/TXN and TXP/RXN must be kept below 50 ohm, and the differential characteristic impedance of the pair must be 100 ohm.

Company Name: GigaDevice

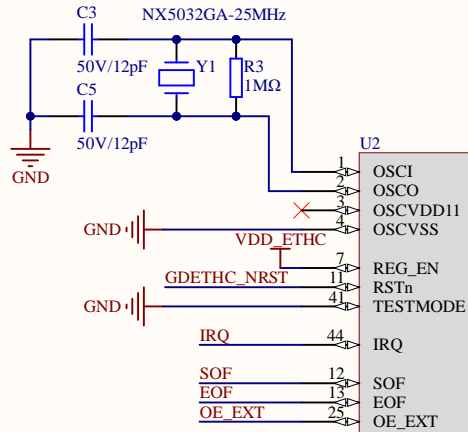
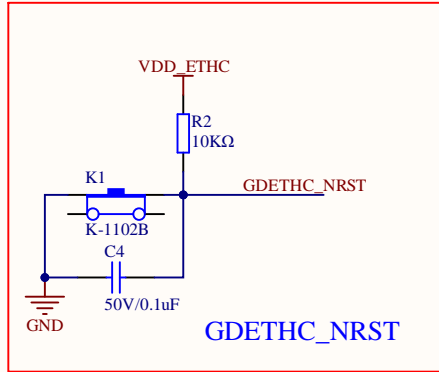
File Name: Ethernet

Revision: 1.1

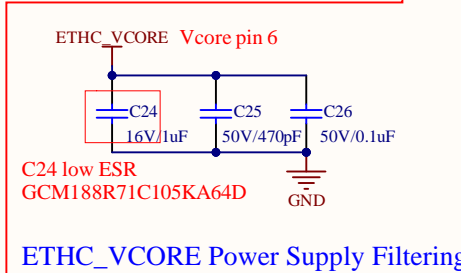
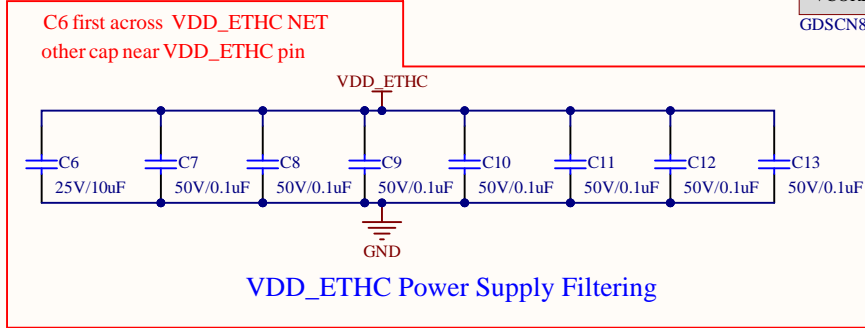
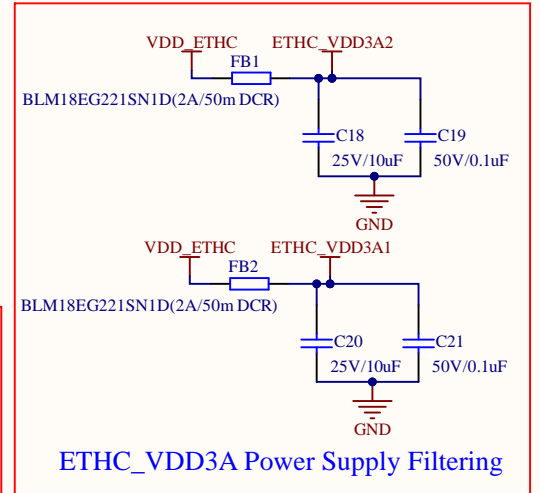
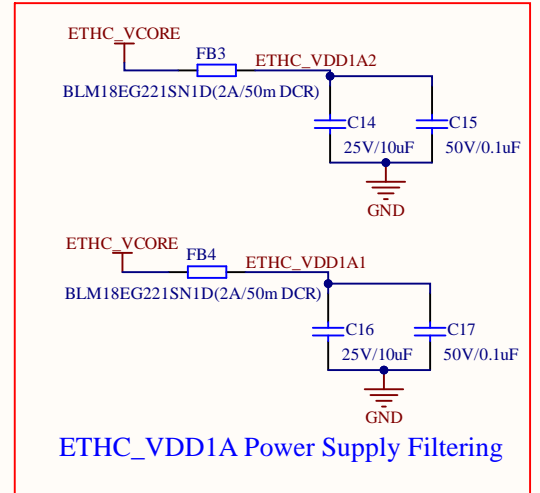
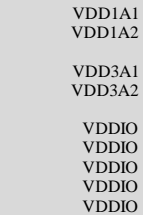
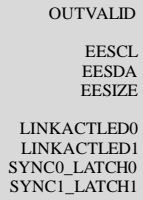
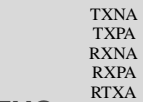
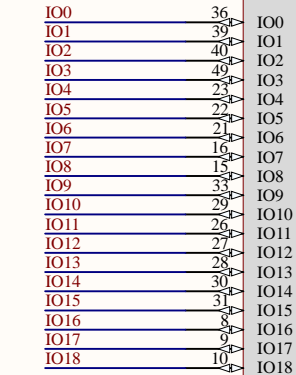
Date: 2025-3

Author: jiawei.yuan

GDETHC



GDETHC



Company Name: GigaDevice

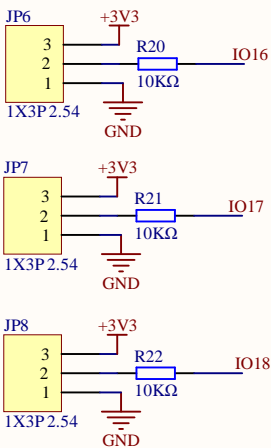
File Name: GDETHC

Revision: 1.1

Date: 2025-3

Author: jiawei.yuan

GDETHCStrap



Signal Name	Logic	Connector	Strap
IO16	0	JP6(1&2) Default	SPI Mode
	1	JP6(2&3)	EXMC Mode
IO17	0	JP7(1&2) Default	LDO no Bypass
	1	JP7(2&3)	LDO Bypass
IO18	0	JP8(1&2) Default	No Reset delay
	1	JP8(2&3)	Open Reset delay

Company Name: GigaDevice

File Name: GDETHCStrap

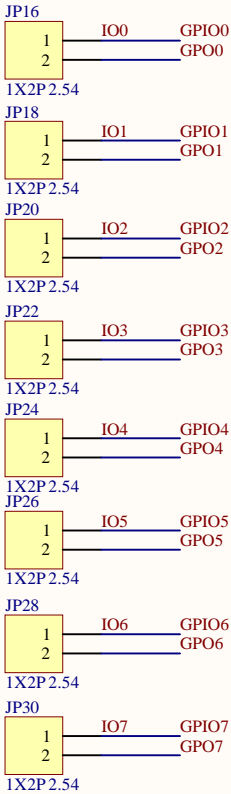
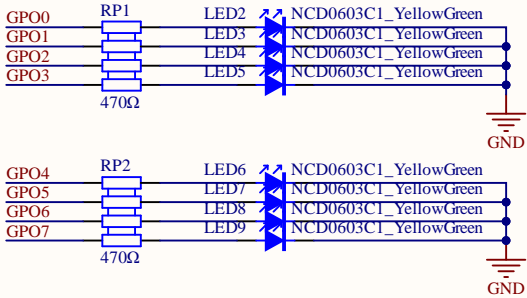
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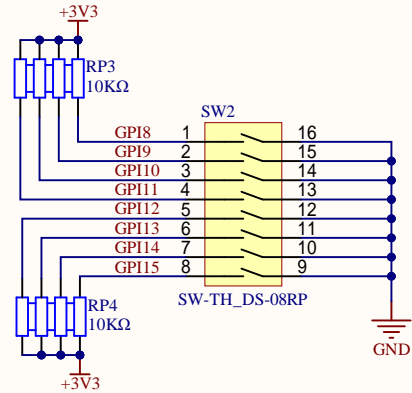
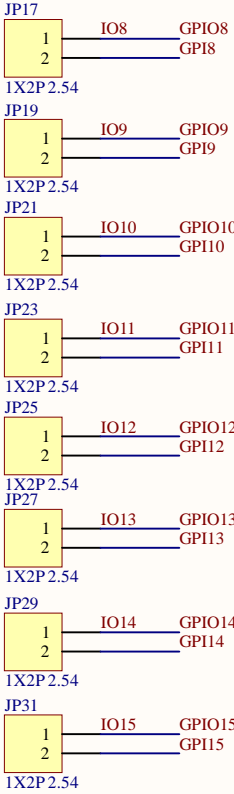
Author: [jiawei.yuan](#)

GPIO

Digital Outputs



Digital Inputs



Input=1(Default)
Input=0(change the Switch position)

Company Name: GigaDevice

File Name: GPIO

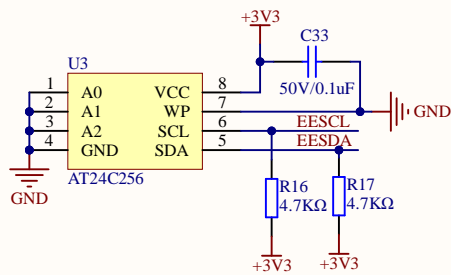
Revision: 1.1

Date: 2025-3

Author: jiawei.yuan

I2C

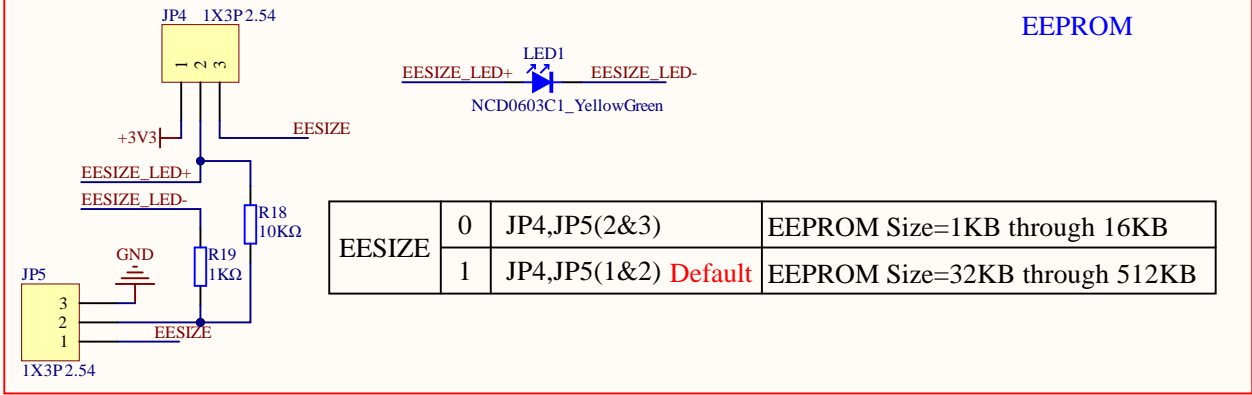
EEPROM



Different sizes can be mounted

I2C EEPROM Lower size Below
16K (2K X 8)
I2C EEPROM Higher size Above
16K (2K X 8)

EEPROM



Company Name: GigaDevice

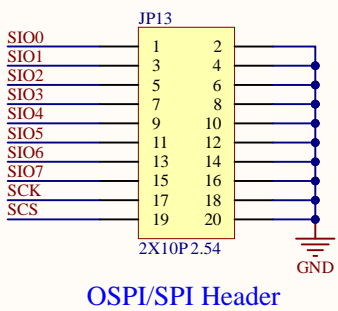
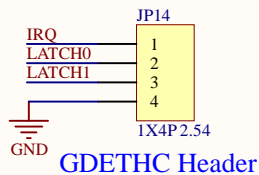
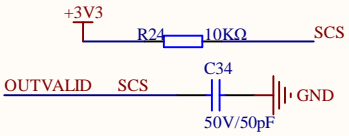
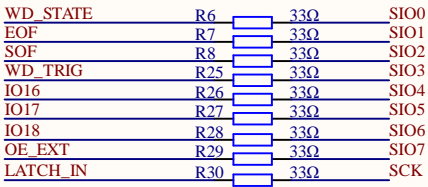
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Revision: 1.1

Date: 2025-3

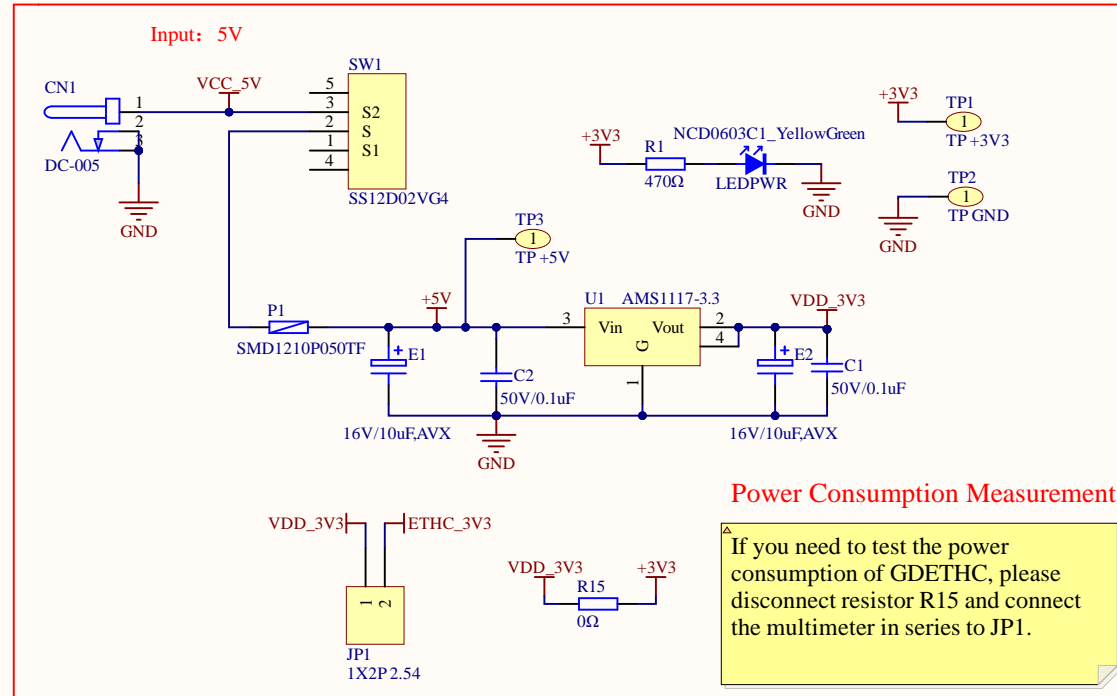
Author: jiawei.yuan

OSPI&SPI



Company Name: GigaDevice		
File Name: OSPI&SPI		
Revision: 1.1	Date: 2025-3	Author: jiawei.yuan

Power



Company Name: GigaDevice

File Name: Power

Revision: 1.1

Date: 2025-3

Author: jiawei.yuan

