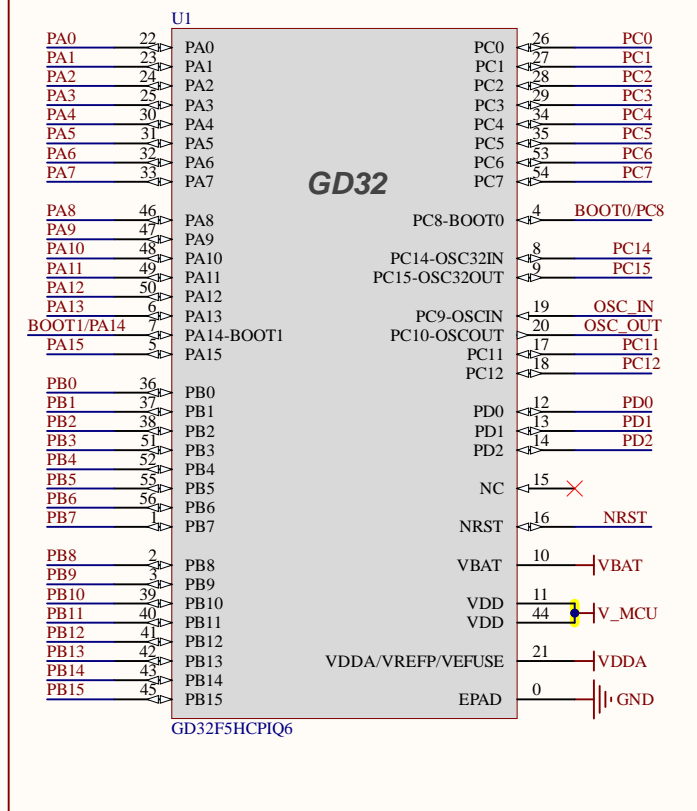
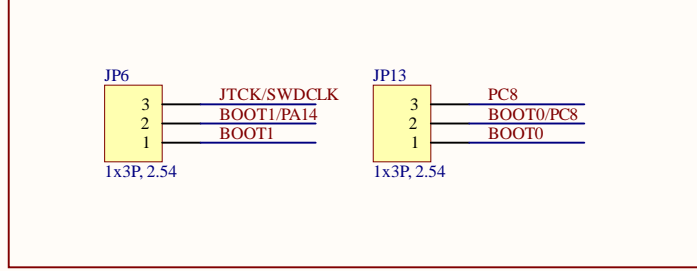


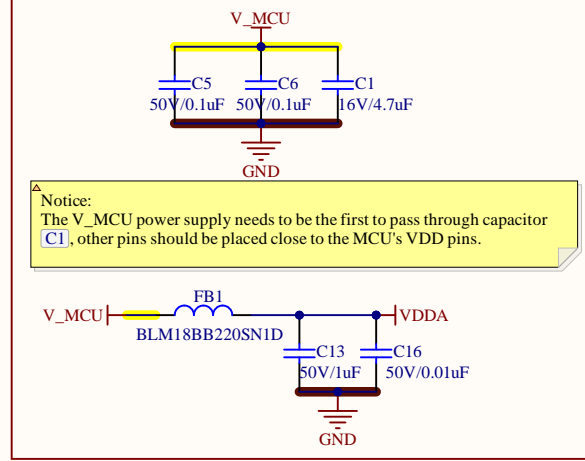
MCU



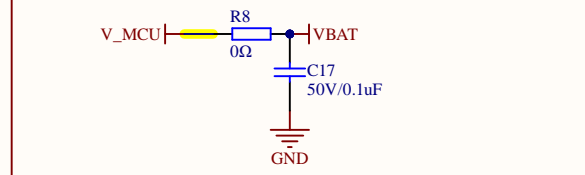
GPIO Function Select



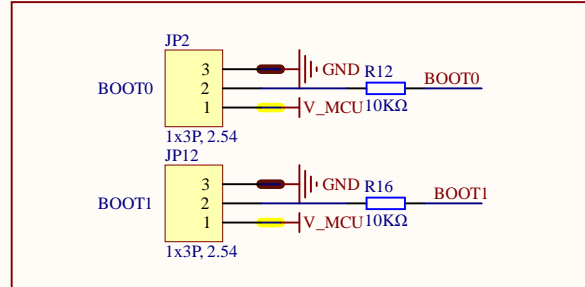
Power Filtering



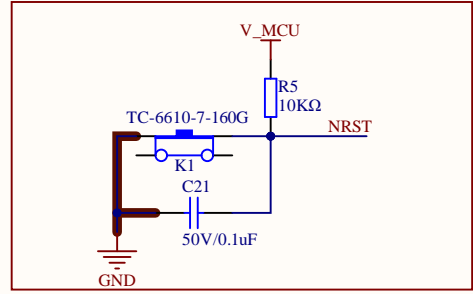
VBAT



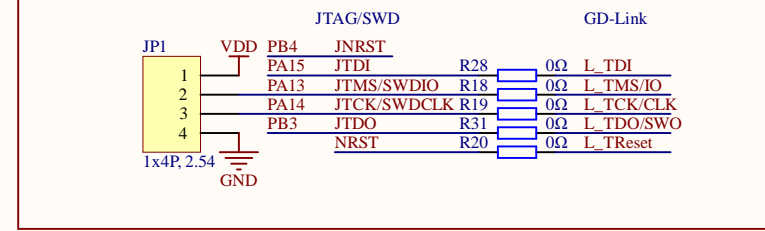
BOOT



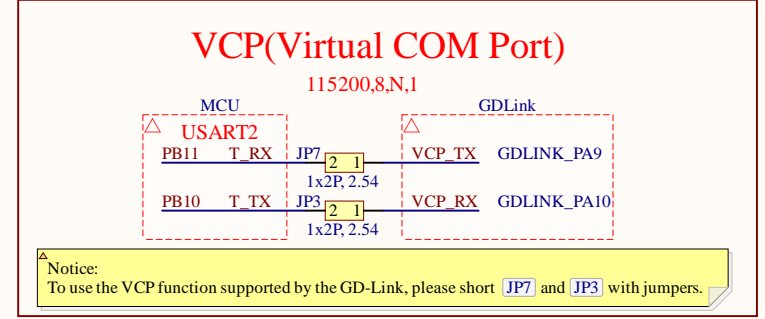
NRST



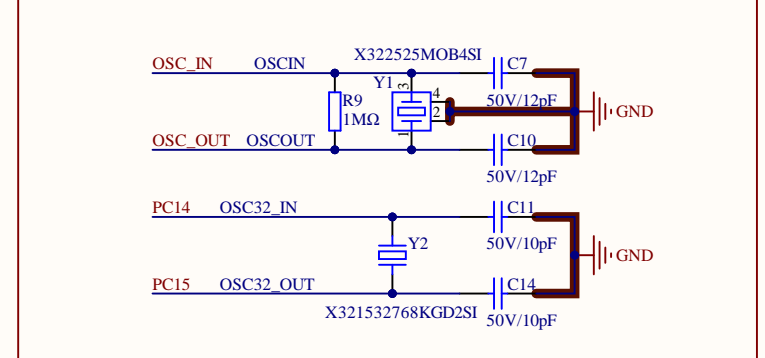
JTAG/SWD



VCP(Virtual COM Port)

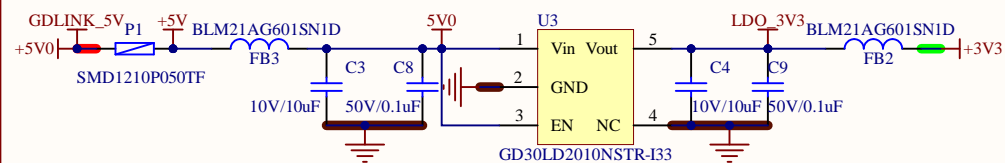


HXTAL&LXTAL

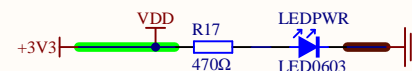


Project Title: GD32F5HCP-START			
Sheet Title: MCU			Size: A4
Designer: boya.jiang	Revision: 1.0		
Sheet: 1 of 6	Date: 2025.11		

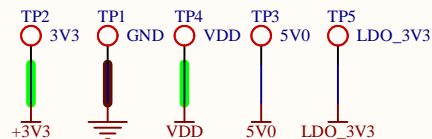
3V3 Power Supply



Power LED

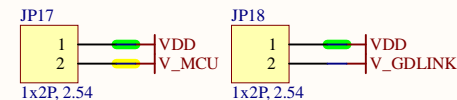


Test Point



Power Consumption

Notice:
If you want to test the MCU's power consumption, connect the multimeter in series to [JP17](#).



Project Title: **GD32F5HCP-START**

Sheet Title: **Power**

Size:
A4

Designer: [boya.jiang](#)

Revision: 1.0

Sheet: 2 of 6

Date: 2025.11



V_GDLINK

JP100

1

2

3

4

L_SWIO

L_SWCLK

GND

SMD, 1x4P, 2.54



Size:
A4

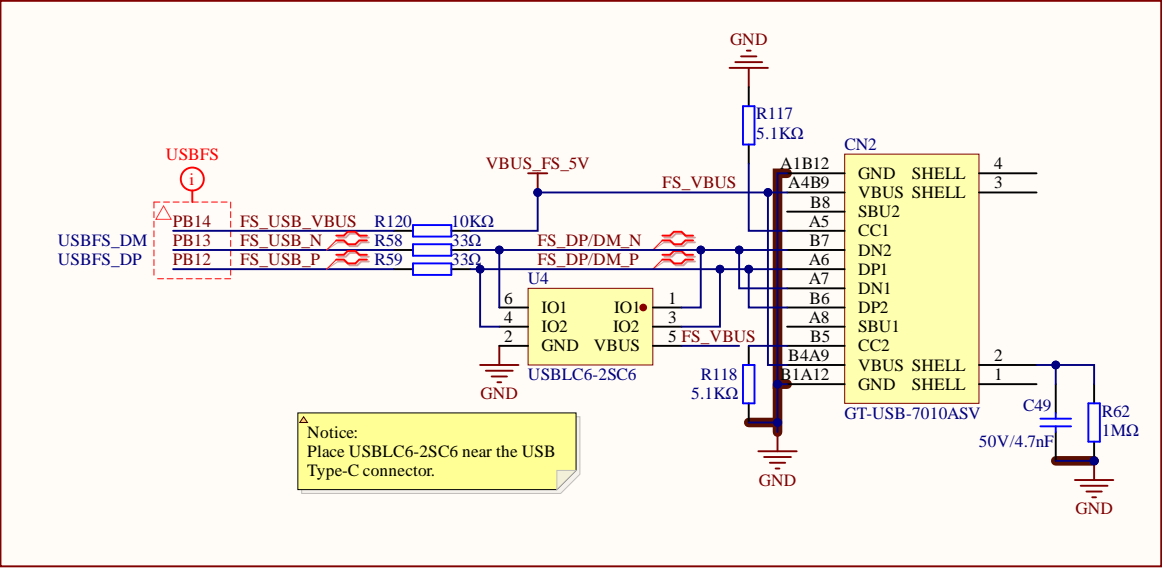
The diagram shows a 7-bit DAC circuit with the following components and connections:

- Inputs:** PA0, PA1, PA3, PC0, PC2, PC3, PC11, PC12
- Resistors:** R6, R7, R1, R3, R2, R4, R13, R14 (all 33Ω)
- Capacitors:** C2, C12, C19, C22, C20, C24, C26, C27 (all 50V/0.01μF)
- Outputs:** A0, A1, A2, A3, A4, A5, A6, A7
- Grounding:** Each output pin is connected to ground through a series of capacitors and resistors.
- Note:** A red dashed box highlights the output pins A1 through A7, with a red circle and 'i' indicating a note A1-7.

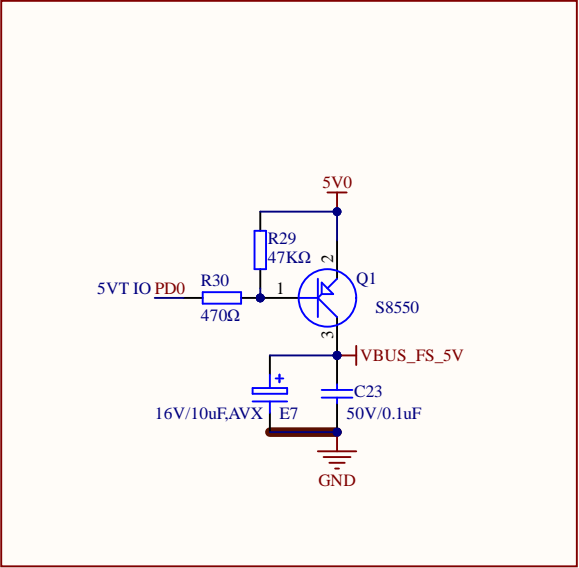


Size:
A4

USB_FS

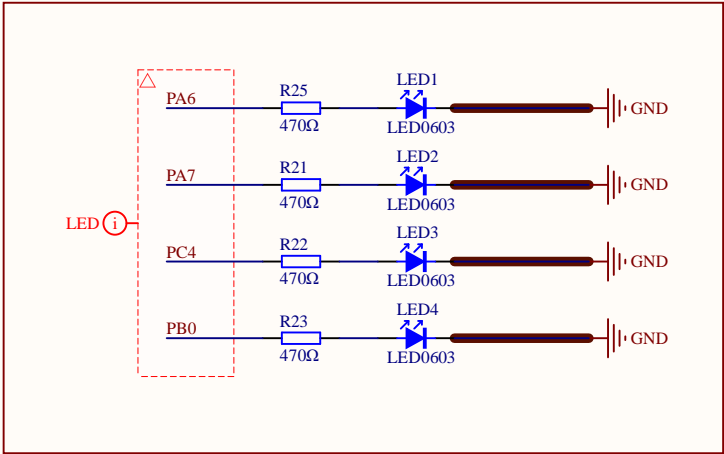


USB_FS VBUS Power Control

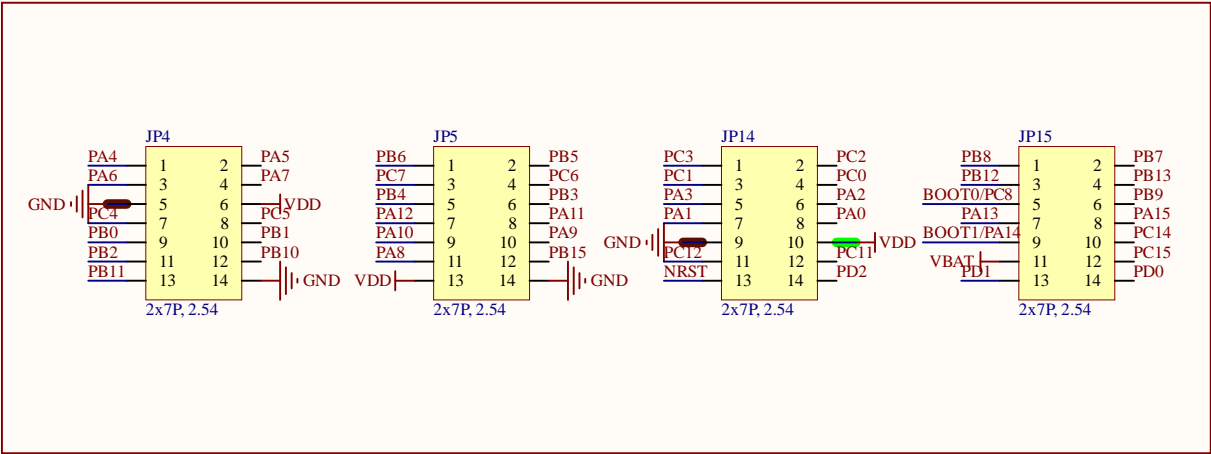


Project Title: GD32F5HCP-START			
Sheet Title: USB_FS			Size: A4
Designer: boya.jiang	Revision: 1.0		
Sheet: 5 of 6	Date: 2025.11		

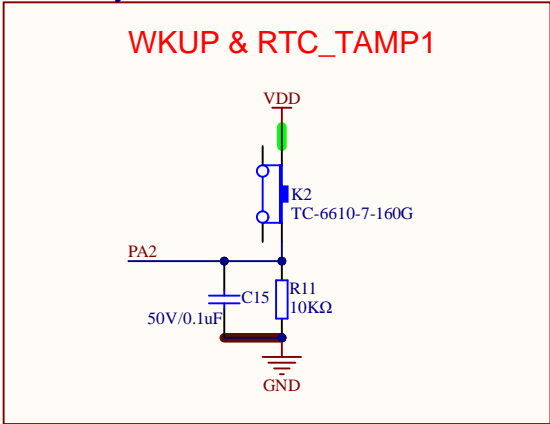
LED



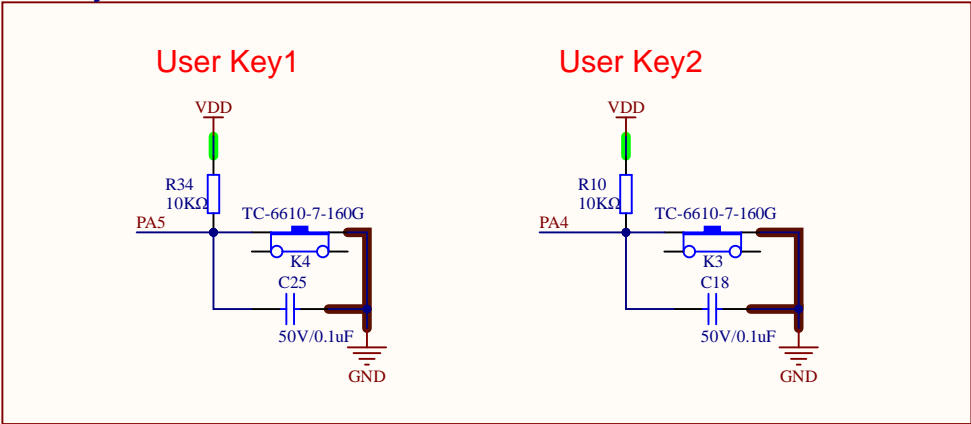
Extension Pin



WKUP Key



User Key




LOGO & Precautions

LG1
PCB
LOGO
GD32-LOGO

LG2

GD32-LOGO-No touching

Project Title: GD32F5HCP-START		
Sheet Title: Extension		Size: A4
Designer: boya.jiang	Revision: 1.0	 GigaDevice
Sheet: 6 of 6	Date: 2025.11	

