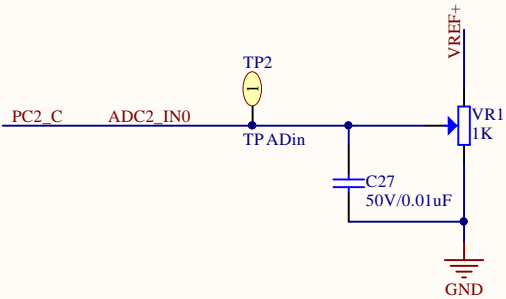


ADC



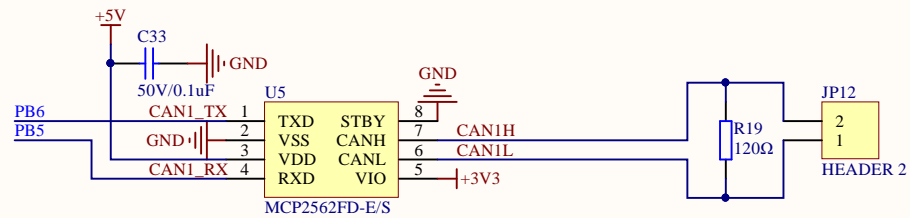
DAC



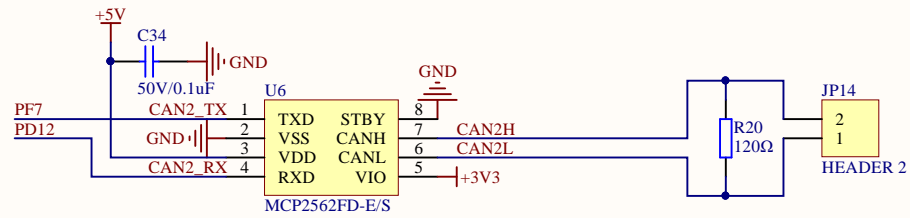
PA5-DAC0_OUT1 are AFIOs, please refer to TLI schematic for right config

Company Name: GigaDevice		
File Name: AD&DA		
Revision: 1.1	Data: 2023-3	Author: jiawei.yuan

CAN



PB5-CAN1_RX are AFIOs, please refer to TLI schematic for right config
PB6-CAN1_TX are AFIOs, please refer to DCI schematic for right config



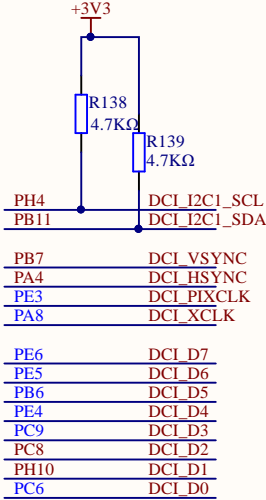
Company Name: GigaDevice

File Name: CAN

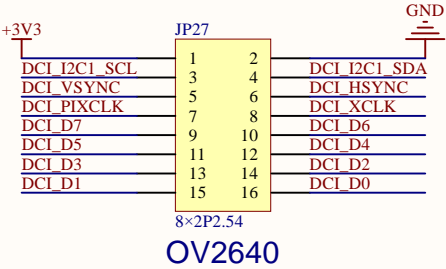
Revision: 1.0

Data: 2023-2

Author: XuFei



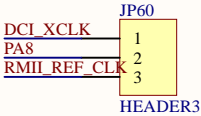
DCI 8bit



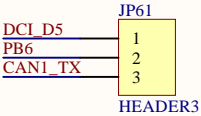
OV2640

DCI_8bit, TLI and SDRAM can be used at the same time

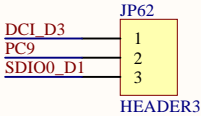
Short JP60(1,2)for DCI function
Short JP60(2,3)for ETH function



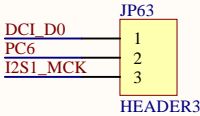
Short JP61(1,2)for DCI function
Short JP61(2,3)for CAN function



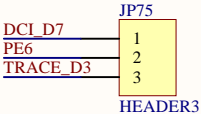
Short JP62(1,2)for DCI function
Short JP62(2,3)for SDIO function



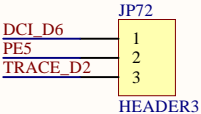
Short JP63(1,2)for DCI function
Short JP63(2,3)for I2S function



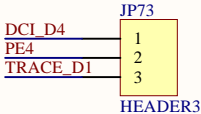
Short JP71(1,2)for DCI function
Short JP71(2,3)for TRACE function



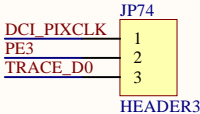
Short JP72(1,2)for DCI function
Short JP72(2,3)for TRACE function



Short JP73(1,2)for DCI function
Short JP73(2,3)for TRACE function



Short JP74(1,2)for DCI function
Short JP74(2,3)for TRACE function



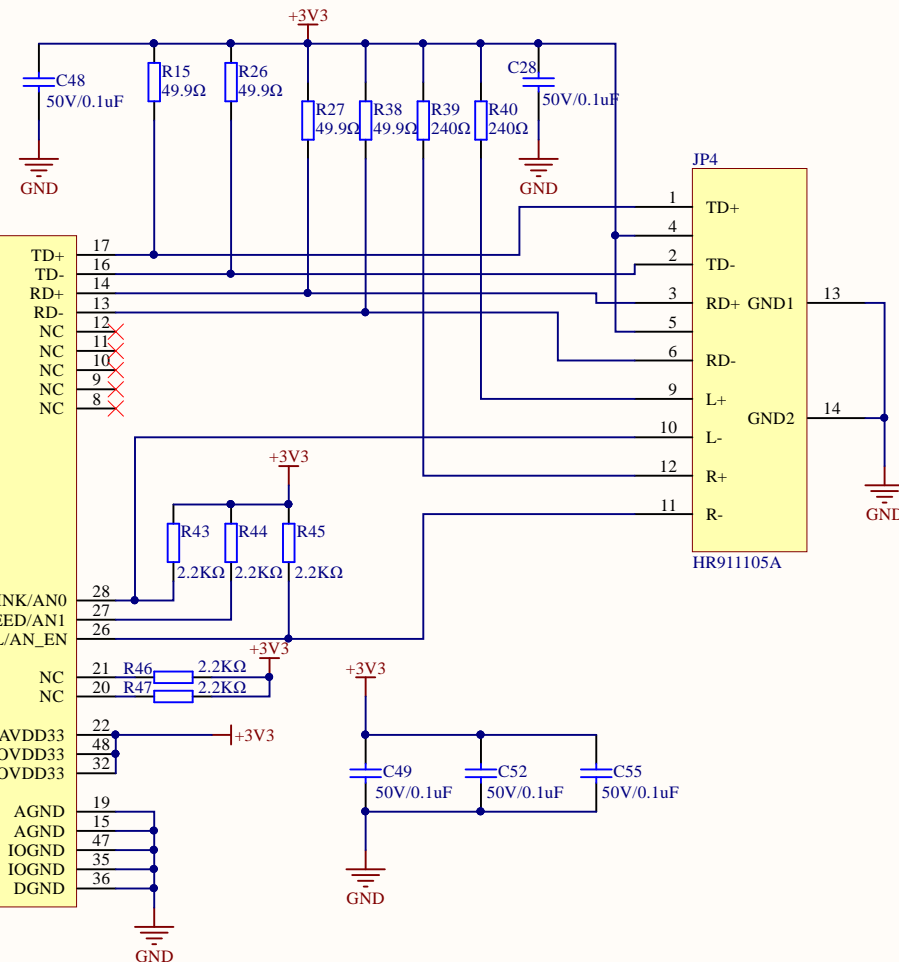
Company Name: GigaDevice

File Name: DCI

Revision: 1.0

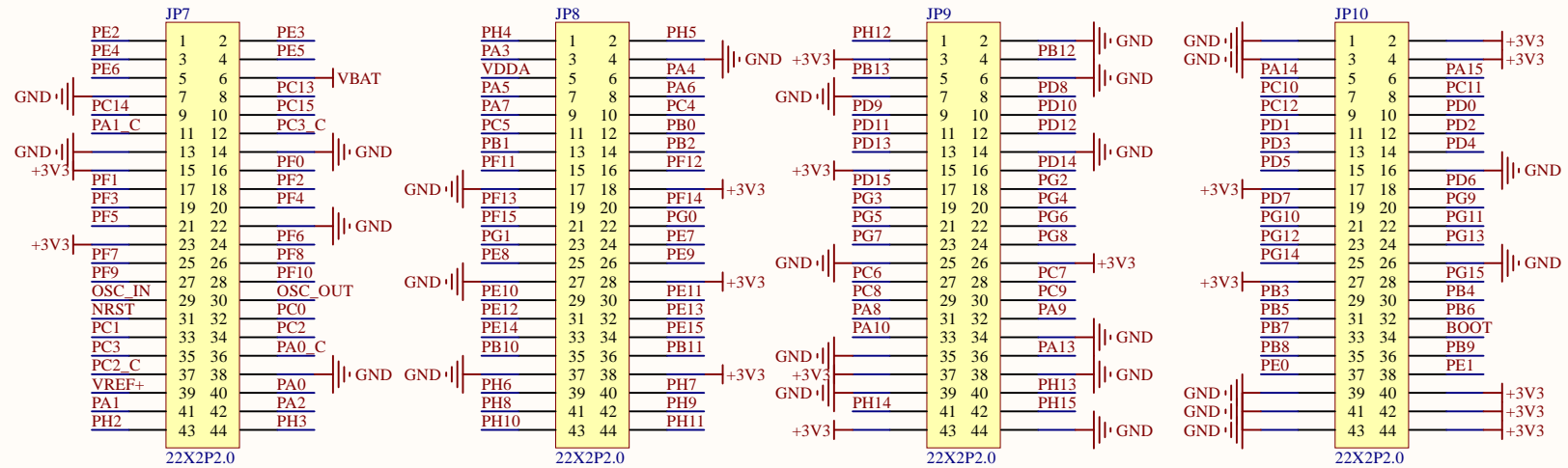
Data: 2023-2

Author: jiawei.yuan

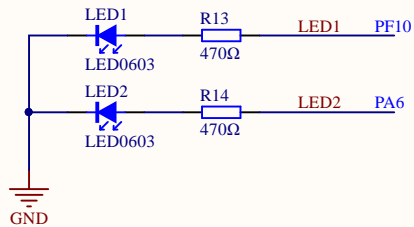


Author: jiawei.yuan

Extension Pin

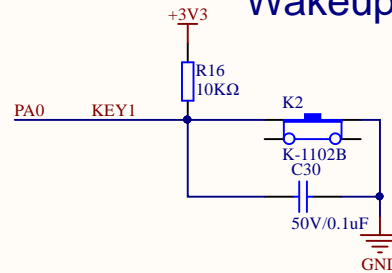


LED

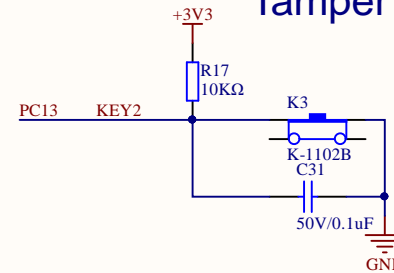


PF10-LED1 are AFIOs, please refer to TLI schematic for right config
PA6-LED2 are AFIOs, please refer to USB schematic for right config

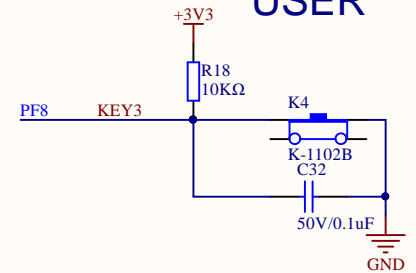
Wakeup



Tamper



USER



PF8-KEY3 are AFIOs, please refer to TLI schematic for right config

Company Name: GigaDevice

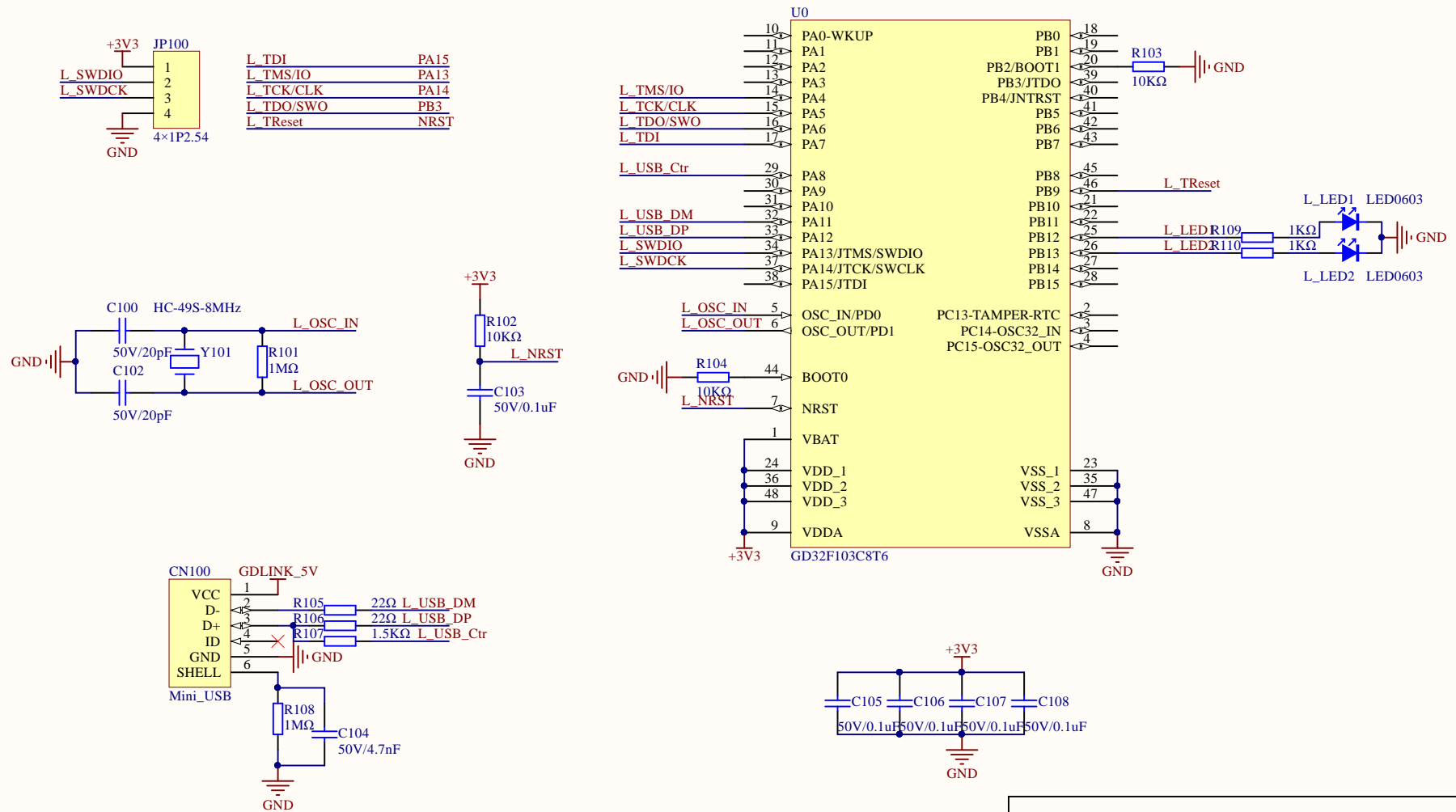
File Name: Extension

Revision: 1.0

Data: 2023-2

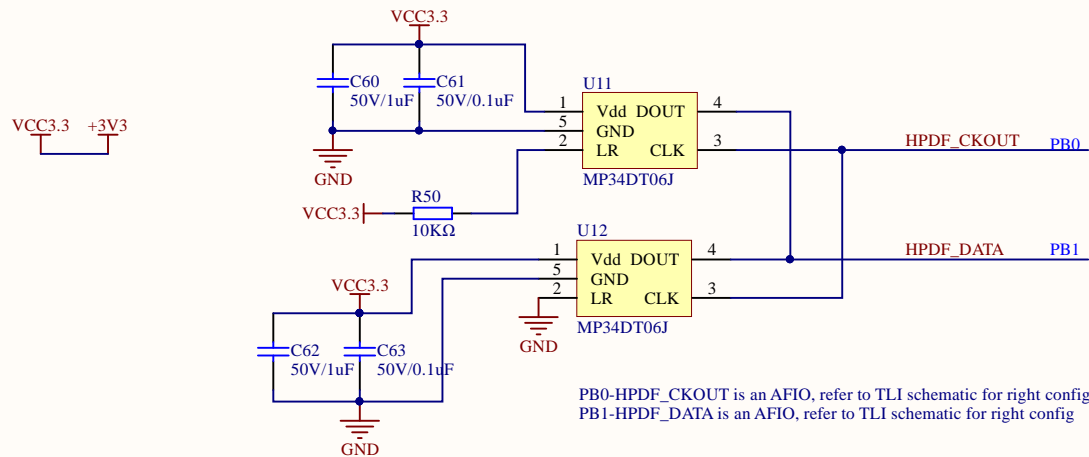
Author: jiawei.yuan

GDLink



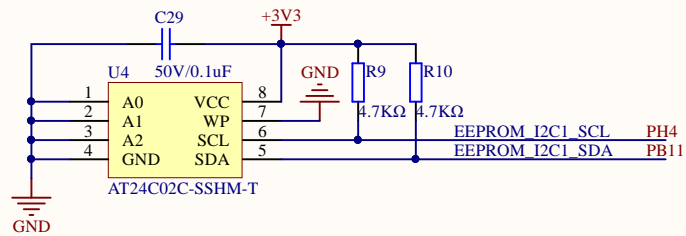
Company Name: GigaDevice		
File Name: GDLink		
Revision: 1.0	Data: 2022-2	Author: XuFei

HPDF&PDM



Company Name: GigaDevice		
File Name: HPDF&PDM		
Revision: 1.0	Data: 2023-2	Author: jiawei.yuan

I2C



Company Name: GigaDevice		
File Name: I2C		
Revision: 1.0	Data: 2023-1	Author: jiawei.yuan

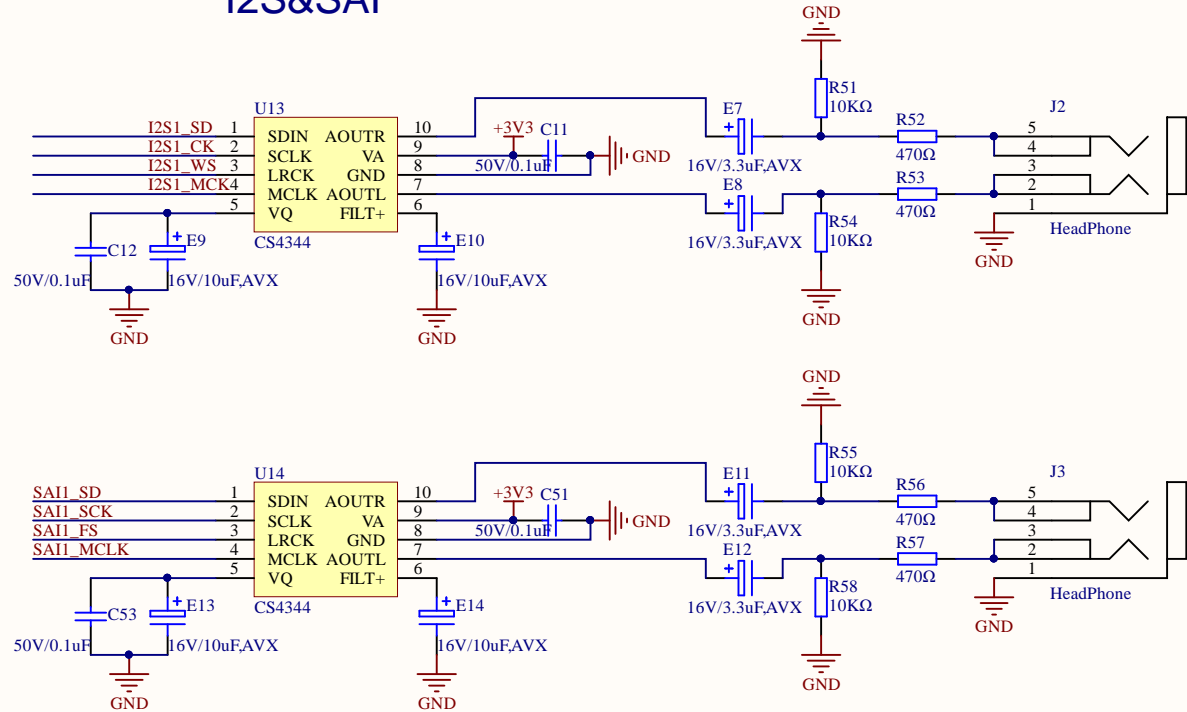
I2S&SAI

PC3 I2S1_SD
PD3 I2S1_CK
PB9 I2S1_WS
PC6 I2S1_MCK

PC3-I2S1_SD are AFIOs, please refer to SDRAM schematic for right config
PD3-I2S1_CK are AFIOs, please refer to TL1 schematic for right config
PB9-I2S1_WS are AFIOs, please refer to TL1 schematic for right config
PC6-I2S1_MCK are AFIOs, please refer to DCI schematic for right config

PG9 SAI1_FS
PH2 SAI1_SCK
PH3 SAI1_MCLK
PG10 SAI1_SD

PH2-SAI1_SCK are AFIOs, please refer to TL1 schematic for right config
PH3-SAI1_MCLK are AFIOs, please refer to TL1 schematic for right config
PG10-SAI1_SD are AFIOs, please refer to TL1 schematic for right config



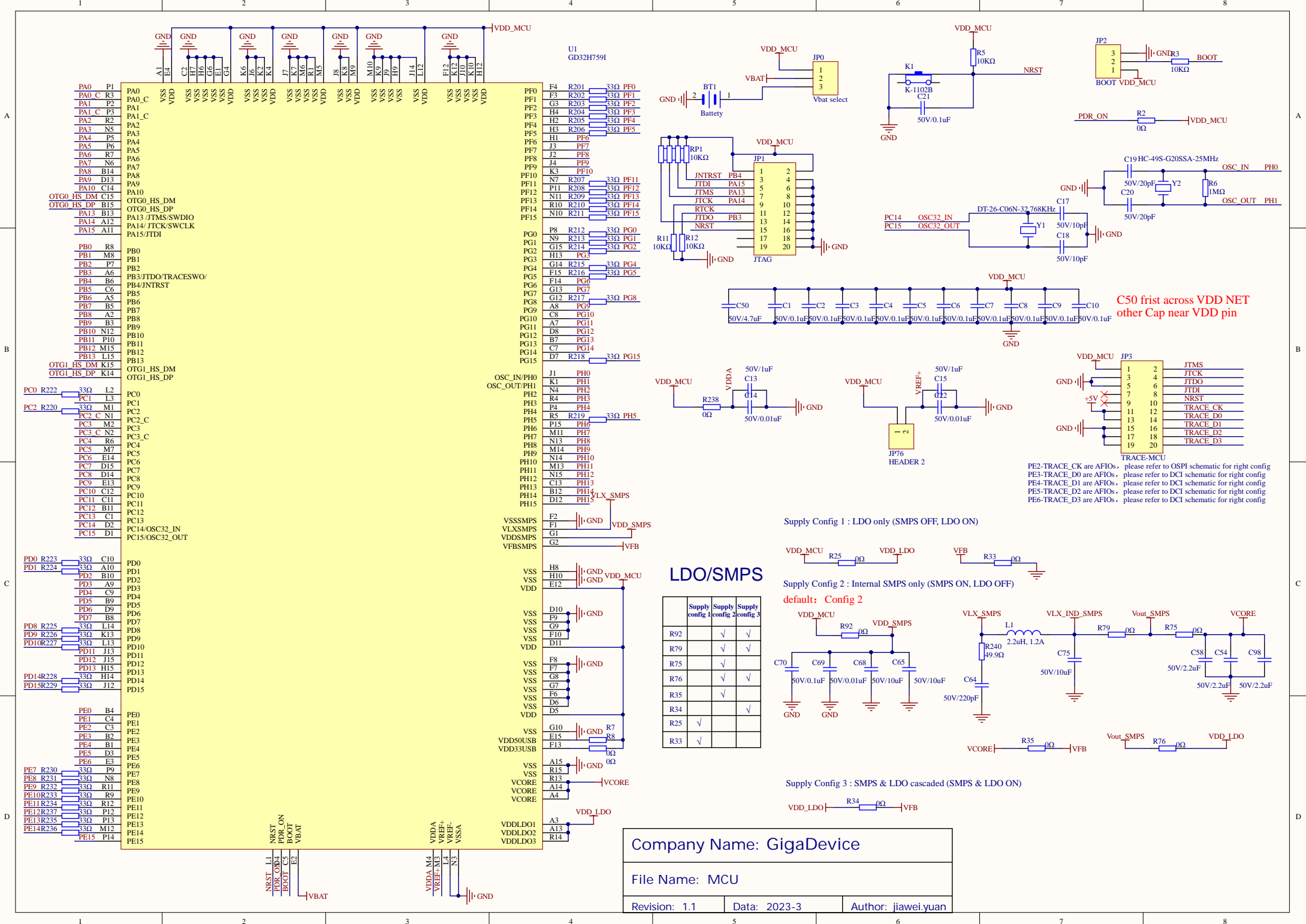
Company Name: GigaDevice

File Name: I2S&SAI

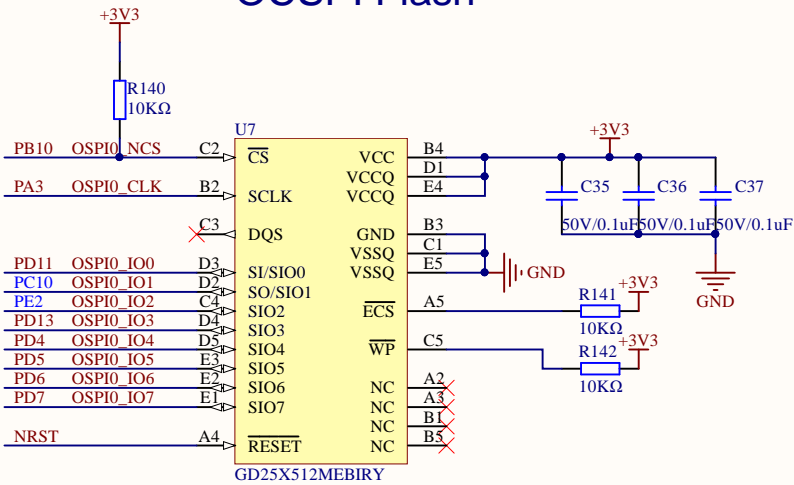
Revision: 1.0

Data: 2023-2

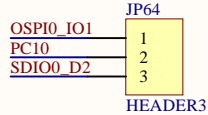
Author: jiawei.yuan



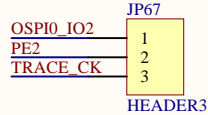
OCSPI Flash



Short JP64(1,2)for OSPI0 function
Short JP64(2,3)for SDIO function



Short JP67(1,2)for OSPI0 function
Short JP67(2,3)for TRACE function



Company Name: GigaDevice

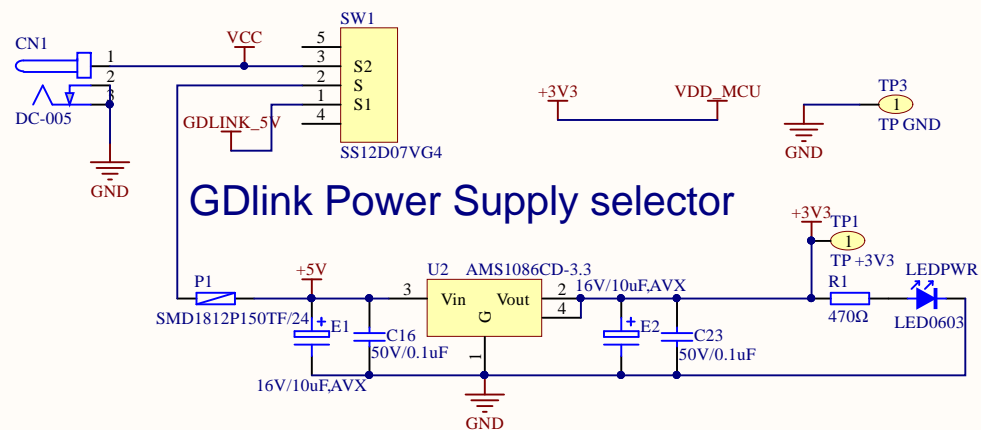
File Name: OSPI

Revision: 1.0

Data: 2023-2

Author: jiawei.yuan

POWER



Company Name: GigaDevice

File Name: Power

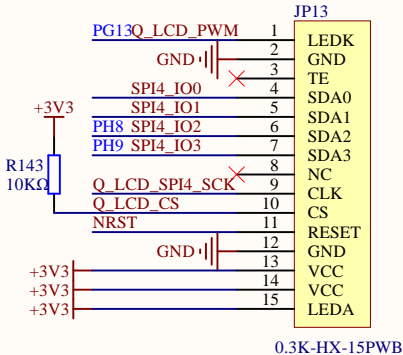
Revision: 1.0

Data: 2023-2

Author: jiawei.yuan

QSPI_LCD

PH6	SPI4_SCK	Q_LCD_SPI4_SCK
PF9	SPI4_MOSI	SPI4_IO0
PH7	SPI4_MISO	SPI4_IO1
PF6	SPI4_NSS	Q_LCD_CS



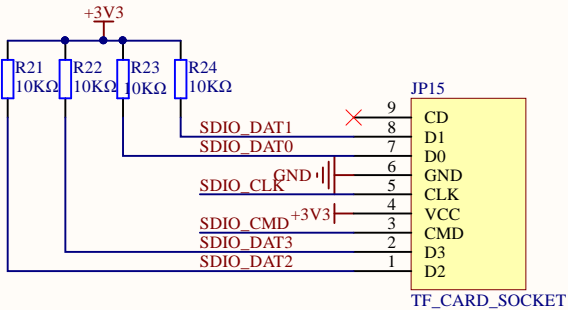
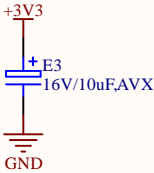
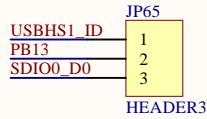
PH8-SPI4_IO2 are AFIOs, please refer to TLI schematic for right config
PH9-SPI4_IO3 are AFIOs, please refer to TLI schematic for right config
PG13-Q_LCD_PWM are AFIOs, please refer to TLI schematic for right config

Company Name: GigaDevice		
File Name: QSPI_LCD		
Revision: 1.0	Data: 2023-2	Author: jiawei.yuan

SDIO

	PD2	SDIO_CMD
	PC12	SDIO_CLK
PB13	SDIO0_D0	SDIO_DAT0
PC9	SDIO0_D1	SDIO_DAT1
PC10	SDIO0_D2	SDIO_DAT2
PC11	SDIO0_D3	SDIO_DAT3

Short JP65(2,3)for SDIO function
Short JP65(1,2)for USB function



PC9-SDIO0_D1 are AFIOs, please refer to DCI schematic for right config
PC10-SDIO0_D2 are AFIOs, please refer to OSPI schematic for right config
PC11-SDIO0_D3 are AFIOs, please refer to TLI schematic for right config

Company Name: GigaDevice

File Name: SDIO

Revision: 1.0

Data: 2023-2

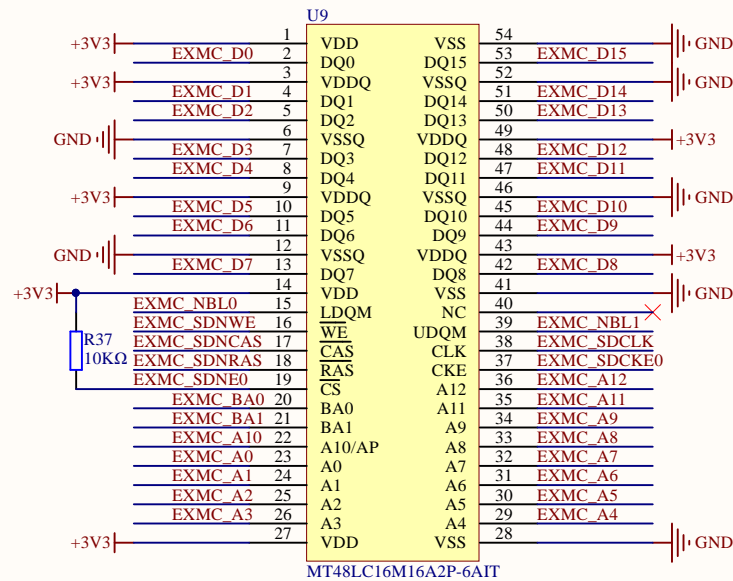
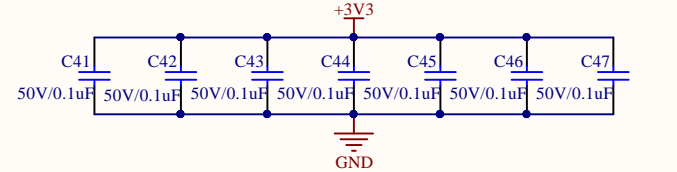
Author: jiawei.yuan

SDRAM

PF0 EXMC_A0
PF1 EXMC_A1
PF2 EXMC_A2
PF3 EXMC_A3
PF4 EXMC_A4
PF5 EXMC_A5
PF12 EXMC_A6
PF13 EXMC_A7
PF14 EXMC_A8
PF15 EXMC_A9
PG0 EXMC_A10
PG1 EXMC_A11
PG2 EXMC_A12

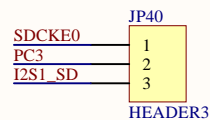
PD14 EXMC_D0
PD15 EXMC_D1
PD0 EXMC_D2
PD1 EXMC_D3
PE7 EXMC_D4
PE8 EXMC_D5
PE9 EXMC_D6
PE10 EXMC_D7
PE11 EXMC_D8
PE12 EXMC_D9
PE13 EXMC_D10
PE14 EXMC_D11
PC0 EXMC_D12
PD8 EXMC_D13
PD9 EXMC_D14
PD10 EXMC_D15

PE0 EXMC_NBL0
PE1 EXMC_NBL1
PG4 EXMC_BA0
PG5 EXMC_BA1
PG8 EXMC_SDCLK
PG15 EXMC_SDNCRAS
PF11 EXMC_SDNRRAS
PC2 EXMC_SDNE0
PH5 EXMC_SDNWE



SDCKE0 PC3 R221 33Ω EXMC_SDCKE0

Short JP40(1,2) for SDRAM function
Short JP40(2,3) for I2S function



DCI_8bit, TLI and SDRAM can be used at the same time

Company Name: GigaDevice

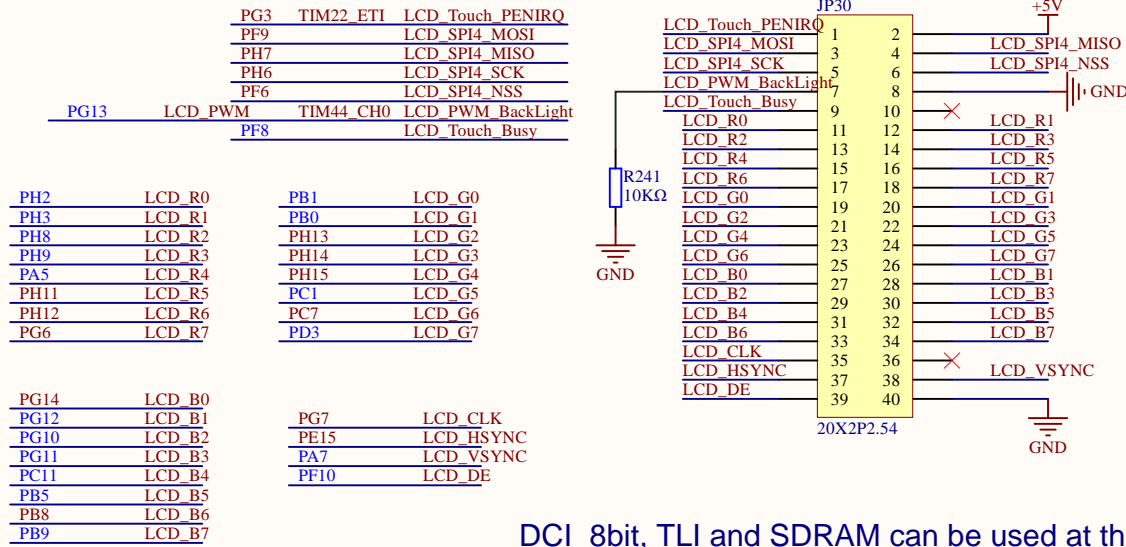
File Name: SDRAM

Revision: 1.0

Data: 2023-2

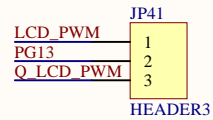
Author: jiawei.yuan

TLI

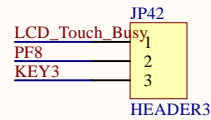


DCI_8bit, TLI and SDRAM can be used at the same time

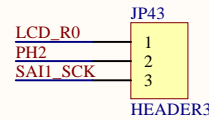
Short JP41(1,2)for TLI function
Short JP41(2,3)for QSPI_LCD function



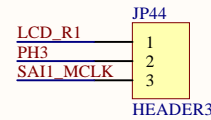
Short JP42(1,2)for TLI function
Short JP42(2,3)for KEY function



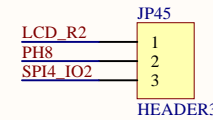
Short JP43(1,2)for TLI function
Short JP43(2,3)for SAI function



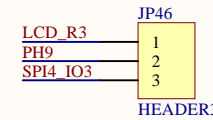
Short JP44(1,2)for TLI function
Short JP44(2,3)for SAI function



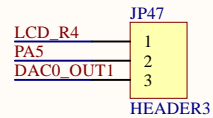
Short JP45(1,2)for TLI function
Short JP45(2,3)for QSPI_LCD function



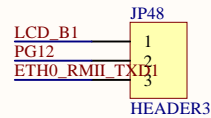
Short JP46(1,2)for TLI function
Short JP46(2,3)for QSPI_LCD function



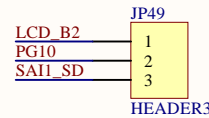
Short JP47(1,2)for TLI function
Short JP47(2,3)for DAC function



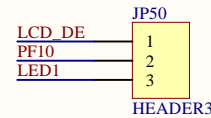
Short JP48(1,2)for TLI function
Short JP48(2,3)for ETH function



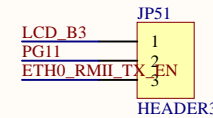
Short JP49(1,2)for TLI function
Short JP49(2,3)for SAI function



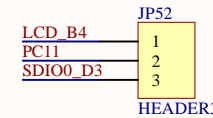
Short JP50(1,2)for TLI function
Short JP50(2,3)for LED function



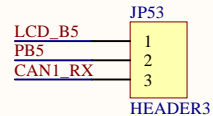
Short JP51(1,2)for TLI function
Short JP51(2,3)for ETH function



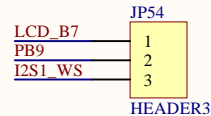
Short JP52(1,2)for TLI function
Short JP52(2,3)for SDIO function



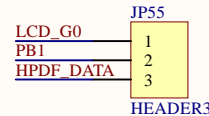
Short JP53(1,2)for TLI function
Short JP53(2,3)for CAN function



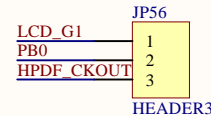
Short JP54(1,2)for TLI function
Short JP54(2,3)for I2S function



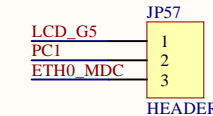
Short JP55(1,2)for TLI function
Short JP55(2,3)for HPDF function



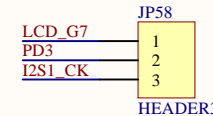
Short JP56(1,2)for TLI function
Short JP56(2,3)for HPDF function



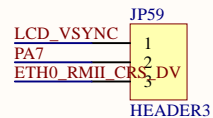
Short JP57(1,2)for TLI function
Short JP57(2,3)for ETH function



Short JP58(1,2)for TLI function
Short JP58(2,3)for I2S function



Short JP59(1,2)for TLI function
Short JP59(2,3)for ETH function



Company Name: GigaDevice

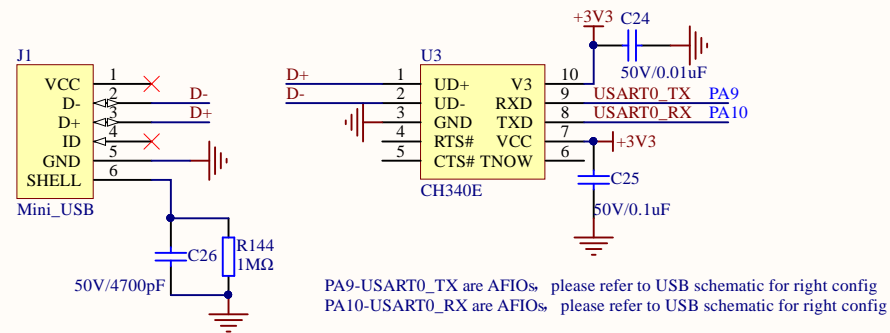
File Name: TLI

Revision: 1.0

Data: 2023-2

Author: jiawei.yuan

USART To USB



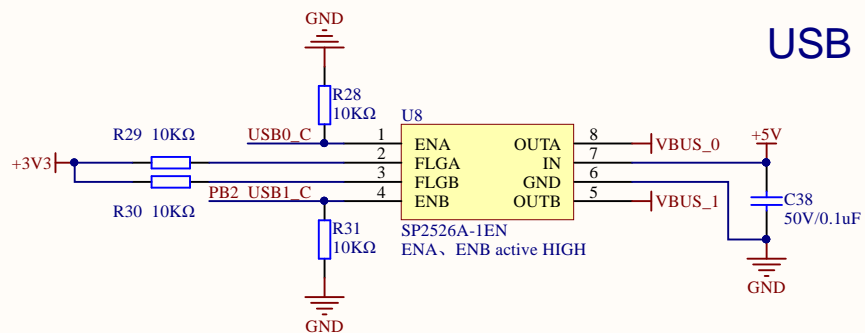
Company Name: GigaDevice

File Name: USART

Revision: 1.0

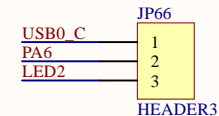
Data: 2023-2

Author: jiawei.yuan

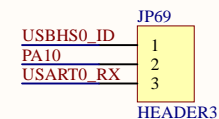


USB

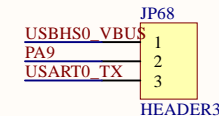
Short JP66(1,2)for USB function
Short JP66(2,3)for LED function



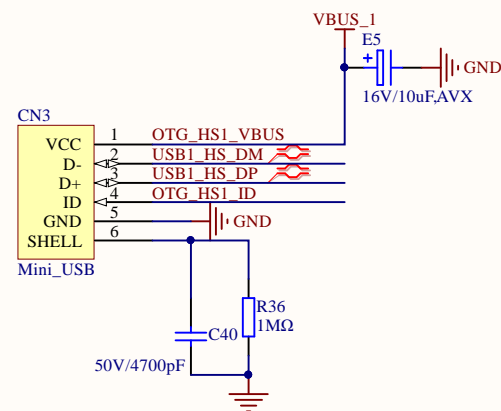
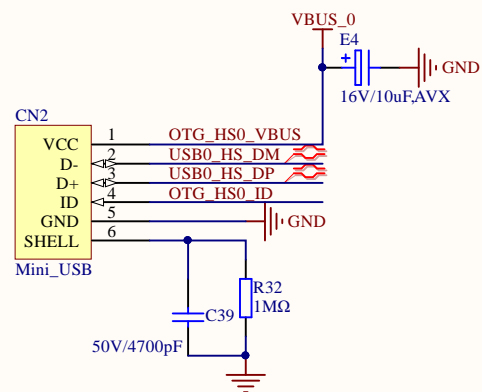
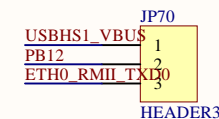
Short JP69(1,2)for USB function
Short JP69(2,3)for USART function



Short JP68(1,2)for USB function
Short JP68(2,3)for USART function



Short JP70(1,2)for USB function
Short JP70(2,3)for ETH function



PA10	USBHS0_ID	OTG_HS0_ID
OTG0_HS_DP	USB0_HS_DP	
OTG0_HS_DM	USB0_HS_DM	
PA9	USBHS0_VBUS	OTG_HS0_VBUS

PB13	USBHS1_ID	OTG_HS1_ID
OTG1_HS_DP	USB1_HS_DP	
OTG1_HS_DM	USB1_HS_DM	
PB12	USBHS1_VBUS	OTG_HS1_VBUS

PB13-USBHS1_ID are AFIOs, please refer to SDIO schematic for right config

Company Name: GigaDevice

File Name: USB_OTG

Revision: 1.0

Data: 2023-2

Author: jiawei.yuan

