

1

2

3

4

A

A

B

B

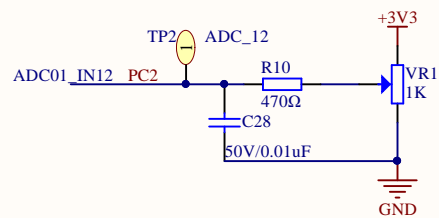
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C

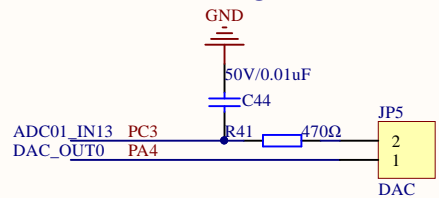
D

D

ADC



DAC



Company Name: GigaDevice

File Name: AD_DA

Revision: 1.1

Data: 2020-6

Author: XuFei

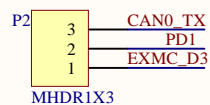
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2

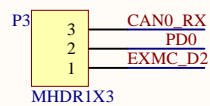
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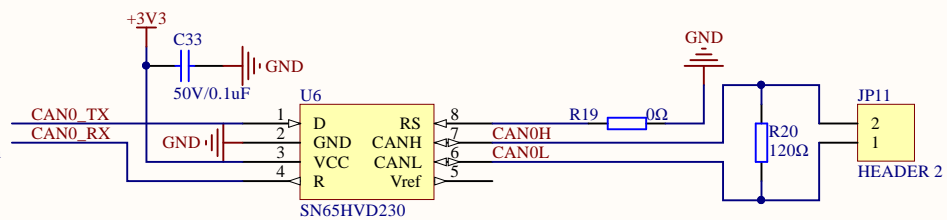
Short P2(1,2) for EXMC function
Short P2(2,3) for CAN0 function



Short P3(1,2) for EXMC function
Short P3(2,3) for CAN0 function



CAN



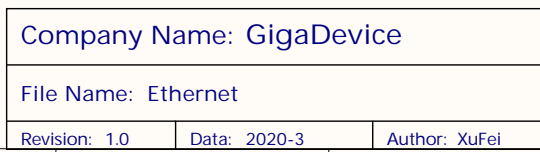
Company Name: GigaDevice

File Name: CAN

Revision: 1.0

Data: 2020-3

Author: XuFei



The schematic shows the following components and connections:

- Microcontroller (U9):** GD9FU1G8F2A.
- Power Supply:** +3V3.
- Resistors:** R50 (10KΩ), R49 (10KΩ), R51 (10KΩ).
- Capacitors:** C24 (50V/0.1μF), C25 (50V/0.1μF).
- Header:** JP22 HEADER 3.
- Pin Connections:**
 - IO7 (EXMC_D744) to VDD1 (+3V3)
 - IO6 (EXMC_D643) to VDD2 (+3V3)
 - IO5 (EXMC_D542) to VSS1 (GND)
 - IO4 (EXMC_D441) to VSS2 (GND)
 - IO3 (EXMC_D332) to GND
 - IO2 (EXMC_D231) to GND
 - IO1 (EXMC_D130) to GND
 - IO0 (EXMC_D029) to GND
 - CL (EXMC_A146) to +3V3 via R50
 - WP (EXMC_A17) to +3V3 via R49
 - RB (EXMC_NW1B) to +3V3 via R51
 - AL (EXMC_NO1B) to GND
 - WE (EXMC_NC1B) to GND
 - INT1 (EXMC_INT1) to JP22 HEADER 3
 - NWAIT (EXMC_NWAIT) to JP22 HEADER 3

Pinout diagram for the LCD module:

Pin	Signal
1	EXMC_D0
2	EXMC_D1
3	EXMC_D2
4	EXMC_D3
5	EXMC_D4
6	EXMC_D5
7	EXMC_D6
8	EXMC_D7
9	EXMC_D8
10	EXMC_D9
11	EXMC_D10
12	EXMC_D11
13	EXMC_D12
14	EXMC_D13
15	EXMC_D14
16	EXMC_D15
17	EXMC_A23RS
18	EXMC_NOE
19	EXMC_NE1
20	EXMC_NOE
21	NRST
22	BackLight
23	TP_INT PG5
24	TP_INT PG5
25	TP_INT PG5
26	TP_INT PG5
27	TP_INT PG5
28	TP_INT PG5
29	TP_INT PG5
30	TP_INT PG5
31	TP_INT PG5
32	TP_INT PG5

Additional connections shown:

- +3V3 connected to Pin 1.
- GND connected to Pin 2.
- +3V3 connected to Pin 32.
- R52 (10KΩ) connected between Pin 4 and +3V3.

PD14	EXMC D0	PD11	EXMC A16
PD15	EXMC D1	PD12	EXMC A17
PD0	EXMC D2	PE2	EXMC A23
PD1	EXMC D3	PD4	EXMC NOE
PE7	EXMC D4	PD5	EXMC NWE
PE8	EXMC D5	PD6	EXMC NWAIT
PE9	EXMC D6	PD7	EXMC NCE1
PE10	EXMC D7	PG6	EXMC INT1
PE11	EXMC D8	PG9	EXMC_NE1
PE12	EXMC_D9		
PE13	EXMC D10		
PE14	EXMC D11		
PE15	EXMC D12		
PD8	EXMC D13		
PD9	EXMC D14		
PD10	EXMC D15		

Author: XuFei

1

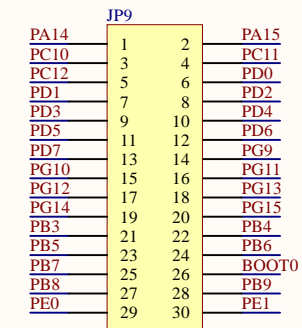
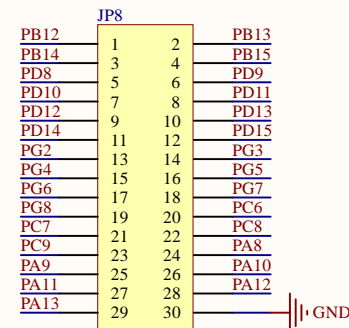
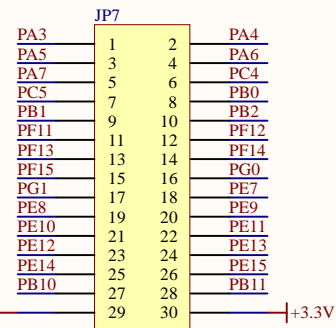
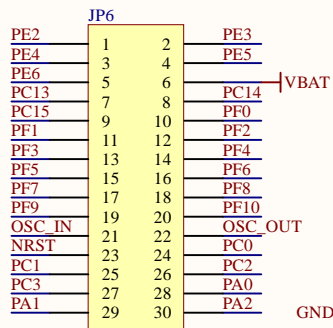
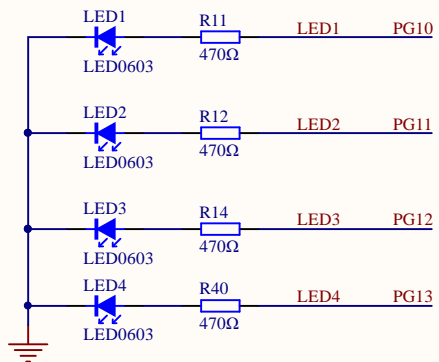
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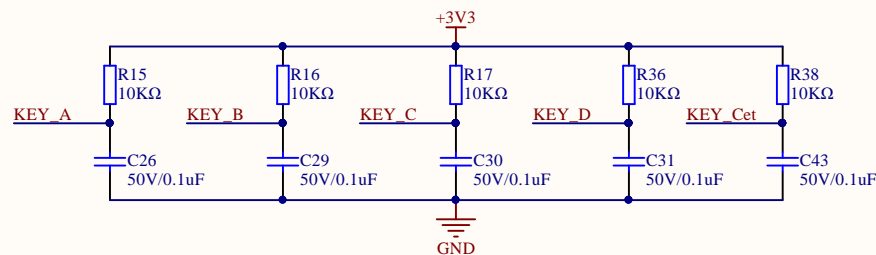
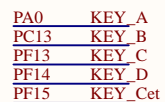
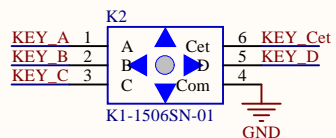
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LED

Extension Pin



KEY



Company Name: GigaDevice

File Name: Extension

Revision: 1.0

Data: 2016-7

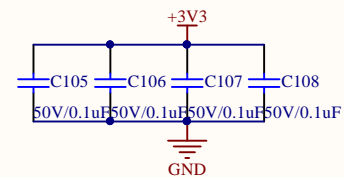
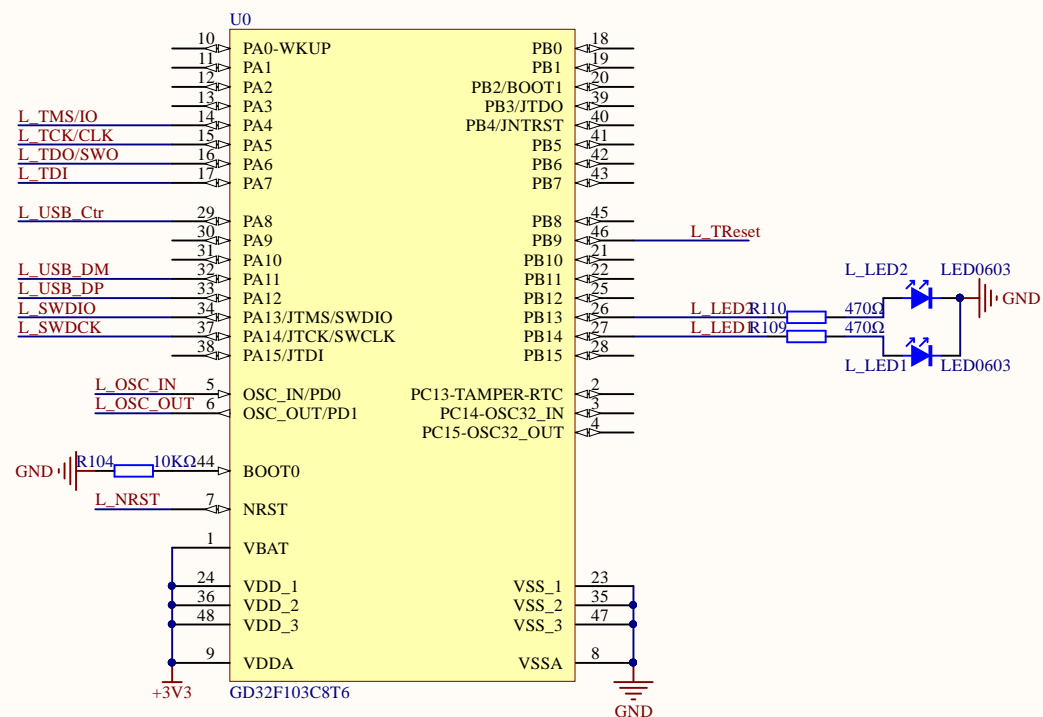
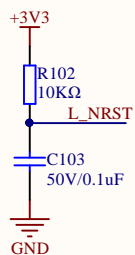
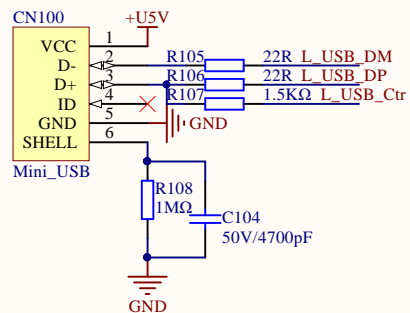
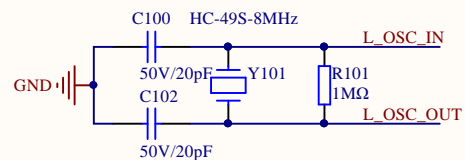
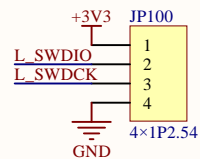
Author: XuFei

1

2

3

4



Company Name: GigaDevice

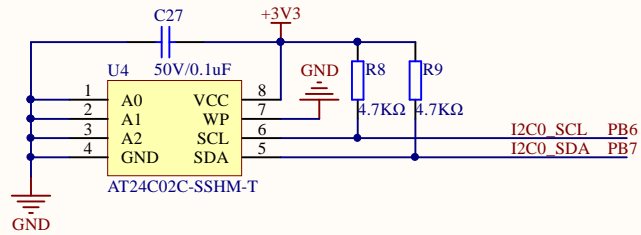
File Name: GDLink

Revision: 1.1

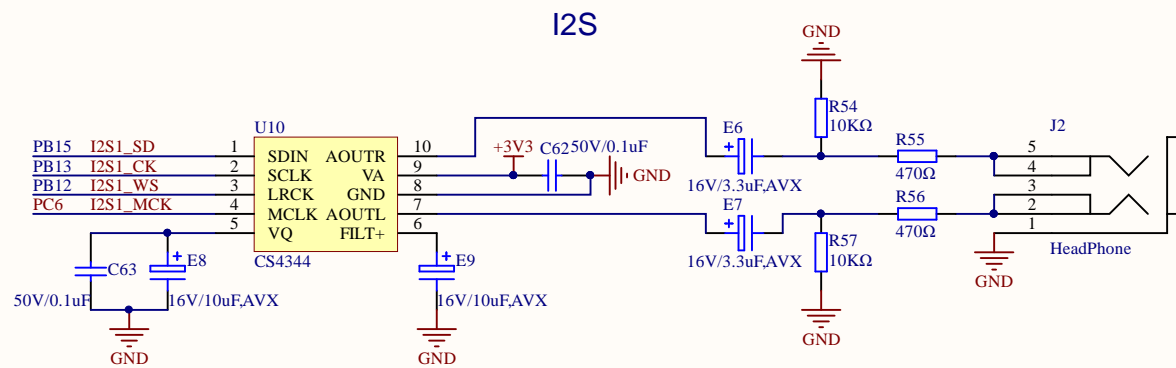
Data: 2020-6

Author: XuFei

I2C

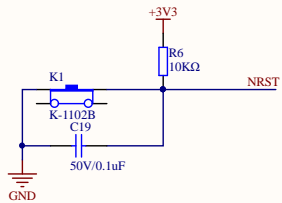
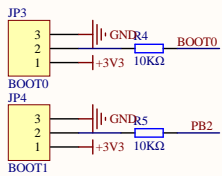
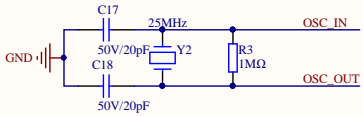
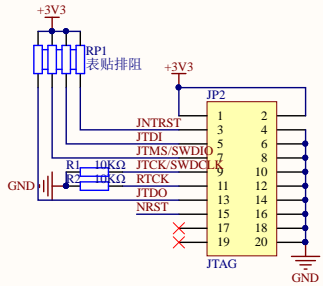


Company Name: GigaDevice		
File Name: I2C		
Revision: 1.0	Data: 2020-3	Author: XuFei



Company Name: GigaDevice		
File Name: I2S		
Revision: 1.1	Data: 2020-3	Author: Xufei

GDLink JTAG PB4
JNTRST PA15
L TDI JTDL PA13
L TMS/IO JTMS/SWDIO PA13
L TCK/CLK JTCK/SWDCLK PA14
L TDO/SWO JTDO PB3
L TRreset NRST



PA0 34 PA0
PA1 35 PA1
PA2 36 PA2
PA3 37 PA3
PA4 40 PA4
PA5 41 PA5
PA6 42 PA6
PA7 43 PA7
PA8 100 PA8
PA9 101 PA9
PA10 102 PA10
PA11 103 PA11
PA12 104 PA12
PA13 105 PA13/JTMS-SWDIO
PA14 109 PA14/JTCK-SWCLK
PA15 110 PA15

PB0 46 PB0
PB1 47 PB1
PB2 48 PB2
PB3 133 PB3
PB4 134 PB4
PB5 135 PB5
PB6 136 PB6
PB7 137 PB7
PB8 139 PB8
PB9 140 PB9
PB10 69 PB10
PB11 70 PB11
PB12 73 PB12
PB13 74 PB13
PB14 75 PB14
PB15 76 PB15

PC0 26 PC0
PC1 27 PC1
PC2 28 PC2
PC3 29 PC3
PC4 44 PC4
PC5 45 PC5
PC6 96 PC6
PC7 97 PC7
PC8 98 PC8
PC9 99 PC9
PC10 111 PC10
PC11 112 PC11
PC12 113 PC12
PC13 7 PC13
PC14 8 PC14
PC15 9 PC15

PD0 114 PD0
PD1 115 PD1
PD2 116 PD2
PD3 117 PD3
PD4 118 PD4
PD5 119 PD5
PD6 122 PD6
PD7 123 PD7
PD8 77 PD8
PD9 78 PD9
PD10 79 PD10
PD11 80 PD11
PD12 81 PD12
PD13 82 PD13
PD14 85 PD14
PD15 86 PD15

OSC_IN
OSC_OUT
NRST

NC
BOOT0
VBAT

VSS_1 71
VDD_1 72
VSS_2 107
VDD_2 108
VSS_3 143
VDD_3 144
VSS_4 38
VDD_4 39
VSS_5 16
VDD_5 17
VSS_6 81
VDD_6 82

U1 GD32E507ZET6

PG0 56 PG0
PG1 57 PG1
PG2 87 PG2
PG3 88 PG3
PG4 89 PG4
PG5 91 PG5
PG6 92 PG6
PG7 93 PG7
PG8 93 PG8
PG9 124 PG9
PG10 125 PG10
PG11 126 PG11
PG12 127 PG12
PG13 128 PG13
PG14 129 PG14
PG15 132 PG15

PF0 10 PF0
PF1 11 PF1
PF2 12 PF2
PF3 13 PF3
PF4 14 PF4
PF5 15 PF5
PF6 18 PF6
PF7 19 PF7
PF8 20 PF8
PF9 21 PF9
PF10 22 PF10
PF11 49 PF11
PF12 50 PF12
PF13 53 PF13
PF14 54 PF14
PF15 55 PF15

PE0 141 PE0
PE1 142 PE1
PE2 1 PE2
PE3 2 PE3
PE4 3 PE4
PE5 4 PE5
PE6 5 PE6
PE7 58 PE7
PE8 59 PE8
PE9 60 PE9
PE10 63 PE10
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PE13 66 PE13
PE14 67 PE14
PE15 68 PE15

VSS_7 61
VDD_7 62

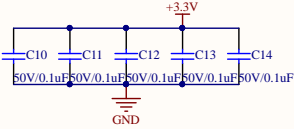
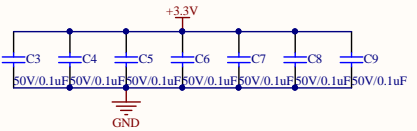
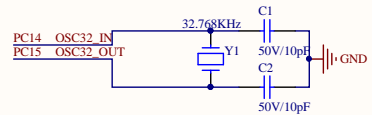
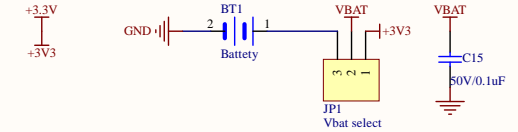
VSS_8 83
VDD_8 84

VSS_9 94
VDD_9 95

VSS_10 120
VDD_10 121

VSS_11 130
VDD_11 131

VSSA 30
VREF- 31
VREF+ 32
VDDA 33



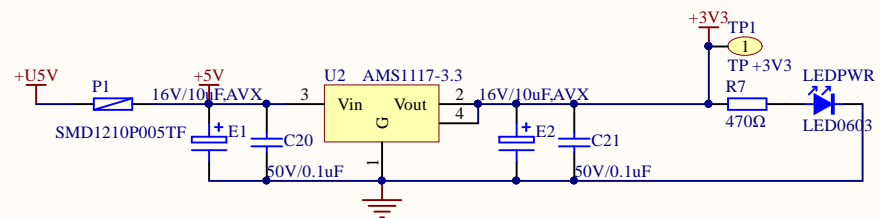
Company Name: GigaDevice

File Name: MCU

Revision: 1.0

Date: 2020-5

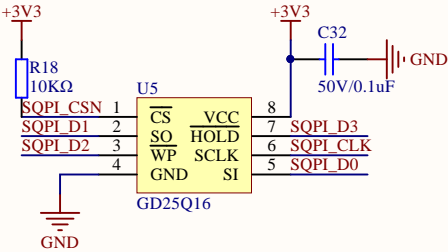
Author: XuFei



Company Name: GigaDevice		
File Name: Power		
Revision: 1.0	Data: 2020-3	Author: XuFei

SQPI Flash

PF0	SQPI_D0
PF2	SQPI_D2
PF4	SQPI_D1
PF6	SQPI_CS
PF8	SQPI_CLK
PF10	SQPI_D3



Company Name: GigaDevice

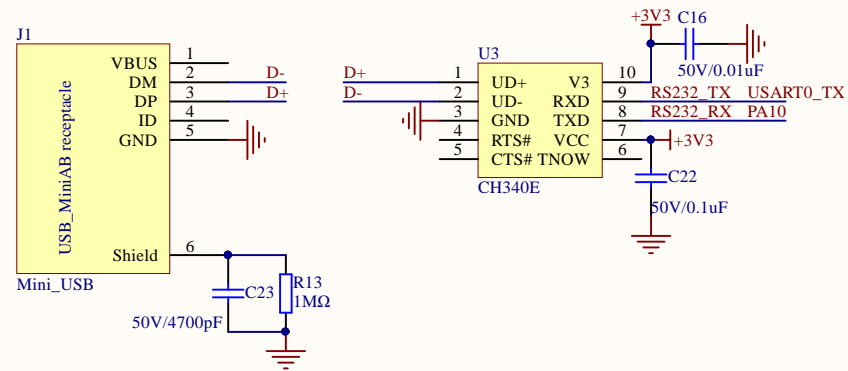
File Name: SQPI

Revision: 1.0

Data: 2020-3

Author: XuFei

USART0 To USB



Company Name: GigaDevice

File Name: USART

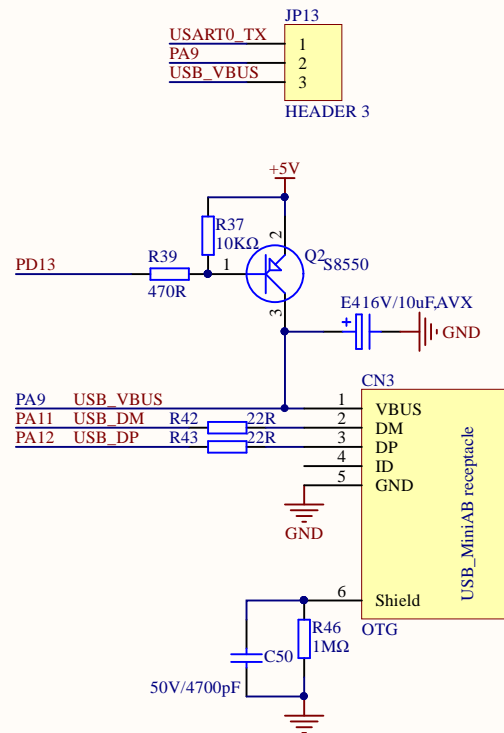
Revision: 1.0

Data: 2020-3

Author: XuFei

USB

Short JP13(1,2)for USART0 function
Short JP13(2,3)for USB function



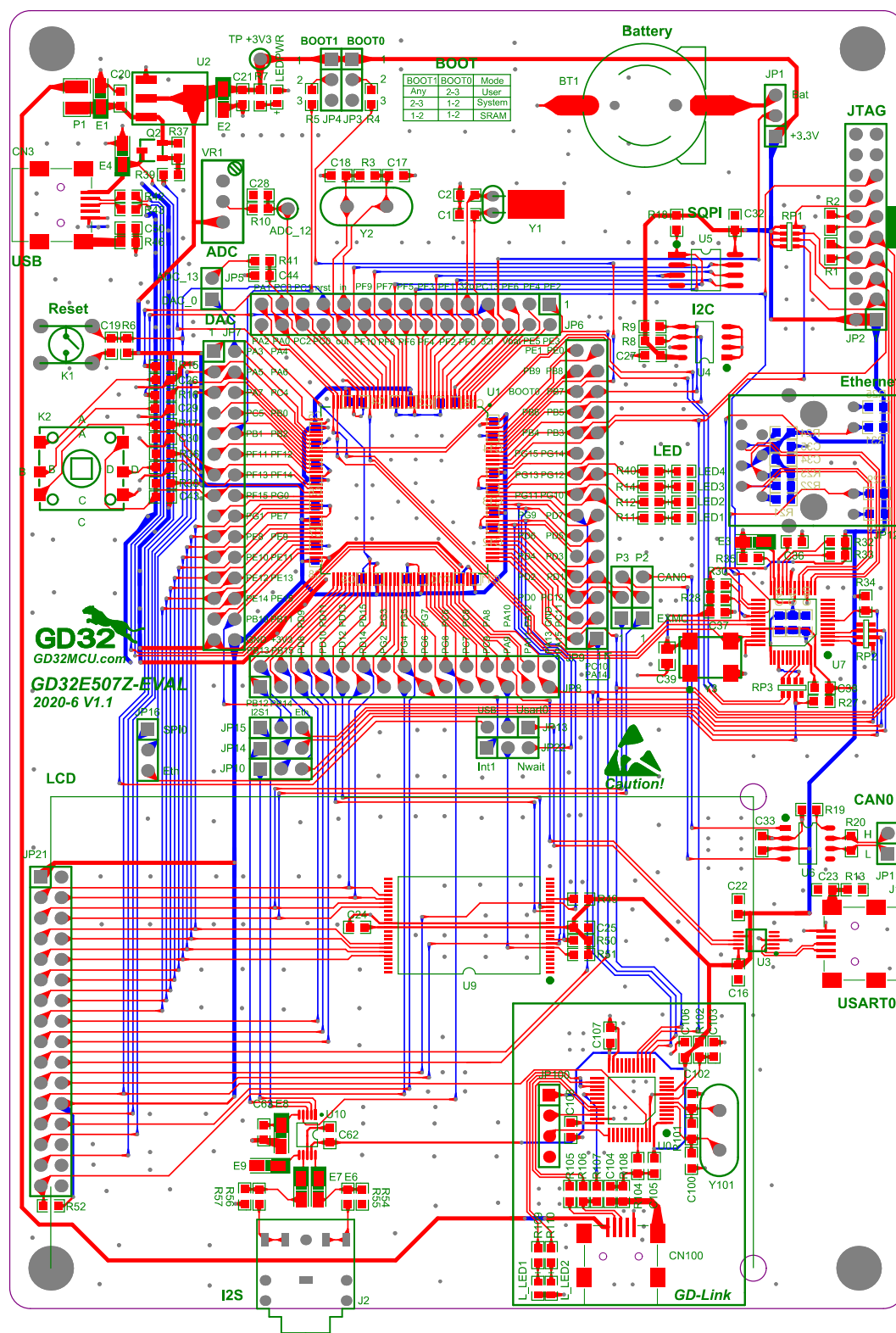
Company Name: GigaDevice

File Name: USB

Revision: 1.1

Data: 2020-3

Author: Xufei



GD32
GD32MCU.com

GD32E507Z-EVAL
2020-6 V1.1



GD-Link