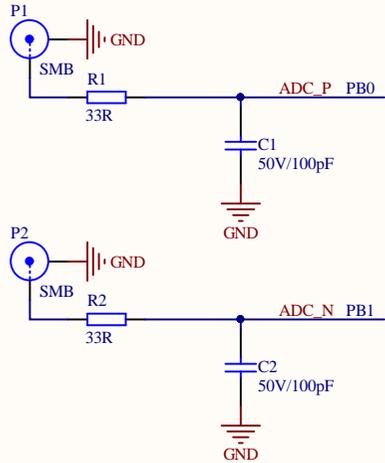
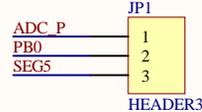


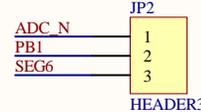
Differential ADC



Short JP1(1,2)for ADC function
Short JP1(2,3)for SLCD function



Short JP2(1,2)for ADC function
Short JP2(2,3)for SLCD function



Company Name: GigaDevice

File Name: ADC

Revision: 1.0

Data: 2023-5-22

Author: peng.zhang

1

2

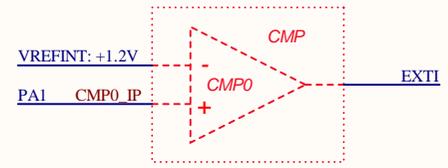
3

4

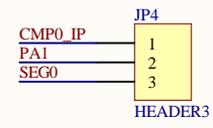
A

A

CMP



Short JP4(1,2)for CMP function
 Short JP4(2,3)for SLCD function



B

B

C

C

D

D

Company Name: GigaDevice		
File Name: CMP		
Revision:	Data: 2023-5-22	Author: peng.zhang

1

2

3

4

1

2

3

4

A

A

B

B

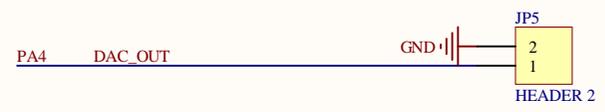
C

C

D

D

DAC



Company Name: GigaDevice

File Name: DAC

Revision: 1.0

Data: 2023-5-22

Author: peng.zhang

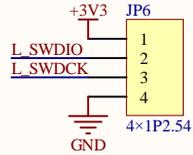
1

2

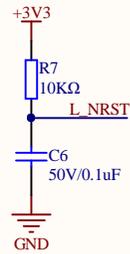
3

4

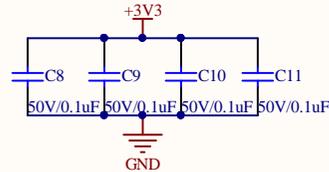
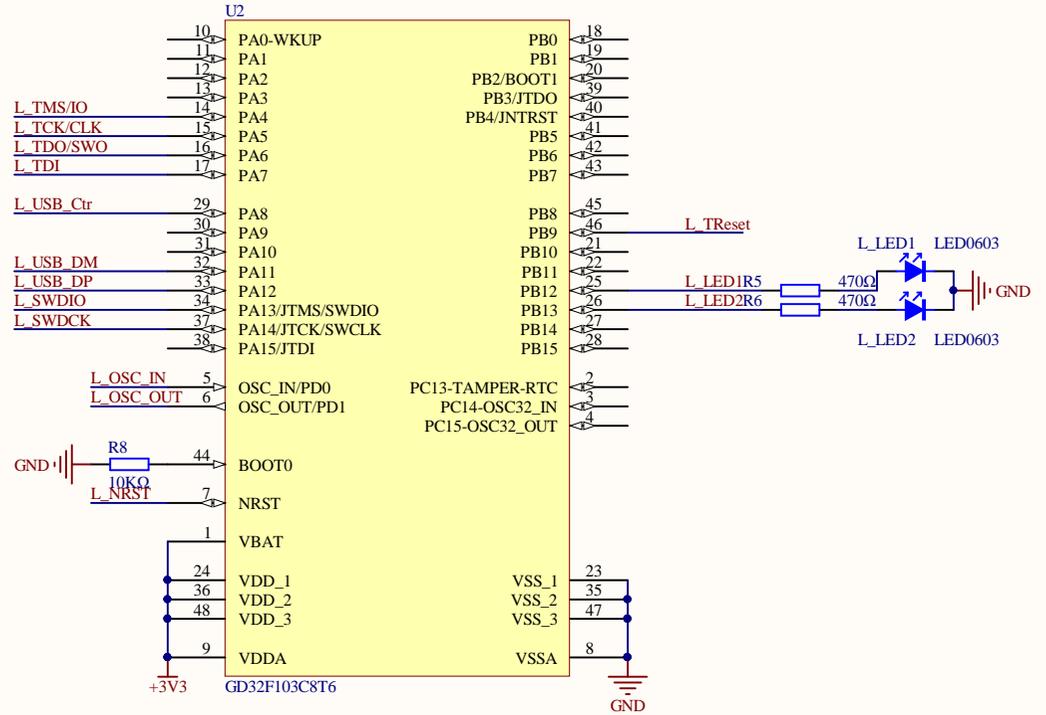
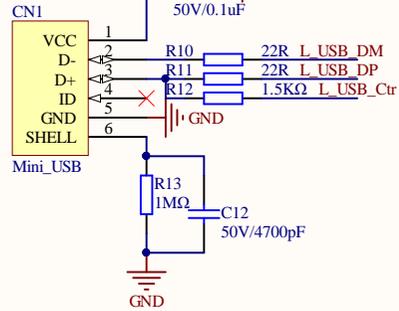
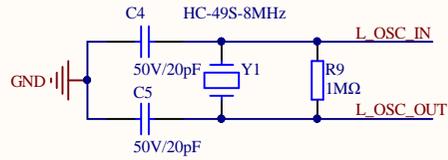
MCU SWD



Reset

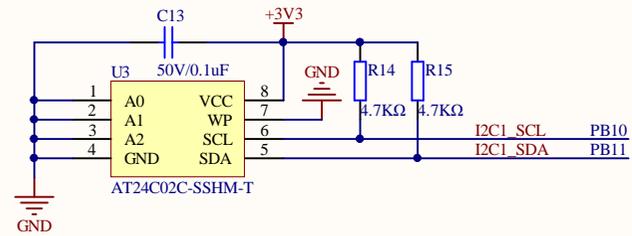


HXTAL

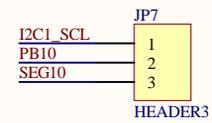


Company Name: GigaDevice		
File Name: GDLink		
Revision: 1.0	Data: 2023-5-22	Author: peng.zhang

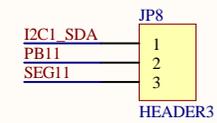
I2C



Short JP7(1,2) for I2C function
Short JP7(2,3) for SLCD function



Short JP8(1,2) for I2C function
Short JP8(2,3) for SLCD function



Company Name: GigaDevice

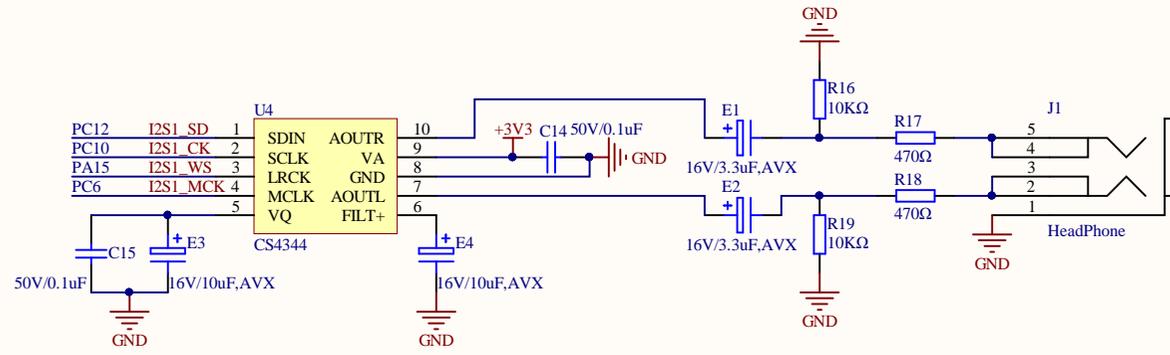
File Name: I2C

Revision: 1.0

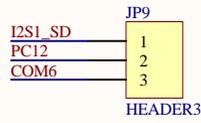
Data: 2023-5-22

Author: peng.zhang

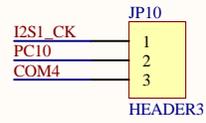
I2S



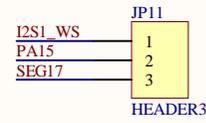
Short JP9(1,2)for I2S function
Short JP9(2,3)for SLCD function



Short JP10(1,2)for I2S function
Short JP10(2,3)for SLCD function



Short JP11(1,2)for I2S function
Short JP11(2,3)for SLCD function



Short JP12(1,2)for I2S function
Short JP12(2,3)for SLCD function



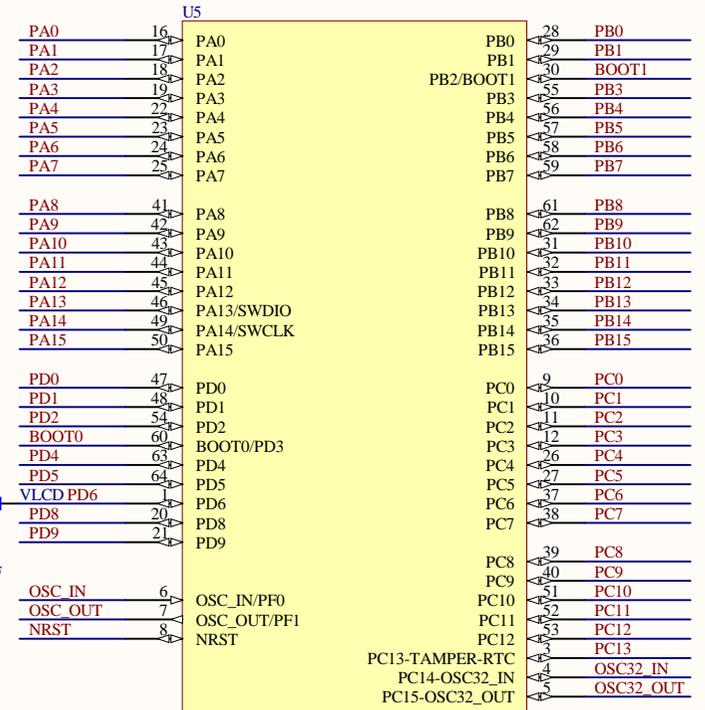
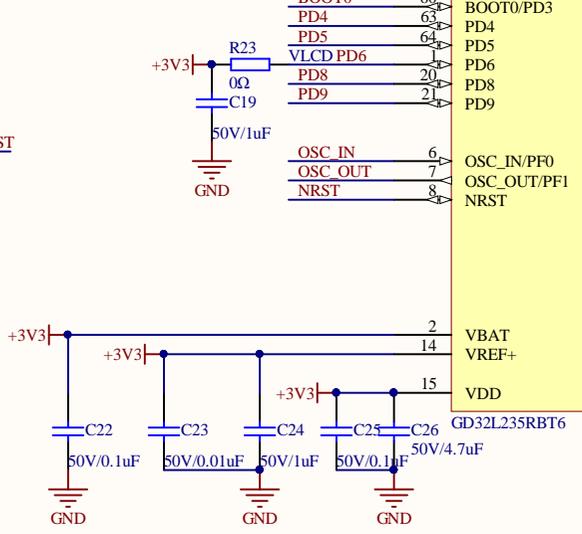
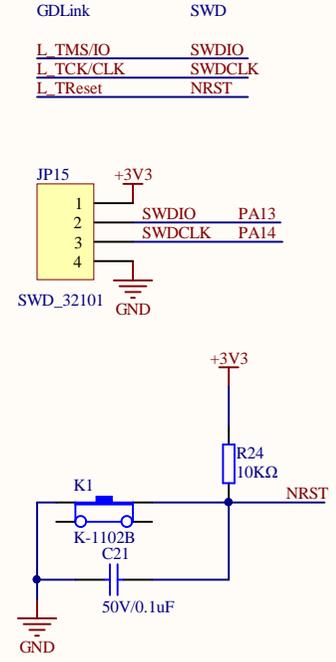
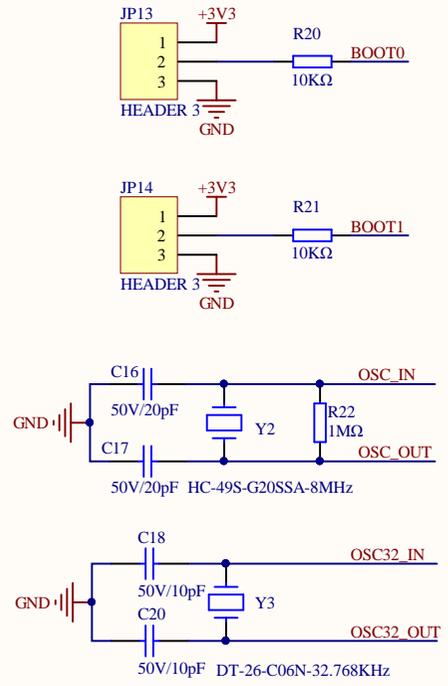
Company Name: GigaDevice

File Name: I2S

Revision: 1.0

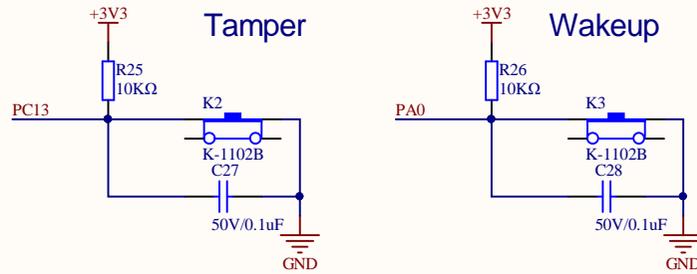
Data: 2023-5-22

Author: peng.zhang

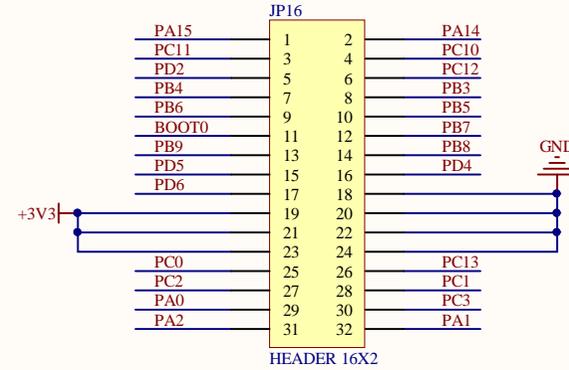


Company Name: GigaDevice		
File Name: MCU		
Revision: 1.0	Data: 2023-5-22	Author: peng.zhang

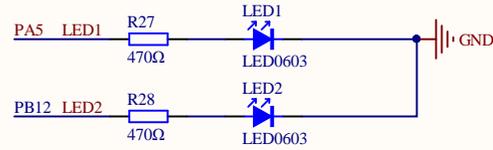
Button KEY



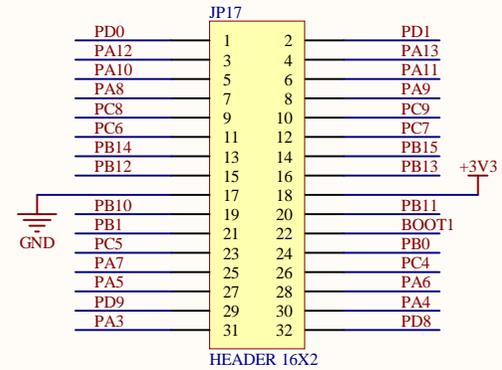
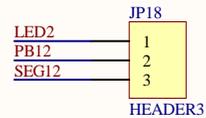
Extention



LED



Short JP18(1,2)for LED function
 Short JP18(2,3)for SLCD function



Company Name: GigaDevice

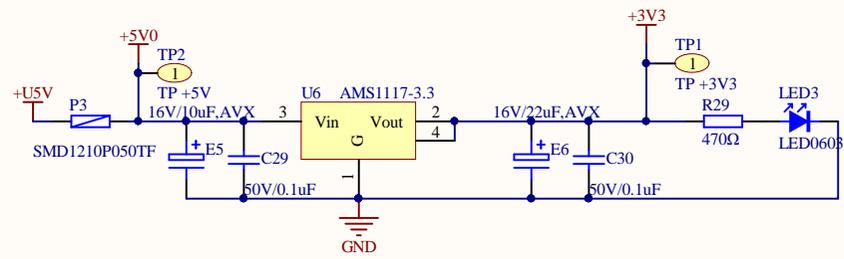
File Name: Peripherals

Revision: 1.0

Data: 2023-5-22

Author: peng.zhang

POWER



Company Name: GigaDevice

File Name: POWER

Revision: 1.0

Data: 2023-5-22

Author: peng.zhang

8COM-32SEG

PA8	COM0
PA9	COM1
PA10	COM2
PB9	COM3
PC10	COM4
PC11	COM5
PC12	COM6
PD2	COM7

PA1	SEG0
PA2	SEG1
PA3	SEG2
PA6	SEG3
PA7	SEG4
PB0	SEG5
PB1	SEG6
PB3	SEG7
PB4	SEG8
PB5	SEG9
PB10	SEG10
PB11	SEG11
PB12	SEG12
PB13	SEG13
PB14	SEG14
PB15	SEG15
PB8	SEG16
PA15	SEG17
PC0	SEG18
PC1	SEG19
PC2	SEG20
PC3	SEG21
PC4	SEG22
PC5	SEG23
PC6	SEG24
PC7	SEG25
PC8	SEG26
PC9	SEG27
PD4	SEG28
PD5	SEG29
PD8	SEG30
PD9	SEG31

PA1 are AFIOs please refer to CMP schematic for right config

PB10 are AFIOs please refer to I2C schematic for right config
 PB11 are AFIOs please refer to I2C schematic for right config

PC12 are AFIOs please refer to I2S schematic for right config
 PC10 are AFIOs please refer to I2S schematic for right config
 PA15 are AFIOs please refer to I2S schematic for right config
 PC6 are AFIOs please refer to I2S schematic for right config

PA2 are AFIOs please refer to USART schematic for right config
 PA3 are AFIOs please refer to USART schematic for right config

PA6 are AFIOs please refer to SPI schematic for right config
 PA7 are AFIOs please refer to SPI schematic for right config
 PD8 are AFIOs please refer to SPI schematic for right config
 PD9 are AFIOs please refer to SPI schematic for right config

PB0 are AFIOs please refer to ADC schematic for right config
 PB1 are AFIOs please refer to ADC schematic for right config

PB12 are AFIOs please refer to Peripherals schematic for right config

COM7	40	1	SEG28
COM6	39	2	SEG17
COM5	38	3	SEG27
COM4	37	4	SEG26
COM3	36	5	SEG25
COM2	35	6	SEG24
COM1	34	7	SEG31
COM0	33	8	SEG30
SEG16	32	9	SEG15
SEG9	31	10	SEG14
SEG8	30	11	SEG13
SEG7	29	12	SEG12
SEG29	28	13	SEG11
SEG2	27	14	SEG10
SEG1	26	15	SEG6
SEG0	25	16	SEG5
SEG21	24	17	SEG23
SEG20	23	18	SEG22
SEG19	22	19	SEG4
SEG18	21	20	SEG3

U7

XD-H2249A

Company Name: GigaDevice

File Name: SLCD

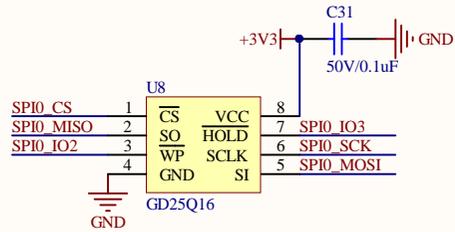
Revision: 1.0

Data: 2023-5-22

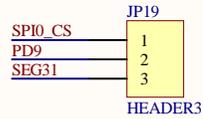
Author: peng.zhang

SPI

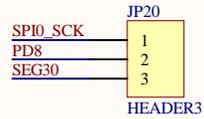
PD9	SPI0_CS
PD8	SPI0_SCK
PA6	SPI0_MISO
PA7	SPI0_MOSI
PB6	SPI0_IO2
PB7	SPI0_IO3



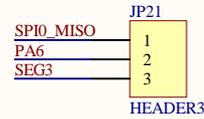
Short JP19(1,2)for SPI function
Short JP19(2,3)for SLCD function



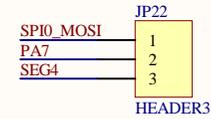
Short JP20(1,2)for SPI function
Short JP20(2,3)for SLCD function



Short JP21(1,2)for SPI function
Short JP21(2,3)for SLCD function



Short JP22(1,2)for SPI function
Short JP22(2,3)for SLCD function



Company Name: GigaDevice

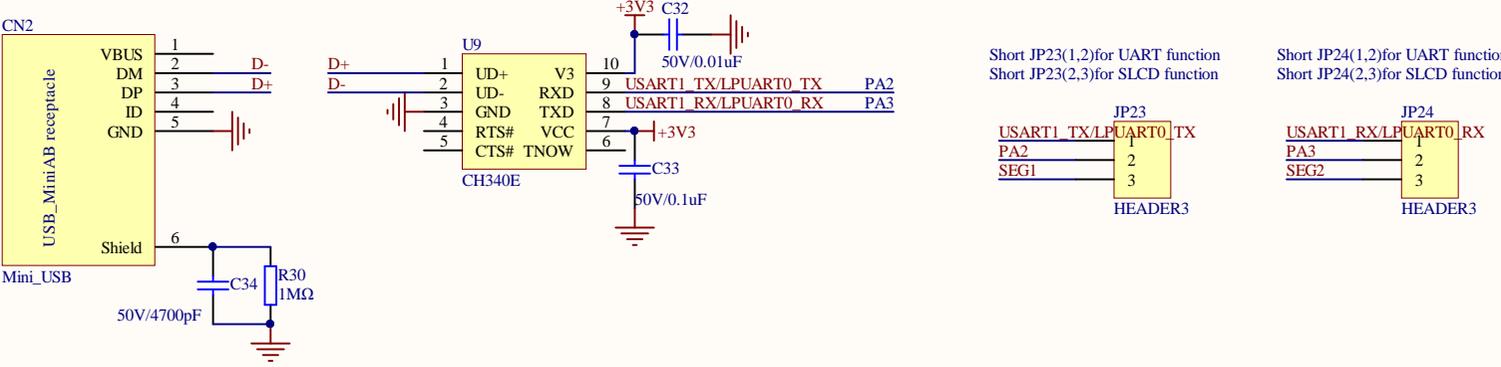
File Name: SPI

Revision: 1.0

Data: 2023-5-22

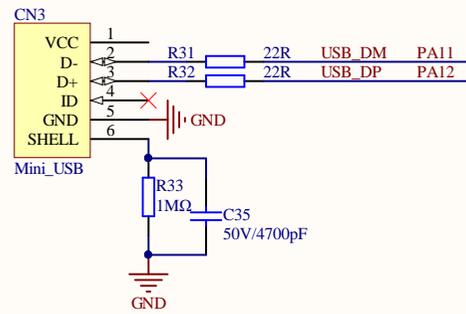
Author: peng.zhang

USART To USB

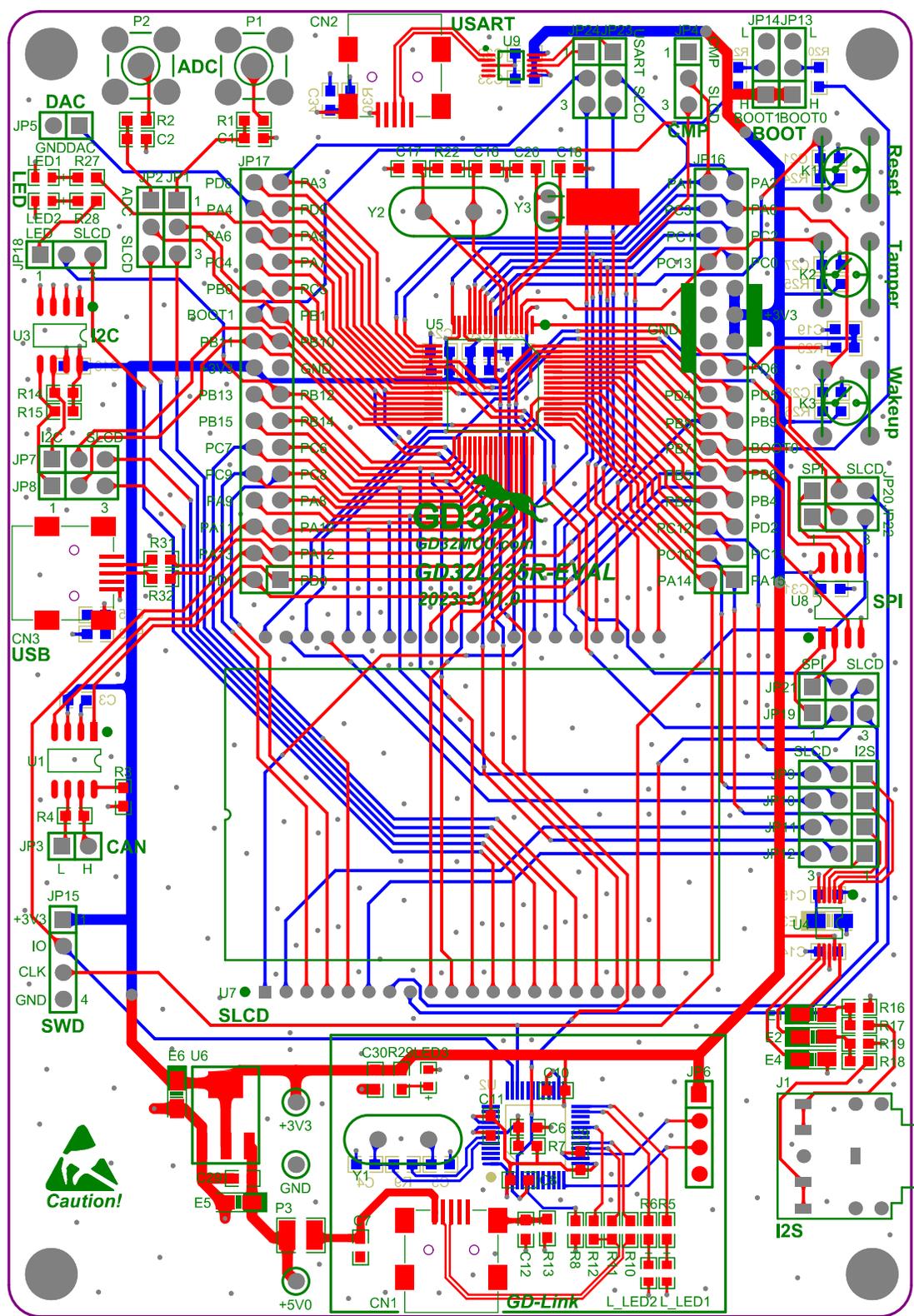


Company Name: GigaDevice		
File Name: USART/LPUART		
Revision: 1.0	Data: 2023-5-22	Author: peng.zhang

USB



Company Name: GigaDevice		
File Name: USB		
Revision: 1.0	Data: 2023-5-22	Author: peng.zhang



GD-Link