

Company Name: GigaDevice		
File Name: ADC		
Revision: 1.1	Data: 2021-7	Author: Xufei

1

2

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4

A

A

B

B

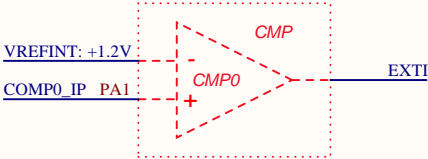
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C

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CMP



Company Name: GigaDevice

File Name: CMP

Revision: 1.1

Data: 2021-7

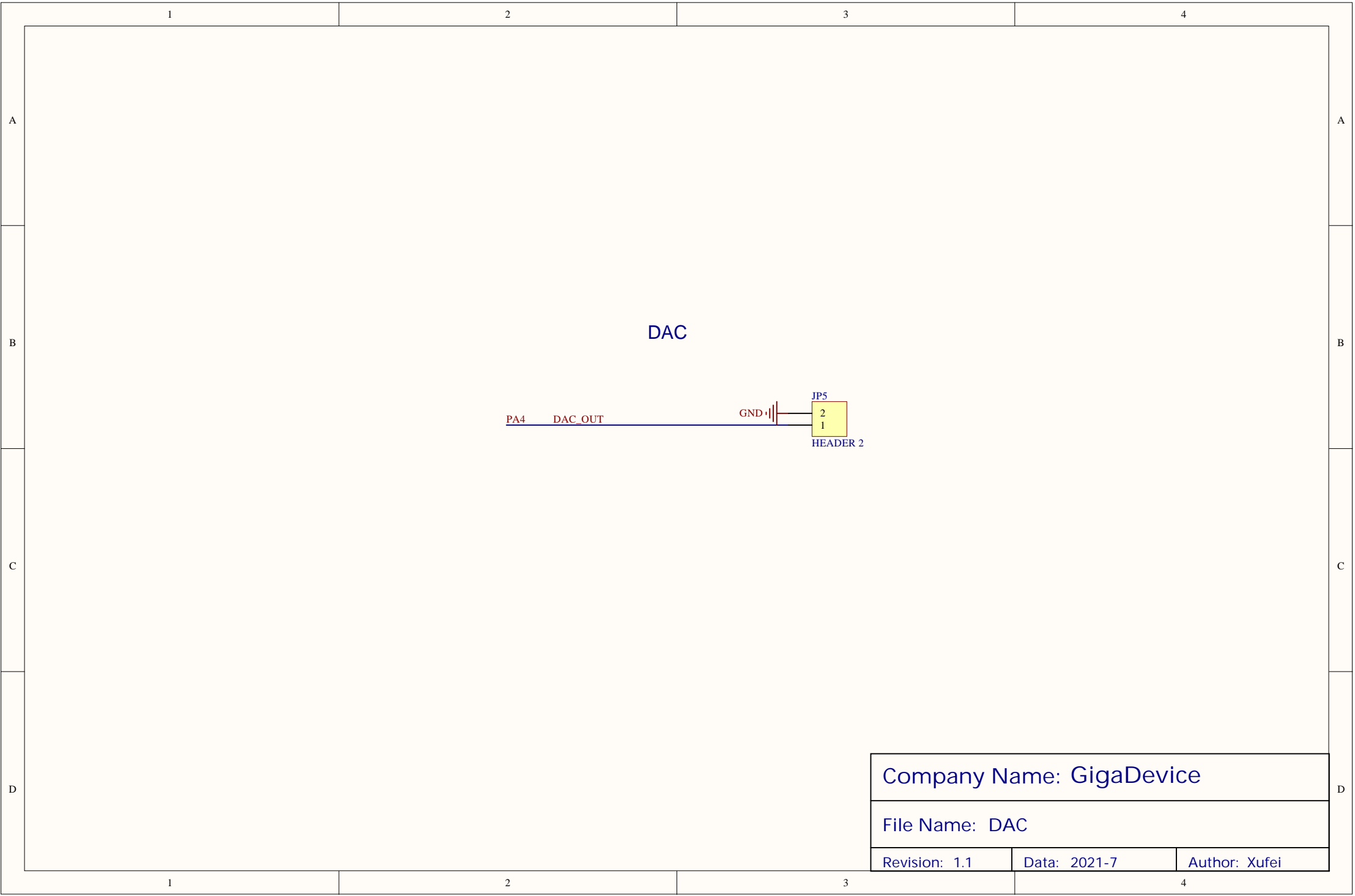
Author: Xufei

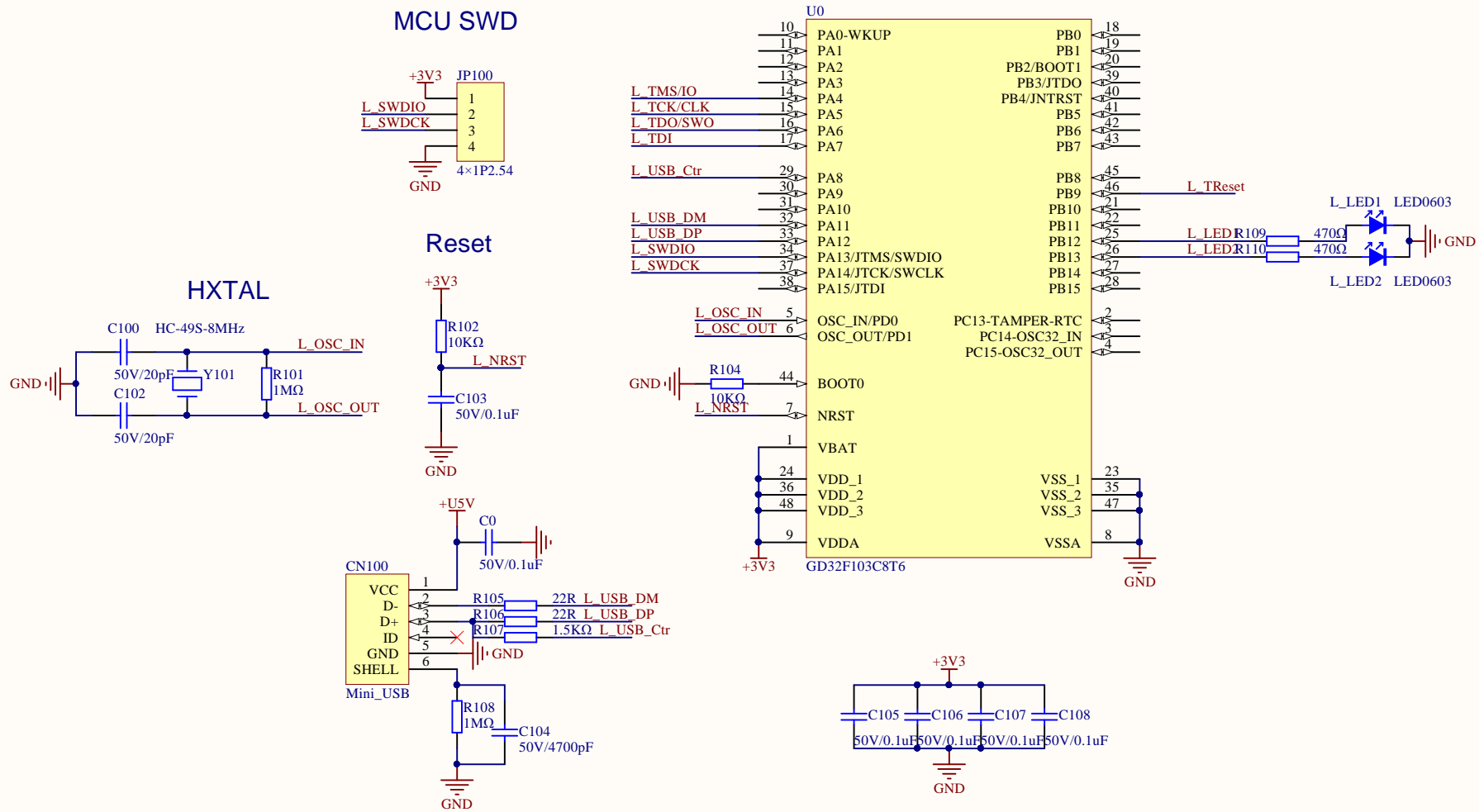
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Company Name: GigaDevice

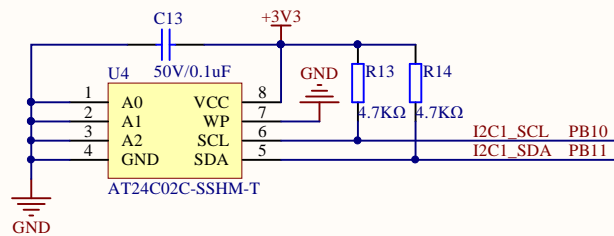
File Name: GDLink

Revision: 1.1

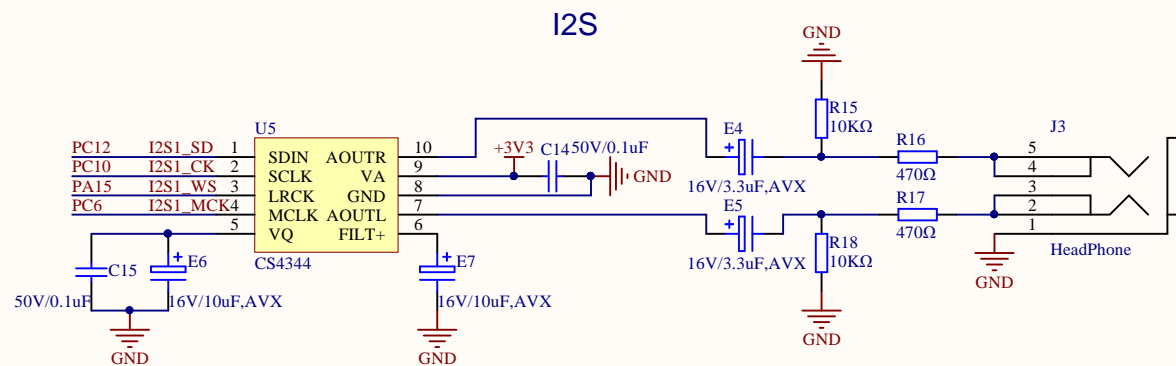
Data: 2021-7

Author: XuFei

## I2C



Company Name: GigaDevice		
File Name: I2C		
Revision: 1.1	Data: 2021-7	Author: Xufei



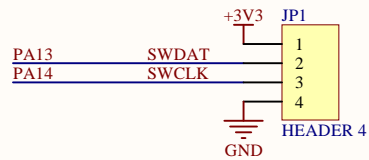
Company Name: GigaDevice

File Name: I2S

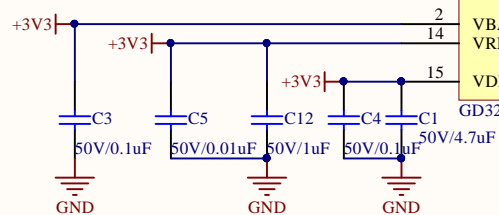
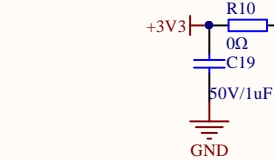
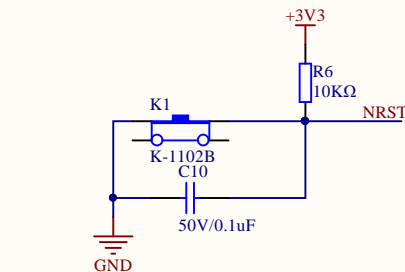
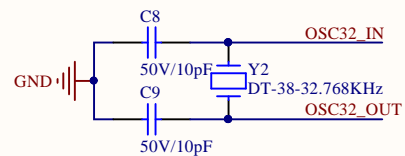
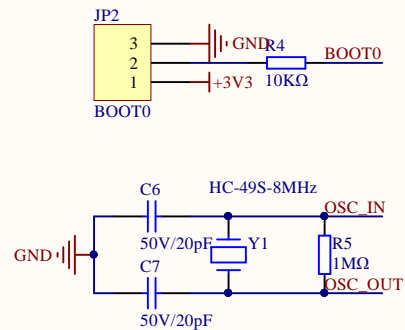
Revision: 1.1

Data: 2021-7

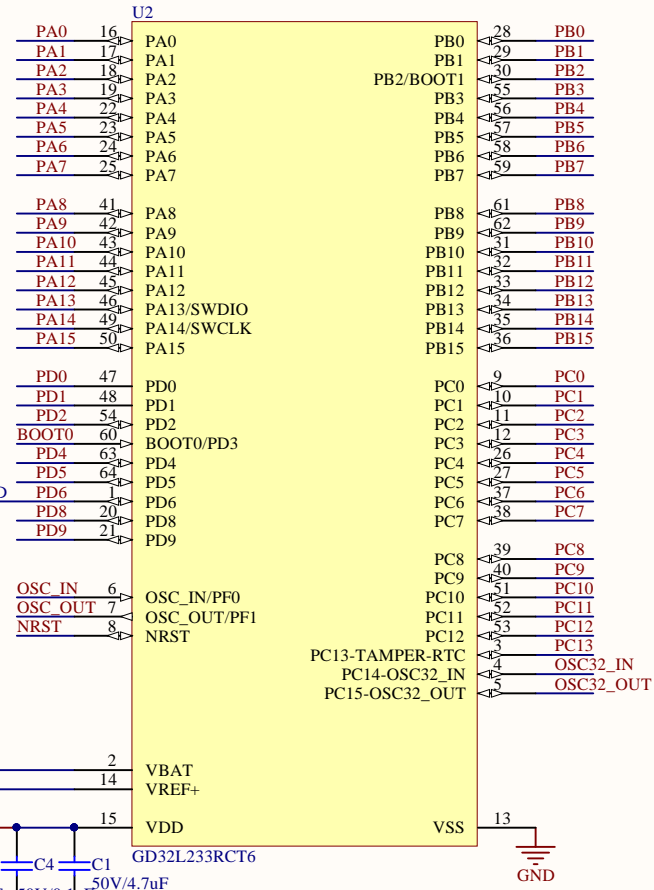
Author: Xufei



GDLink SWD  
 L\_TMS/IO SWDAT  
 L\_TCK/CLK SWCLK  
 L\_TReset NRST



## MCU



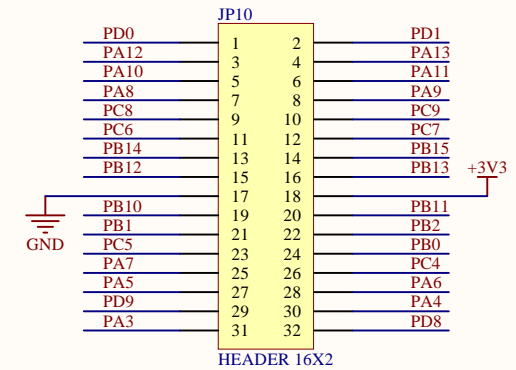
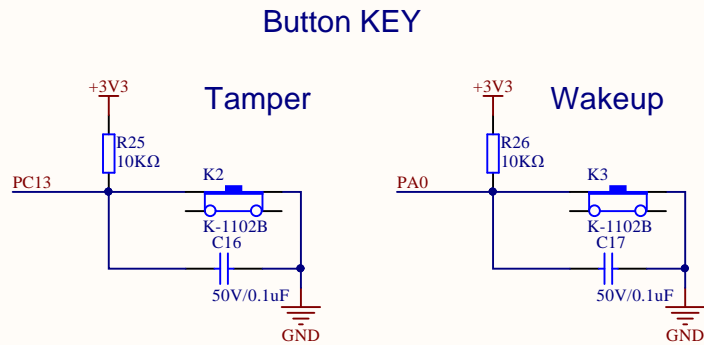
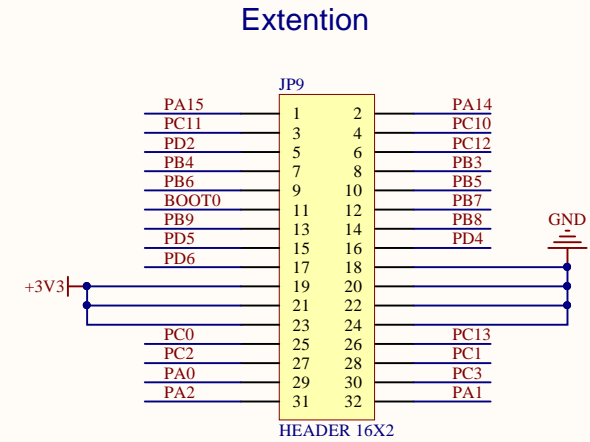
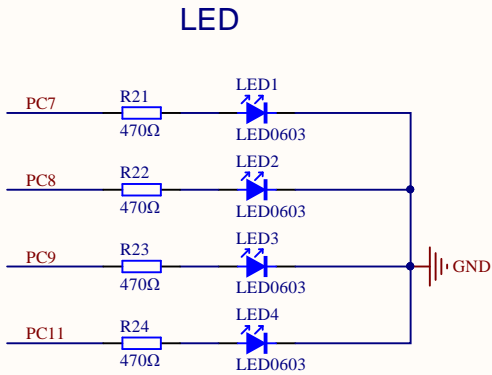
Company Name: GigaDeice

File Name: MCU

Revision: 1.1

Data: 2021-7

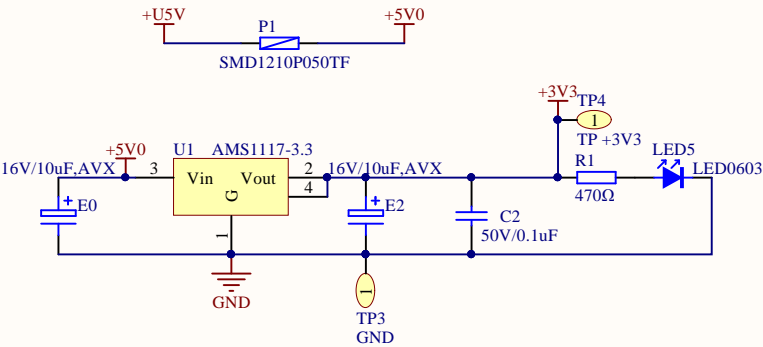
Author: Xufei



Company Name: GigaDevice		
File Name: Peripherals		
Revision: 1.1	Data: 2021-7	Author: Xufei



POWER















Company Name: GigaDevice

File Name: POWER

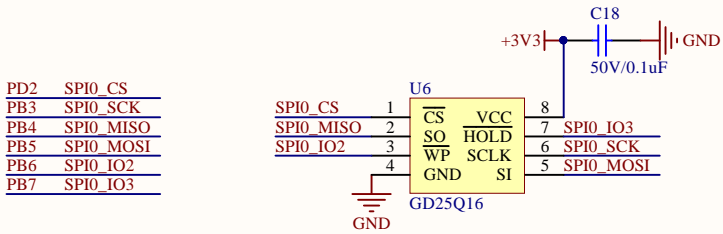
Revision: 1.1

Data: 2021-7

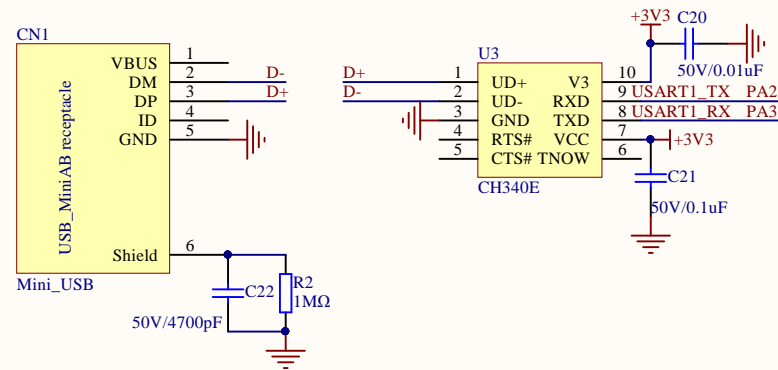
Author: Xufei

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A				A																																																																																																
B	<div>SLCD</div> <div><div><div>JP7</div><table><tr><td>PA8</td><td>COM0</td><td>R00</td><td></td><td>100Ω</td><td>1</td></tr><tr><td>PA9</td><td>COM1</td><td>R01</td><td></td><td>100Ω</td><td>2</td></tr><tr><td>PA10</td><td>COM2</td><td>R02</td><td></td><td>100Ω</td><td>3</td></tr><tr><td>PB9</td><td>COM3</td><td>R03</td><td></td><td>100Ω</td><td>4</td></tr><tr><td></td><td>PB1</td><td>SEG6</td><td></td><td></td><td>5</td></tr><tr><td></td><td>PB8</td><td>SEG16</td><td></td><td></td><td>6</td></tr><tr><td></td><td>PB12</td><td>SEG12</td><td></td><td></td><td>7</td></tr><tr><td></td><td>PB13</td><td>SEG13</td><td></td><td></td><td>8</td></tr><tr><td></td><td>PB14</td><td>SEG14</td><td></td><td></td><td>9</td></tr><tr><td></td><td>PB15</td><td>SEG15</td><td></td><td></td><td>10</td></tr><tr><td></td><td>PC0</td><td>SEG18</td><td></td><td></td><td>11</td></tr><tr><td></td><td>PC1</td><td>SEG19</td><td></td><td></td><td>12</td></tr><tr><td></td><td>PC2</td><td>SEG20</td><td></td><td></td><td>13</td></tr><tr><td></td><td>PC3</td><td>SEG21</td><td></td><td></td><td>14</td></tr><tr><td></td><td>PC4</td><td>SEG22</td><td></td><td></td><td>15</td></tr><tr><td></td><td>PC5</td><td>SEG23</td><td></td><td></td><td>16</td></tr></table></div><div>EDS826</div></div> <td>B</td>			PA8	COM0	R00		100Ω	1	PA9	COM1	R01		100Ω	2	PA10	COM2	R02		100Ω	3	PB9	COM3	R03		100Ω	4		PB1	SEG6			5		PB8	SEG16			6		PB12	SEG12			7		PB13	SEG13			8		PB14	SEG14			9		PB15	SEG15			10		PC0	SEG18			11		PC1	SEG19			12		PC2	SEG20			13		PC3	SEG21			14		PC4	SEG22			15		PC5	SEG23			16	B
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QSPI



## USART To USB



Company Name: GigaDevice

File Name: USART/LPUART

Revision: 1.1

Data: 2021-7

Author: Xufei

