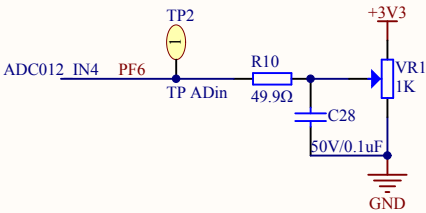
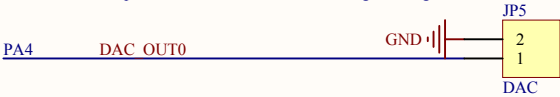


ADC



DAC

PA4 is an AFIO, please refer to TLI Schematic for right config



Company Name: GigaDevice

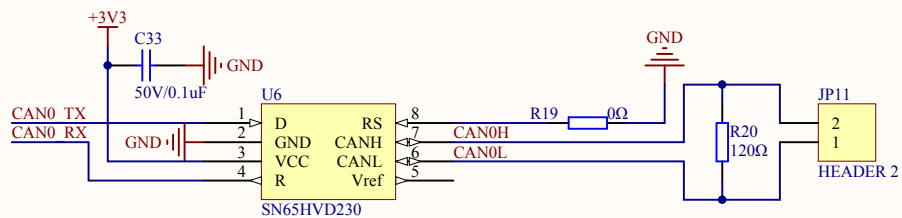
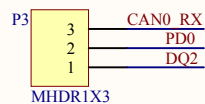
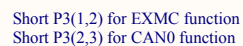
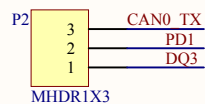
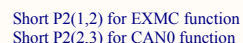
File Name: AD_DA

Revision: 1.0

Data: 2016-7

Author: XuFei

CAN



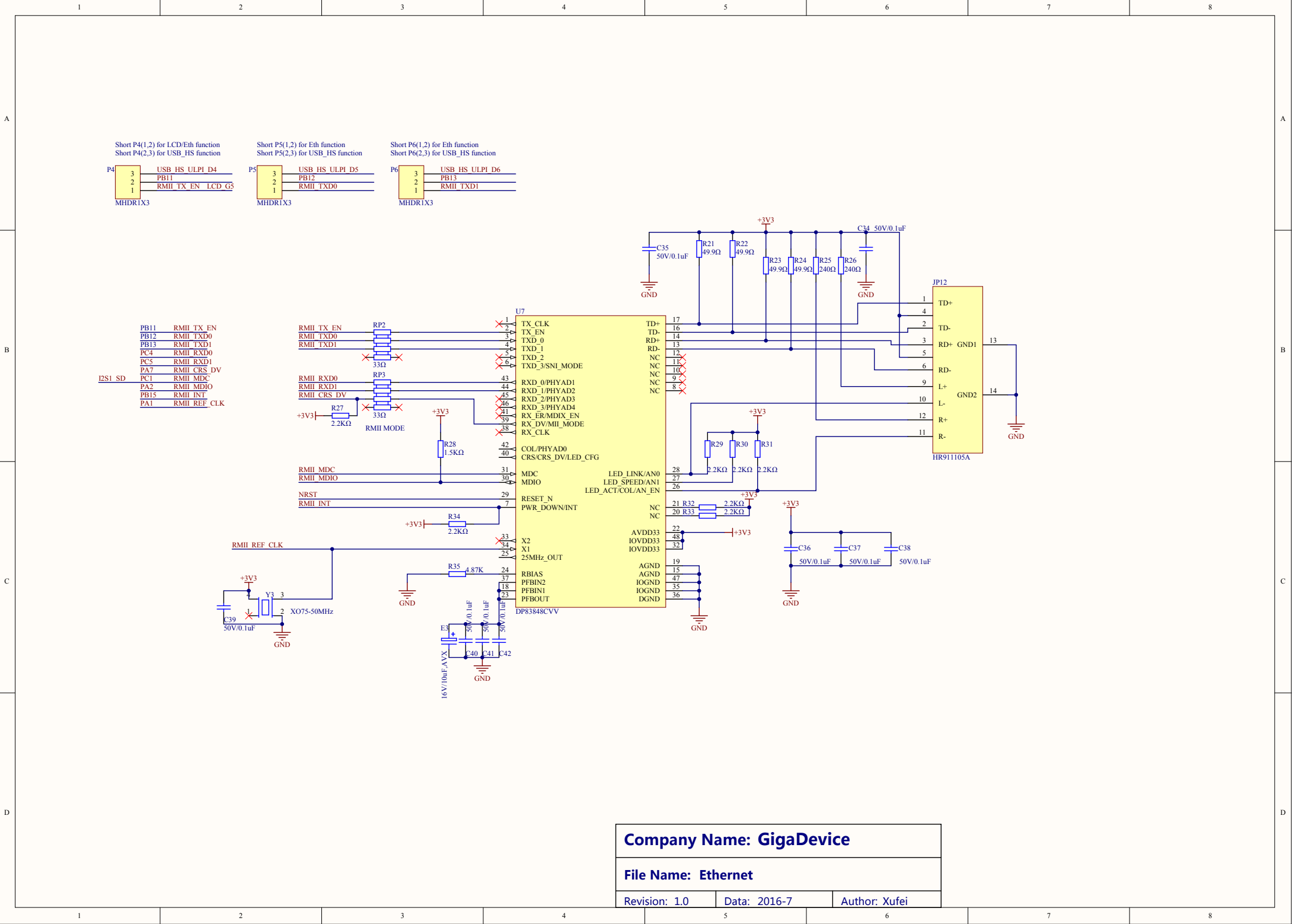
Company Name: GigaDevice

File Name: CAN

Revision: 1.0

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| Data: 2016-7 |
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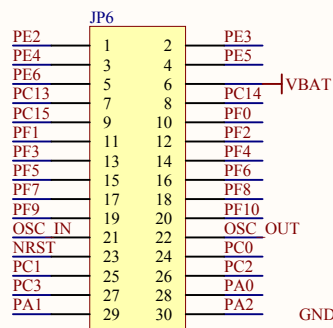
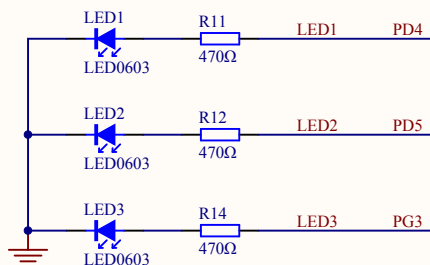
Author: XuFei



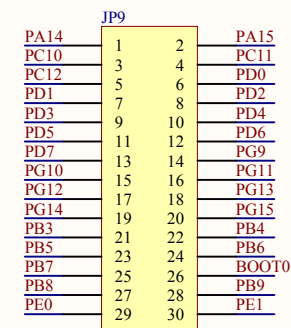
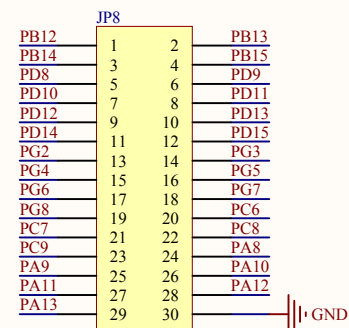
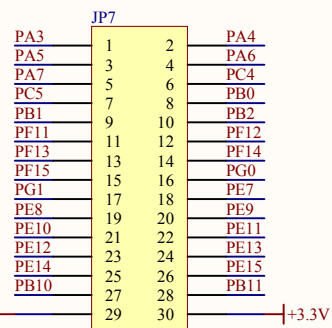
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| Company Name: GigaDevice | | |
| File Name: Ethernet | | |
| Revision: 1.0 | Data: 2016-7 | Author: Xufei |

LED

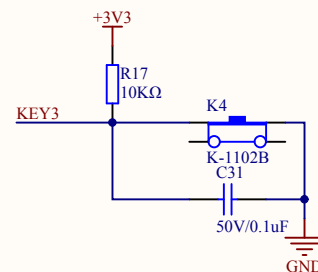
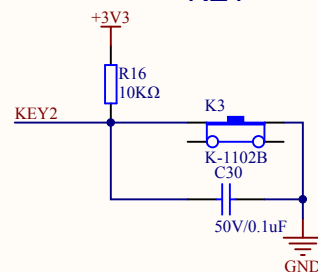
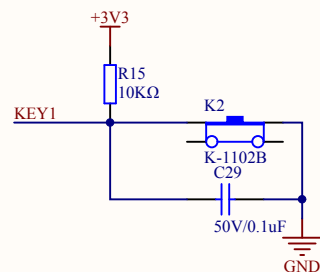
Extension Pin



GND



KEY



PA0 KEY1
PC13 KEY2
PB14 KEY3

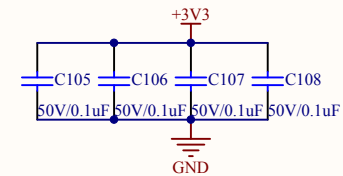
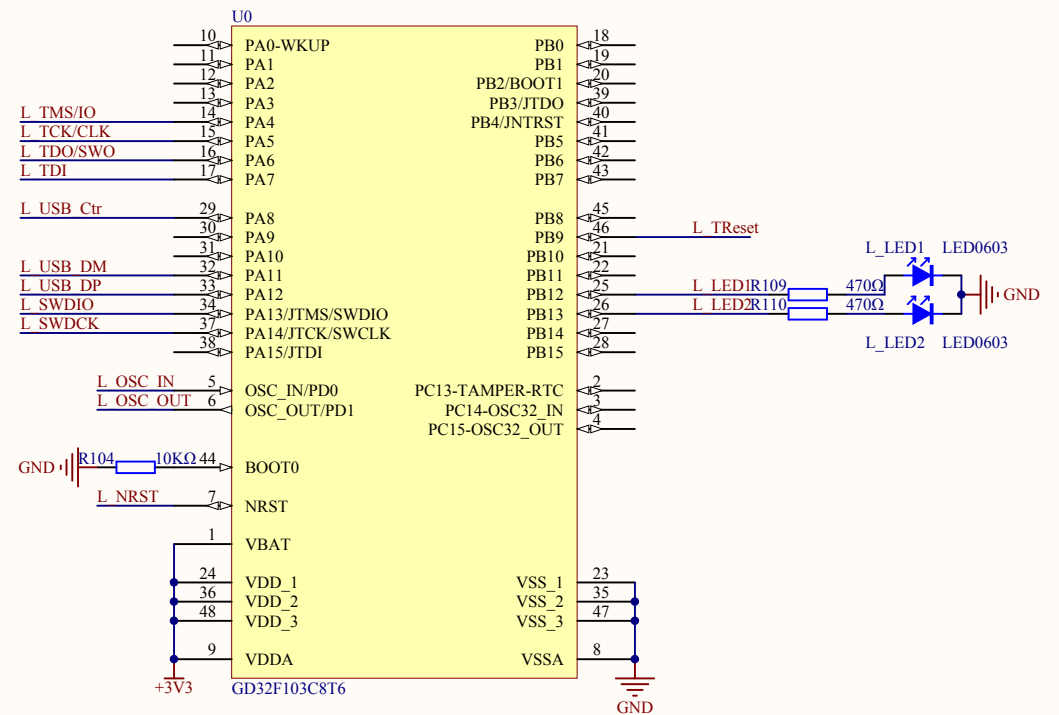
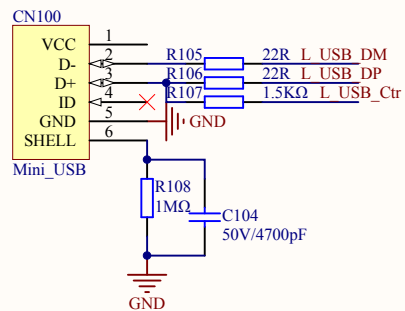
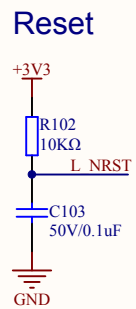
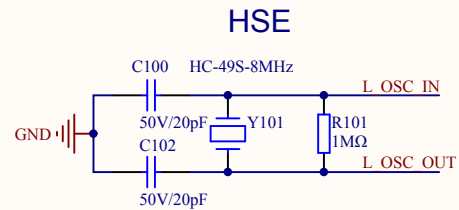
Company Name: GigaDevice

File Name: Extension

Revision: 1.0

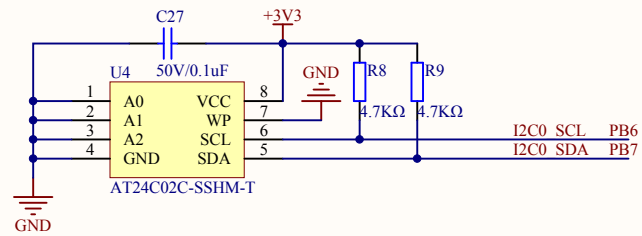
Data: 2016-7

Author: XuFei

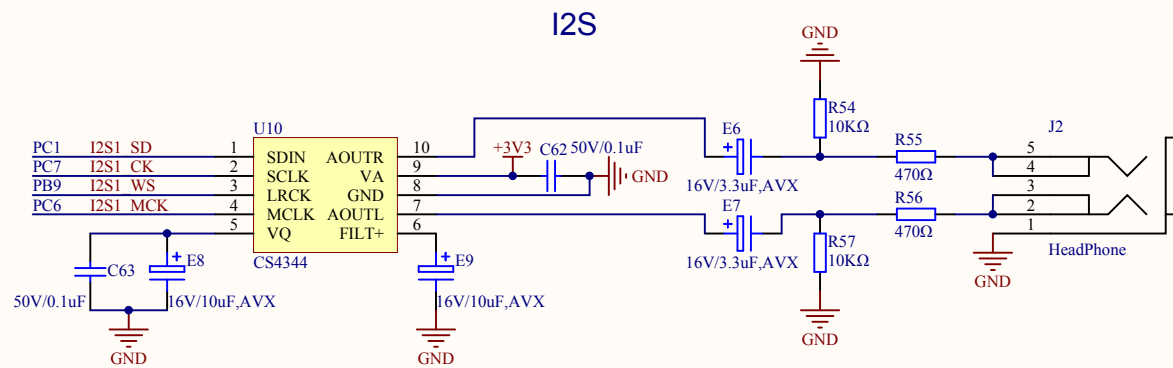


Author: XuFei

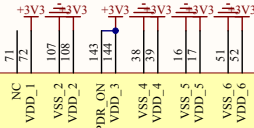
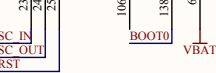
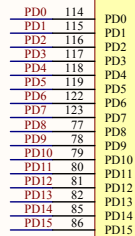
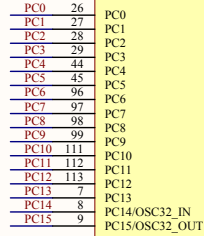
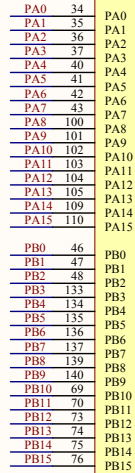
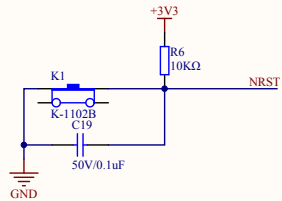
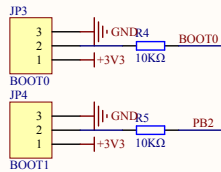
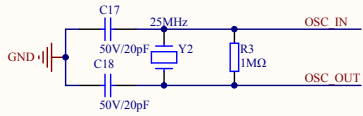
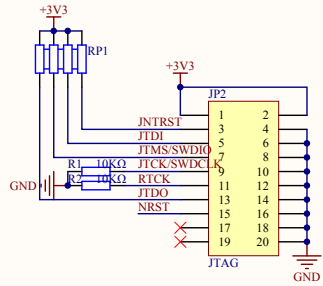
I2C



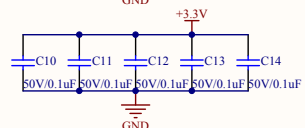
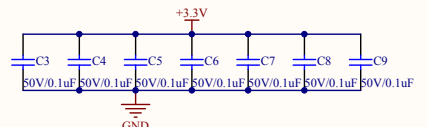
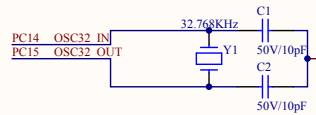
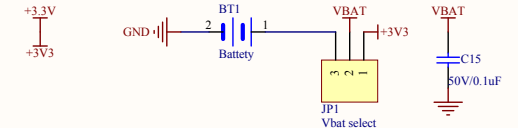
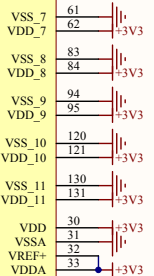
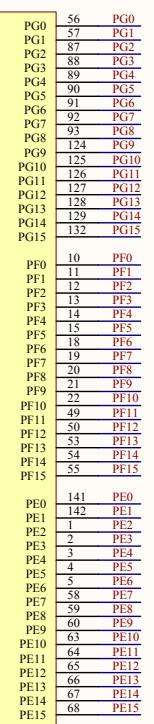
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| Company Name: GigaDevice | | |
| File Name: I2C | | |
| Revision: 1.0 | Data: 2016-7 | Author: XuFei |



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| Company Name: GigaDevice | | |
| File Name: I2S | | |
| Revision: 1.1 | Data: 2016-9 | Author: Xufei |



U1 GD32F4xxZxT6



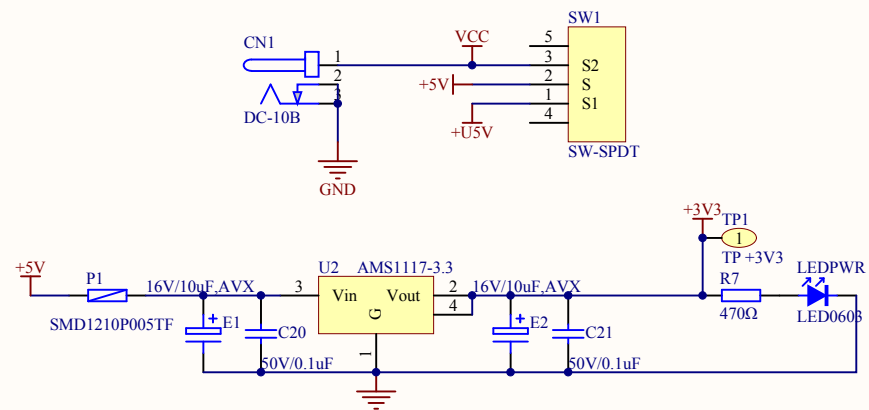
Company Name: GigaDevice

File Name: MCU

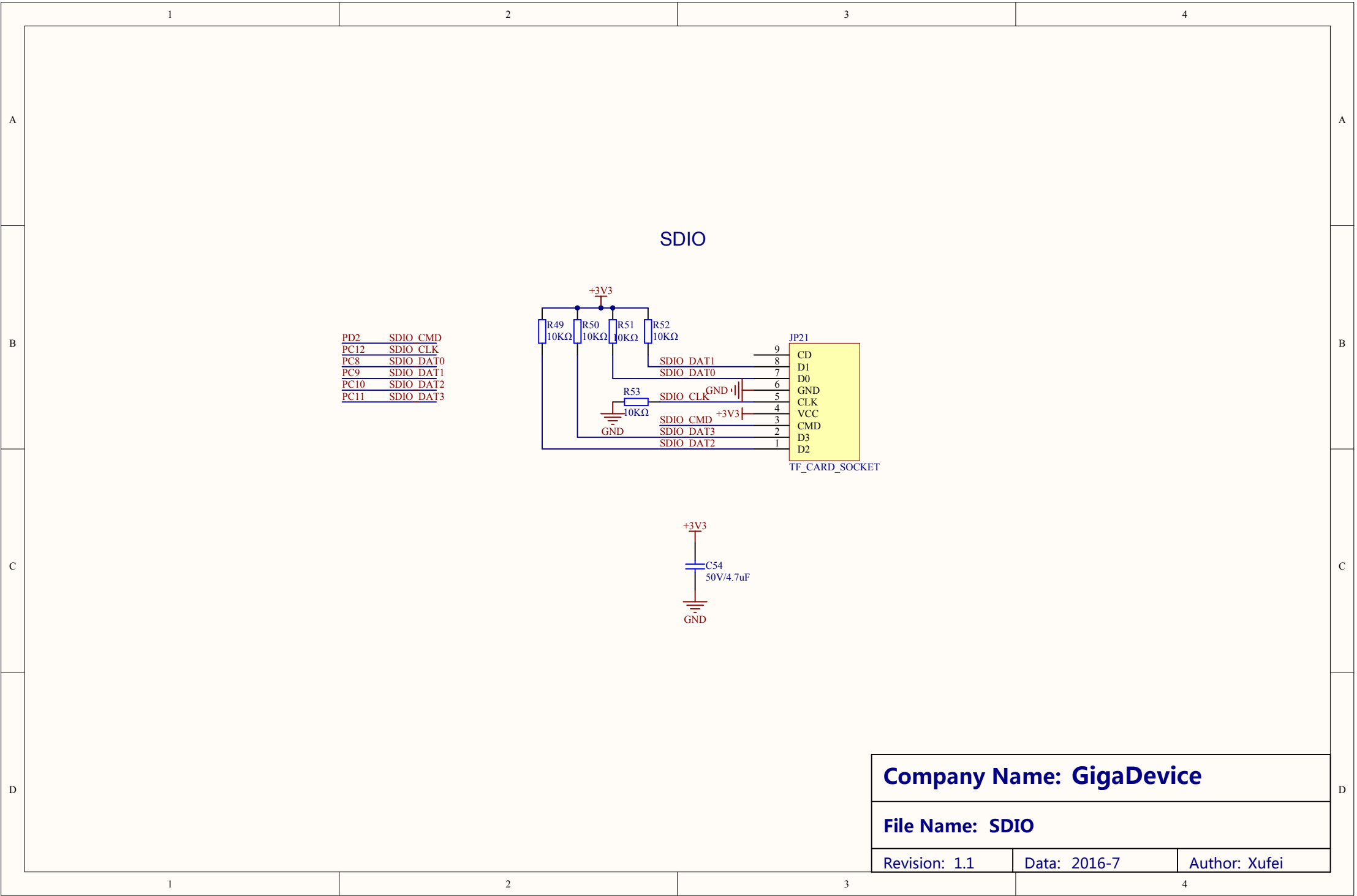
Revision: 1.0

Date: 2016-7

Author: XuFei



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| Company Name: GigaDevice | | |
| File Name: Power | | |
| Revision: 1.0 | Data: 2016-7 | Author: XuFei |

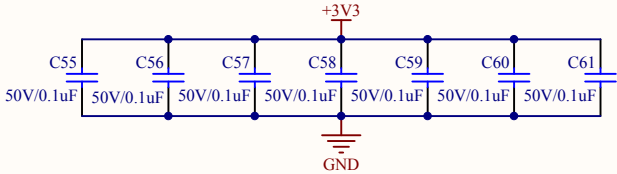
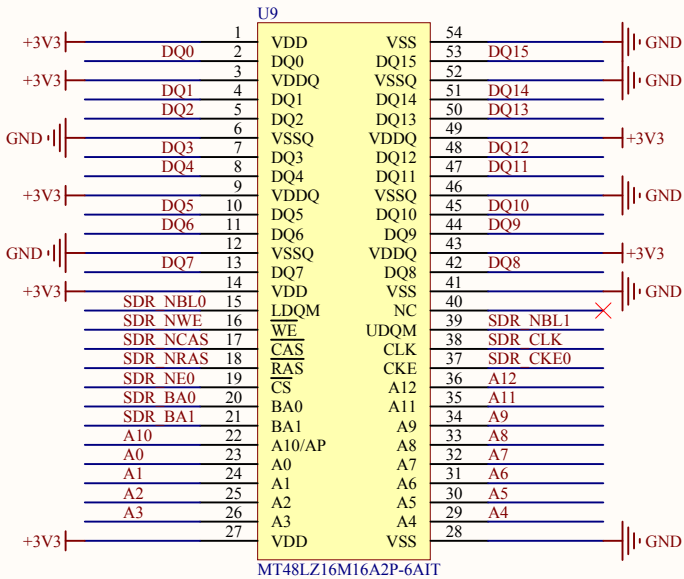


SDRAM

| | |
|------|-----|
| PF0 | A0 |
| PF1 | A1 |
| PF2 | A2 |
| PF3 | A3 |
| PF4 | A4 |
| PF5 | A5 |
| PF12 | A6 |
| PF13 | A7 |
| PF14 | A8 |
| PF15 | A9 |
| PG0 | A10 |
| PG1 | A11 |
| PG2 | A12 |

| | |
|------|------|
| PD14 | DQ0 |
| PD15 | DQ1 |
| PD0 | DQ2 |
| PD1 | DQ3 |
| PE7 | DQ4 |
| PE8 | DQ5 |
| PE9 | DQ6 |
| PE10 | DQ7 |
| PE11 | DQ8 |
| PE12 | DQ9 |
| PE13 | DQ10 |
| PE14 | DQ11 |
| PE15 | DQ12 |
| PD8 | DQ13 |
| PD9 | DQ14 |
| PD10 | DQ15 |

| | |
|------|----------|
| PE0 | SDR NBL0 |
| PE1 | SDR NBL1 |
| PC3 | SDR CKE0 |
| PG4 | SDR BA0 |
| PG5 | SDR BA1 |
| PG8 | SDR CLK |
| PG15 | SDR NCAS |
| PF11 | SDR NRAS |
| PC2 | SDR NE0 |
| PC0 | SDR NWE |



Company Name: GigaDevice

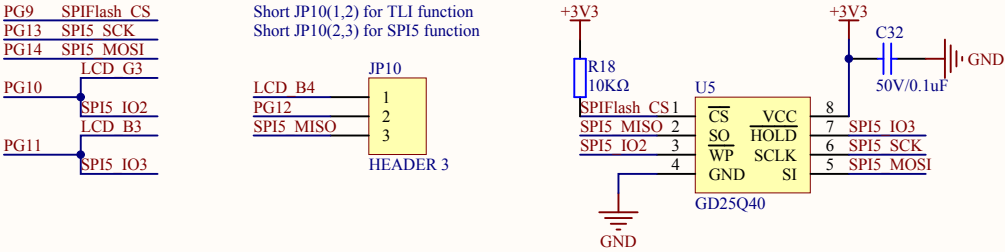
File Name: SDRAM

Revision: 1.0

Data: 2016-7

Author: Xufei

SPI Flash



Company Name: GigaDevice

File Name: SPI

Revision: 1.0

Data: 2016-7

Author: XuFei

A



C

D

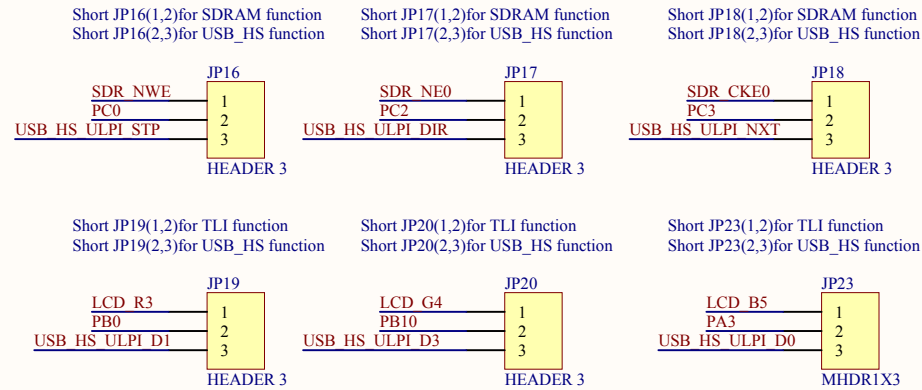
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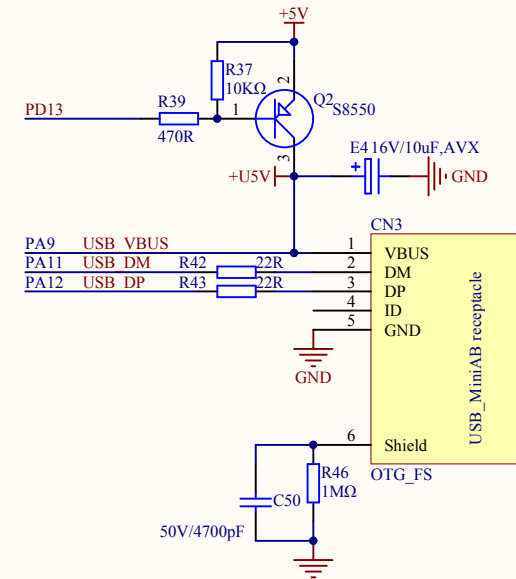
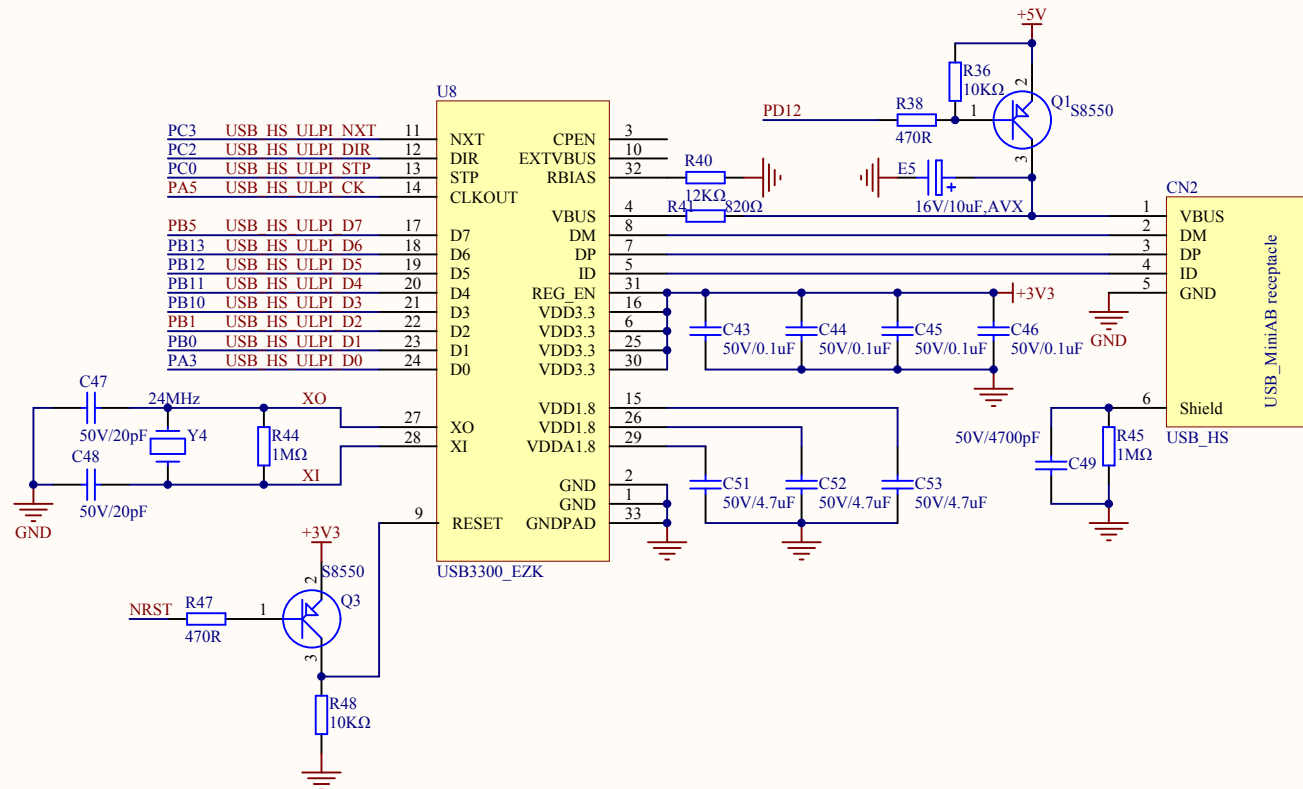
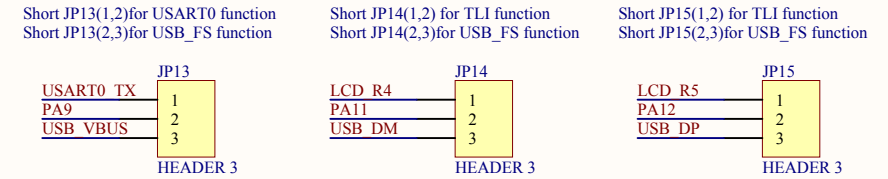
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4

USB HS



USB FS



Company Name: GigaDevice

File Name: USB

Revision: 1.1

Data: 2016-7

Author: Xuwei

