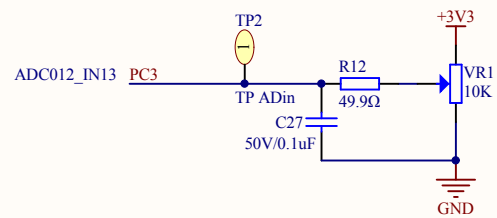
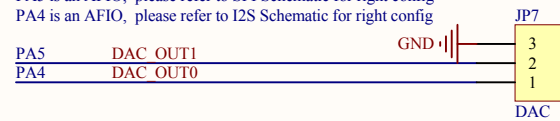


ADC



DAC

PA5 is an AFIO, please refer to SPI Schematic for right config
PA4 is an AFIO, please refer to I2S Schematic for right config



Company Name: GigaDevice

File Name: AD_DA

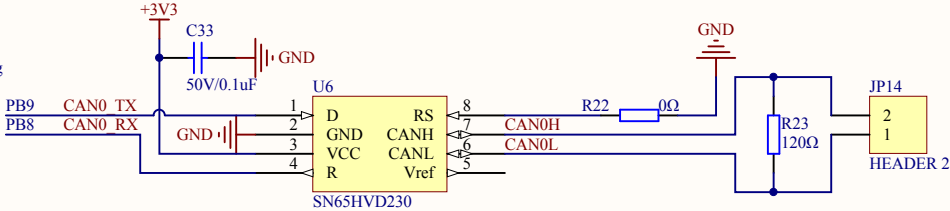
Revision: 1.2

Data: 2015-8-3

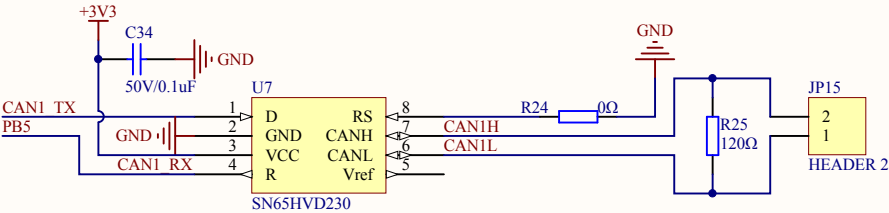
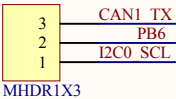
Author: wangzhan

CAN

PB8, PB9 are AFIOs, please refer to DC1 schematic for right config



Short P4(1,2) for I2C0 function
Short P4(2,3) for CAN1 function



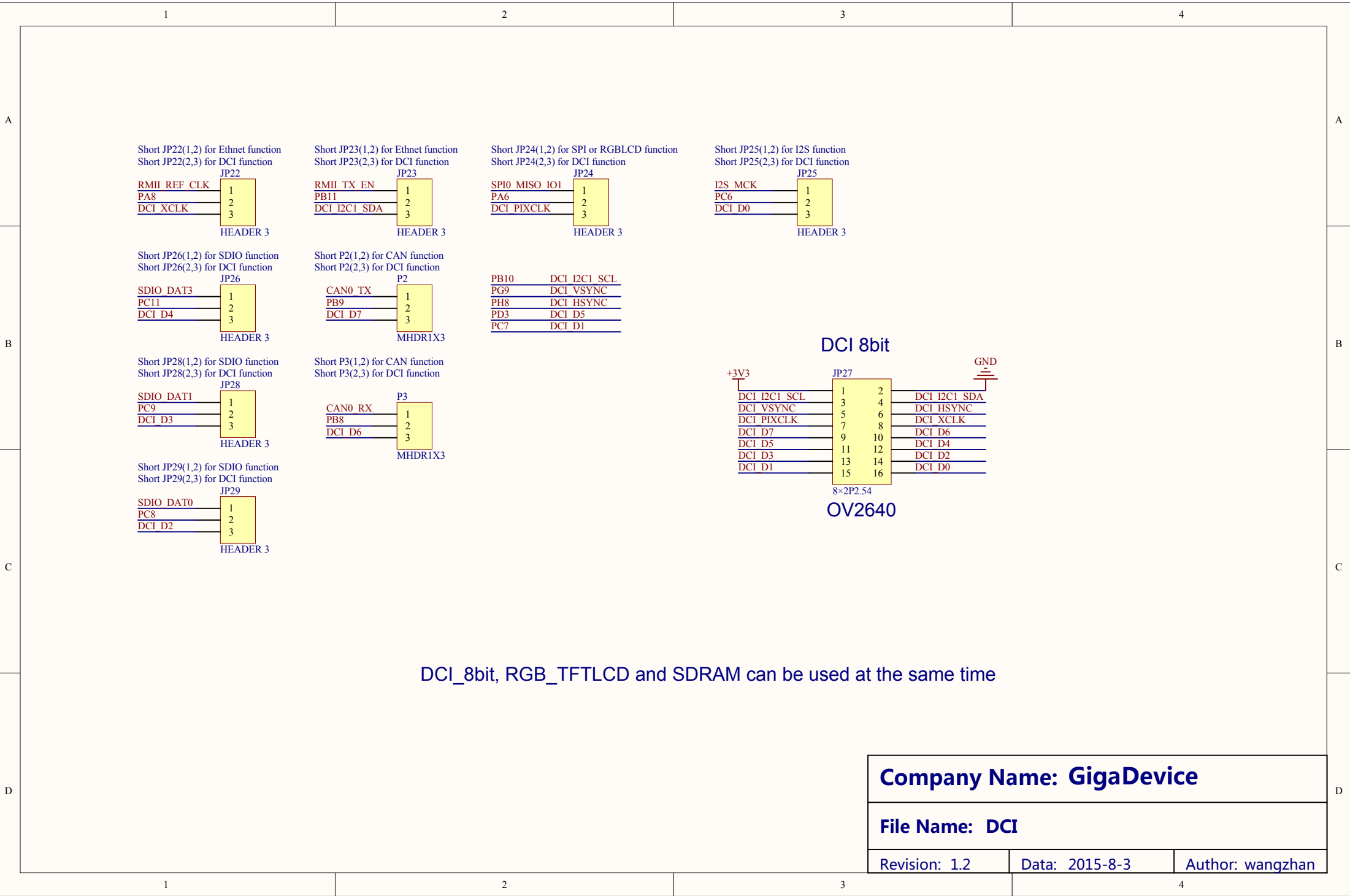
Company Name: GigaDevice

File Name: CAN

Revision: 1.2

Data: 2015-8-3

Author: wangzhan

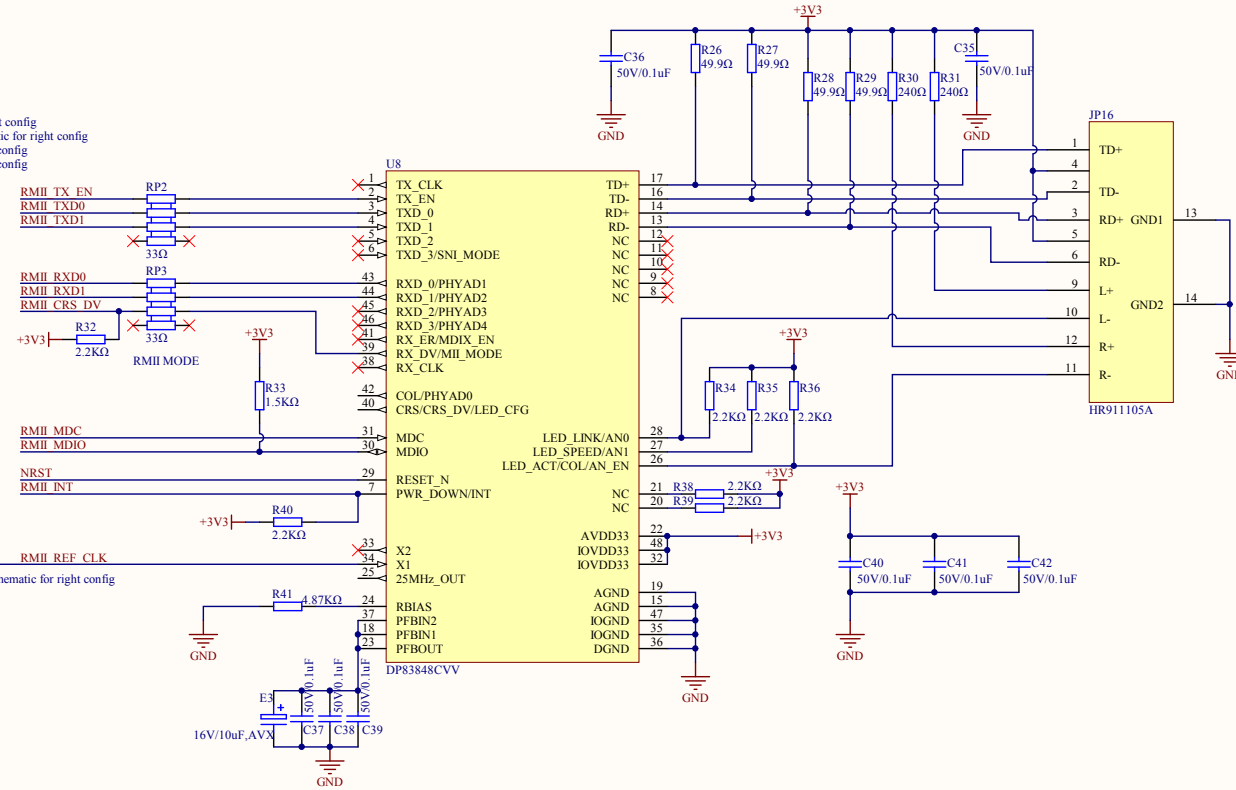


Ethernet

PB11 is an AFIO, refer to DCI schematic for right config
 PB12 and PB13 are AFIOs refer to I2S schematic for right config
 PA7 is an AFIO, refer to SPI schematic for right config
 PA2 is an AFIO, refer to SPI schematic for right config

PB11 RMII TX_EN
 PB12 RMII TXD0
 PB13 RMII TXD1
 PC4 RMII RXD0
 PC5 RMII RXD1
 PA7 RMII CRS_DV
 PC1 RMII MDC
 PA2 RMII MDIO
 PB0 RMII INT
 PA1 RMII REF_CLK

PA8 MCO RMII REF_CLK
 PA8 is an AFIO, refer to DCI schematic for right config



Company Name: GigaDevice

File Name: Ethernet

Revision: 1.2

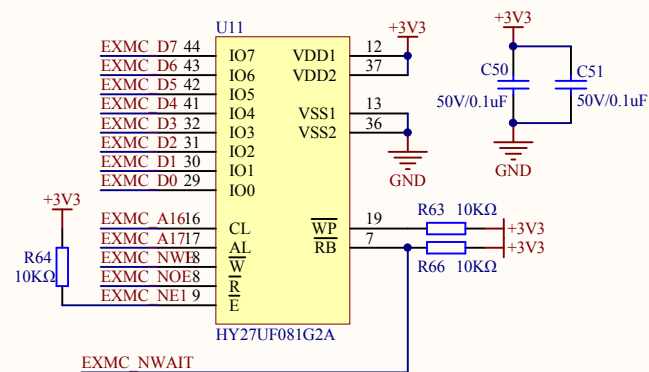
Date: 2015-8-3

Author: wangzhan

Nand Flash

PD14	EXMC_D0
PD15	EXMC_D1
PD0	EXMC_D2
PD1	EXMC_D3
PE7	EXMC_D4
PE8	EXMC_D5
PE9	EXMC_D6
PE10	EXMC_D7

PD11	EXMC_A16
PD12	EXMC_A17
PD7	EXMC_NE1
PD4	EXMC_NOE
PD5	EXMC_NWE
PD6	EXMC_NWAIT



Company Name: GigaDevice

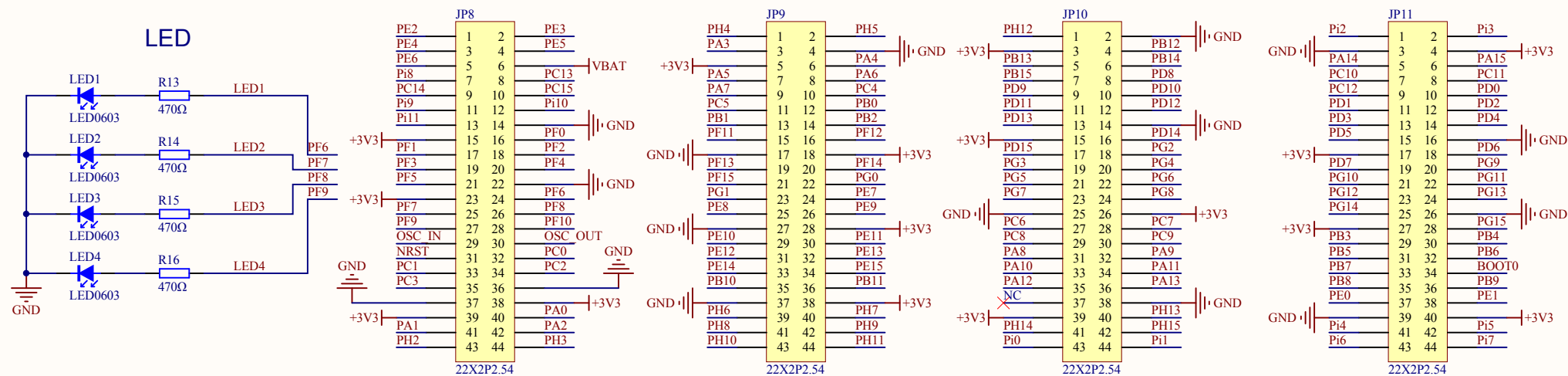
File Name: EXMC

Revision: 1.2

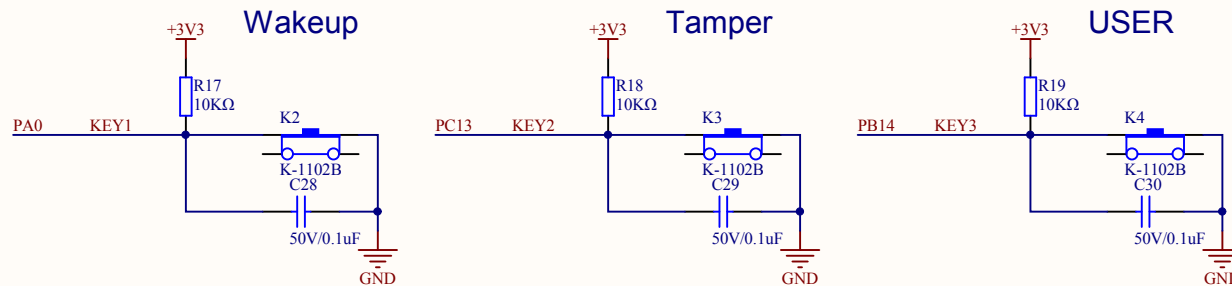
Data: 2015-8-3

Author: wangzhan

Extension Pin



KEY



Company Name: GigaDevice

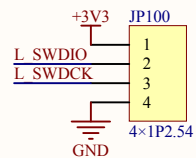
File Name: Extension

Revision: 1.2

Data: 2015-8-3

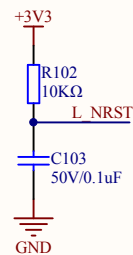
Author: wangzhan

MCU SWD

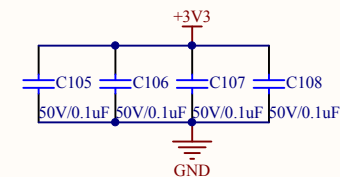
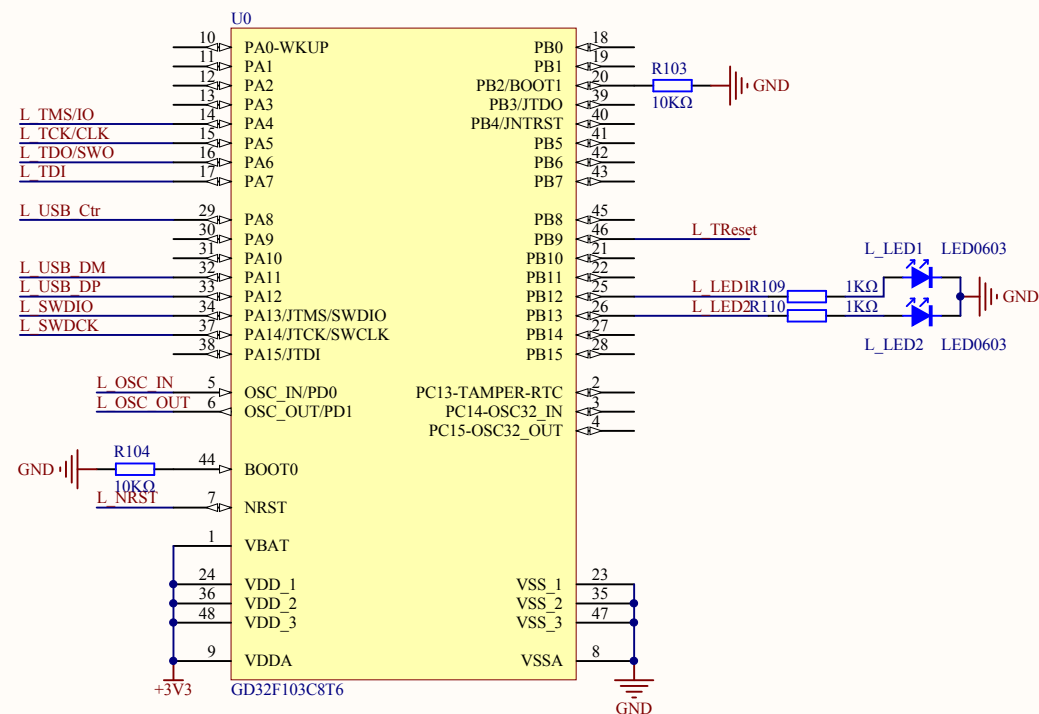
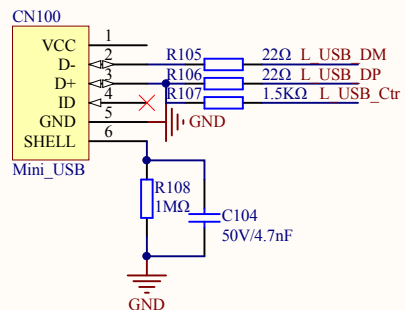
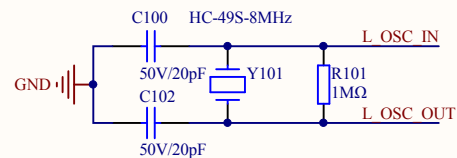


L TDI	PA15
L TMS/IO	PA13
L TCK/CLK	PA14
L TDO/SWO	PB3
L TReset	NRST

Reset



HSE



Company Name: GigaDevice

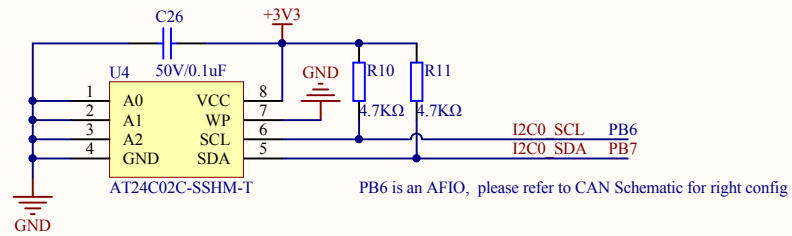
File Name: GDLink

Revision: 1.0

Data: 2015-8-3

Author: XuFei

I2C



Company Name: GigaDevice		
File Name: I2C		
Revision: 1.2	Data: 2015-8-3	Author: wangzhan

I2S

PC6 is an AFIO, please refer to DCI Schematic for right config
PA7 is an AFIO, please refer to SPI Schematic for right config
PA5 is an AFIO, please refer to SPI Schematic for right config

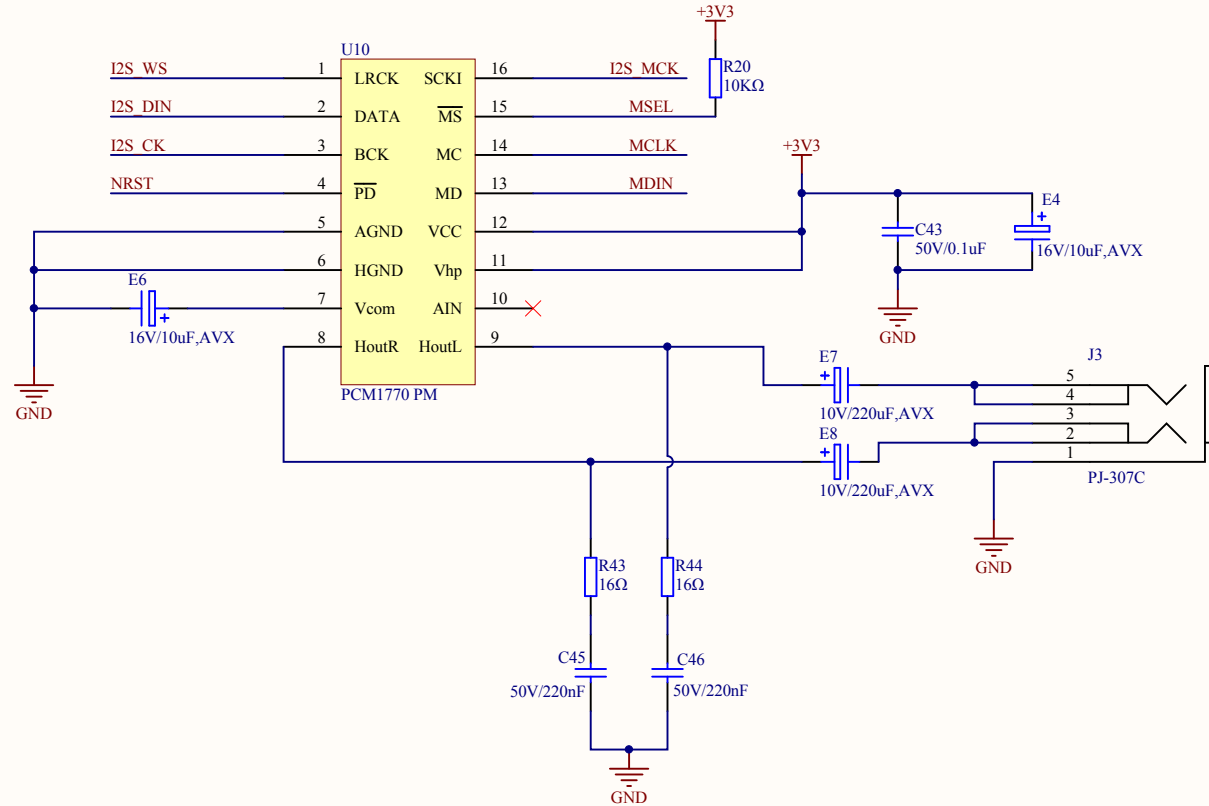
PC6	I2S MCK
PB15	I2S DIN
PA7	SPI0 MOSI IO0 MDIN
PA5	SPI0 SCK MCLK

Short JP18(1,2) and JP19(1,2) for Ethernet function
Short JP18(2,3) and JP19(2,3) for I2S function

JP18	JP19
RMII TXD0	RMII TXD1
PB12	PB13
I2S WS	I2S CK

Short JP21(1,2) for DAC_OUT0 function
Short JP21(2,3) for I2S function

JP21
DAC_OUT0
PA4
MSEL



Company Name: GigaDevice		
File Name: I2S		
Revision: 1.2	Data: 2015-8-3	Author: wangzhan

A

B

C

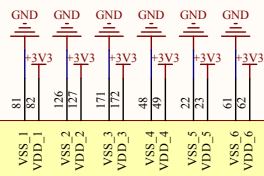
D

A

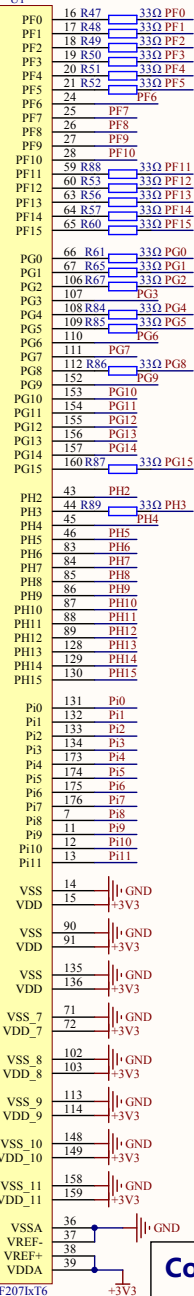
B

C

D



U1



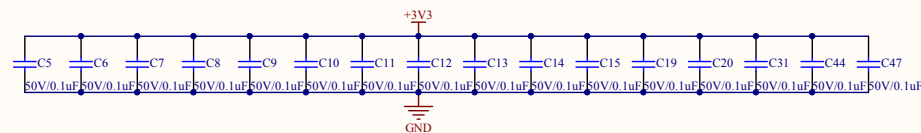
GD32F207kT6

OSC_IN/PD0/PH0
OSC_OUT/PD1/PH1
NRST

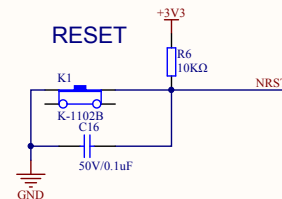
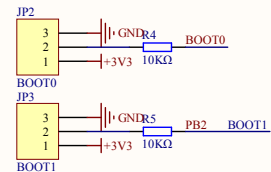
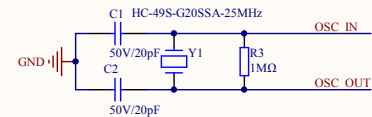
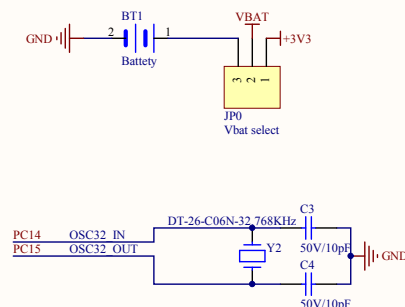
NC

BOOT0

VBAT



JTRST PB4
JTDI PA15
JTMS/SWDIO PA13
JTCK/SWDCLK PA14
JTDO PB3



Company Name: GigaDevice

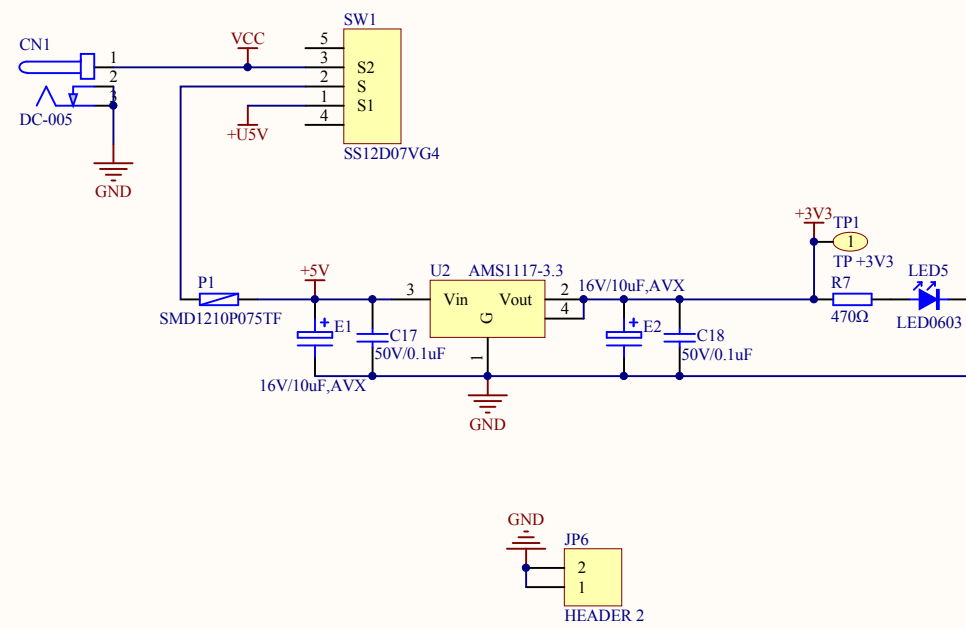
File Name: MCU

Revision: 1.2

Date: 2015-8-3

Author: wangzhan

POWER



Company Name: GigaDevice		
File Name: Power		
Revision: 1.2	Data: 2015-8-3	Author: wangzhan

PA5, PA7 are AFIOs, please refer to SPI schematic for right config
 PA6 are AFIOs, please refer to DCI schematic for right config

RGB_TFTLCD

	PH5	LCD Touch PENIRQ
PA7	SPI0 MOSI IO0	LCD SPI0 MOSI
PA6	SPI0 MISO IO1	LCD SPI0 MISO
PA5	SPI0 SCK	LCD SPI0 SCK
	PG3	LCD SPI0 NSS
	PH6	LCD PWM BackLight
	PH7	LCD Touch Busy

PH4	LCD R0
Pi3	LCD R1

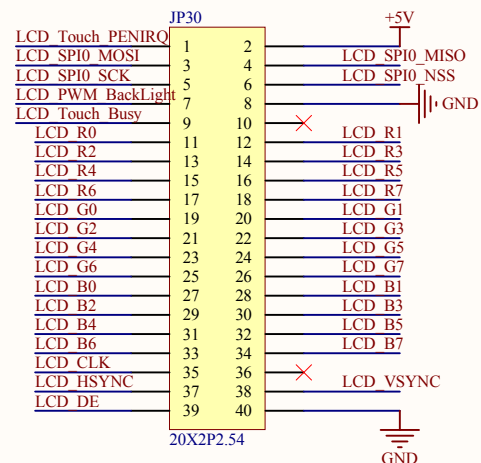
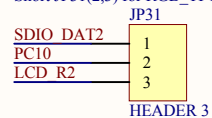
PH9	LCD R3
PH10	LCD R4
PH11	LCD R5
PH12	LCD R6
PG6	LCD R7

PE5	LCD G0
PE6	LCD G1
PH13	LCD G2
PH14	LCD G3
PH15	LCD G4
Pi0	LCD G5
Pi1	LCD G6
Pi2	LCD G7

PE4	LCD B0
PG12	LCD B1
PG10	LCD B2
PG11	LCD B3
Pi4	LCD B4
Pi5	LCD B5
Pi6	LCD B6
Pi7	LCD B7

PG7	LCD CLK
Pi10	LCD HSYNC
Pi9	LCD VSYNC
PF10	LCD DE

Short JP31(1,2) for SDIO function
 Short JP31(2,3) for RGB_TFTLCD function



DCI_8bit, RGB_TFTLCD and SDRAM can be used at the same time

Company Name: GigaDevice

File Name: RGB_TFTLCD

Revision: 1.2

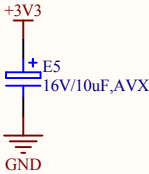
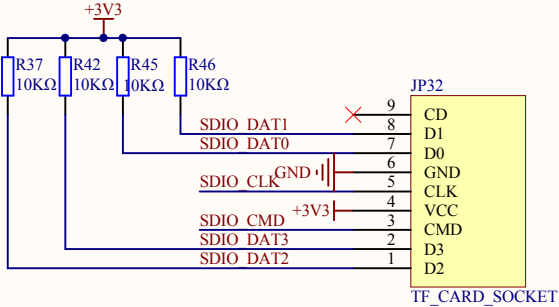
Data: 2015-8-3

Author: wangzhan

SDIO

PC11 is an AFIO, please refer to DCI Schematic for right config
PC8 is an AFIO, please refer to DCI Schematic for right config
PC9 is an AFIO, please refer to DCI Schematic for right config
PC10 is an AFIO, please refer to RGB_TFTLCD Schematic for right config

PC11	SDIO_DAT3
PD2	SDIO_CMD
PC12	SDIO_CLK
PC8	SDIO_DAT0
PC9	SDIO_DAT1
PC10	SDIO_DAT2



Company Name: GigaDevice

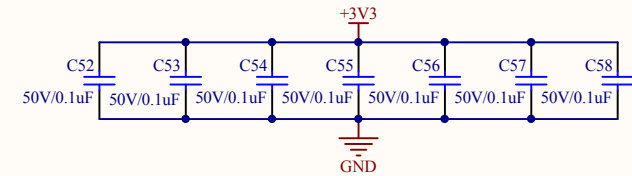
File Name: SDIO

Revision: 1.2

Data: 2015-8-3

Author: wangzhan

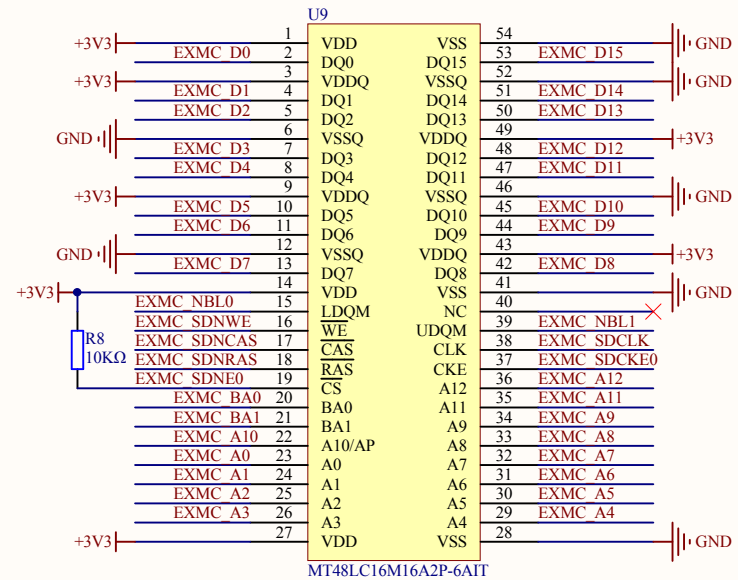
SDRAM



PF0	EXMC_A0
PF1	EXMC_A1
PF2	EXMC_A2
PF3	EXMC_A3
PF4	EXMC_A4
PF5	EXMC_A5
PF12	EXMC_A6
PF13	EXMC_A7
PF14	EXMC_A8
PF15	EXMC_A9
PG0	EXMC_A10
PG1	EXMC_A11
PG2	EXMC_A12

PD14	EXMC_D0
PD15	EXMC_D1
PD0	EXMC_D2
PD1	EXMC_D3
PE7	EXMC_D4
PE8	EXMC_D5
PE9	EXMC_D6
PE10	EXMC_D7
PE11	EXMC_D8
PE12	EXMC_D9
PE13	EXMC_D10
PE14	EXMC_D11
PE15	EXMC_D12
PD8	EXMC_D13
PD9	EXMC_D14
PD10	EXMC_D15

PE0	EXMC NBL0
PE1	EXMC NBL1
PH2	EXMC SDCKE0
PG4	EXMC BA0
PG5	EXMC BA1
PG8	EXMC SDCLK
PG15	EXMC SDNCAS
PF11	EXMC SDNRAS
PH3	EXMC SDNE0
PC0	EXMC SDNWE



DCI_8bit, RGB_TFTLCD and SDRAM can be used at the same time

Company Name: GigaDevice

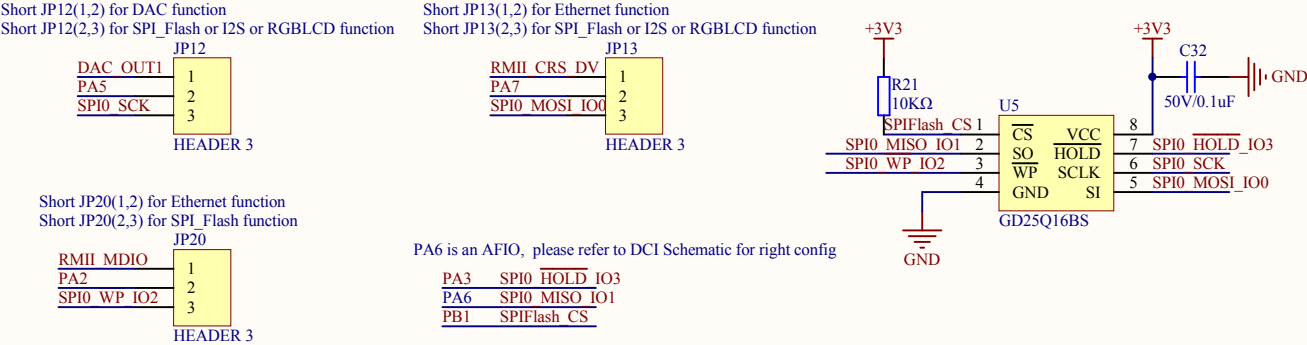
File Name: SDRAM

Revision: 1.2

Data: 2015-8-3

Author: wangzhan

Standard & Quad SPI Flash



Company Name: GigaDevice

File Name: SPI

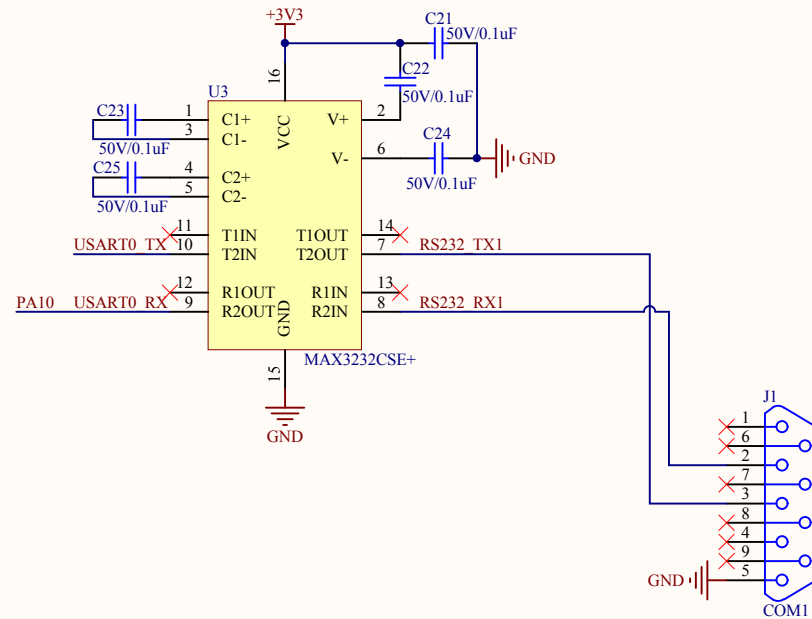
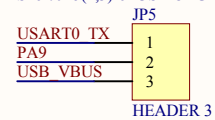
Revision: 1.2

Data: 2015-8-3

Author: wangzhan

USART0

Short JP5(1,2)for USART0 function
Short JP5(2,3)for USB OTG function



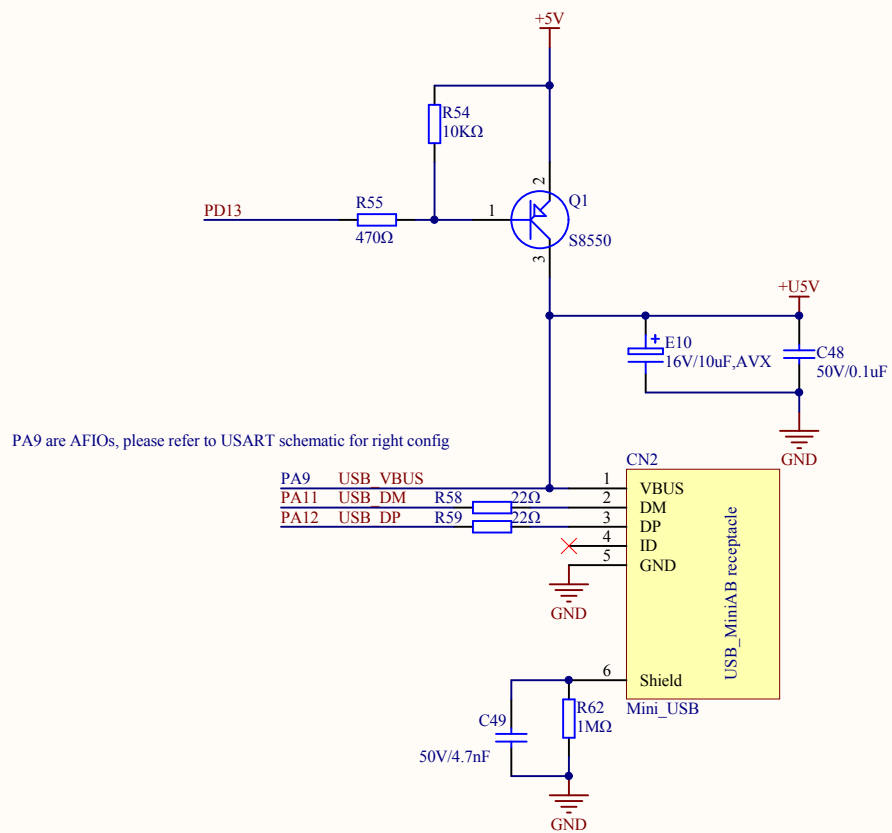
Company Name: GigaDevice

File Name: USART

Revision: 1.2

Data: 2015-8-3

Author: wangzhan



Company Name: GigaDevice		
File Name: USB_OTG		
Revision: 1.2	Data: 2015-8-3	Author: wangzhan

