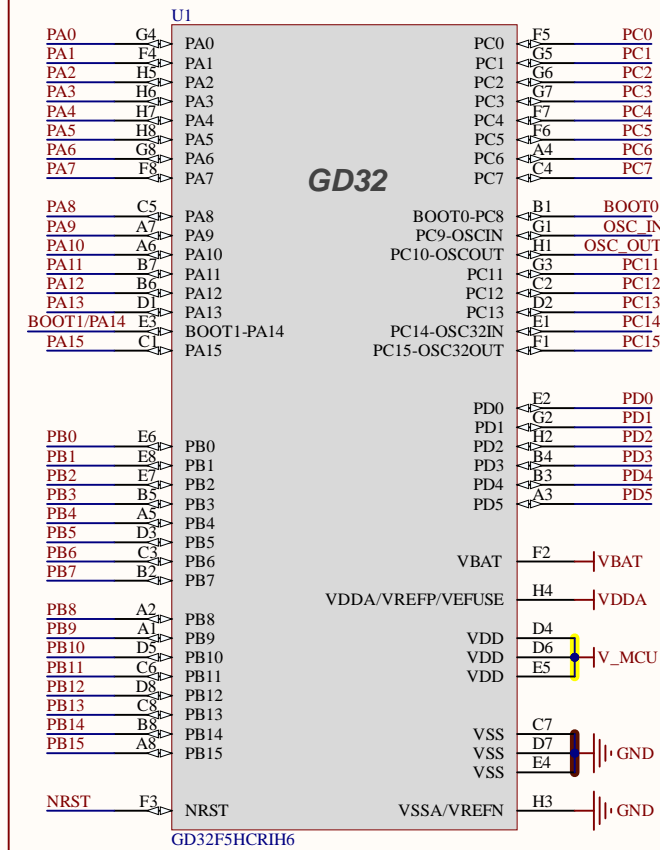
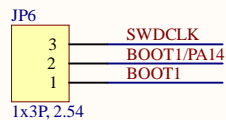


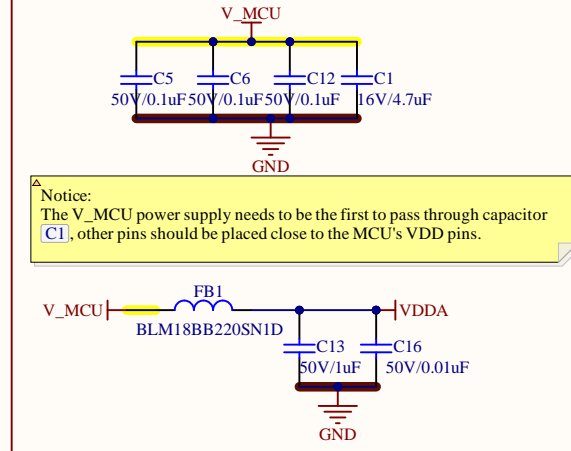
MCU



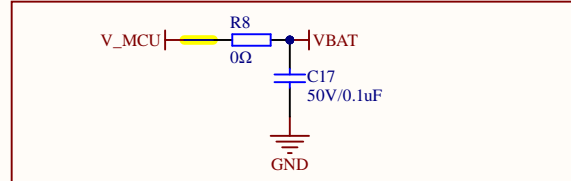
GPIO Function Select



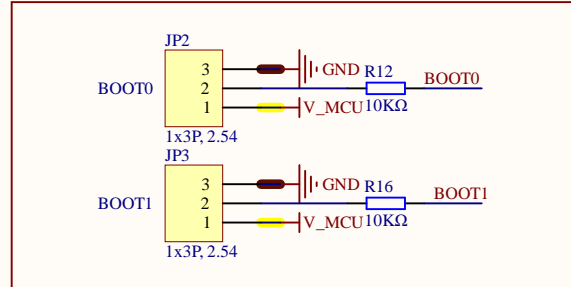
Power Filtering



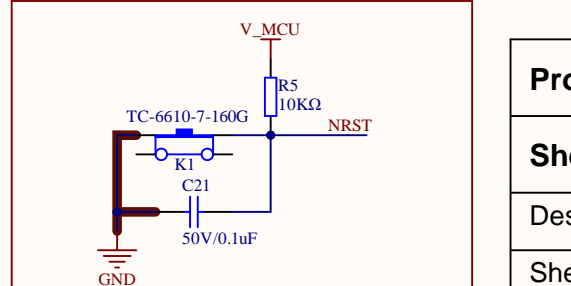
VBAT



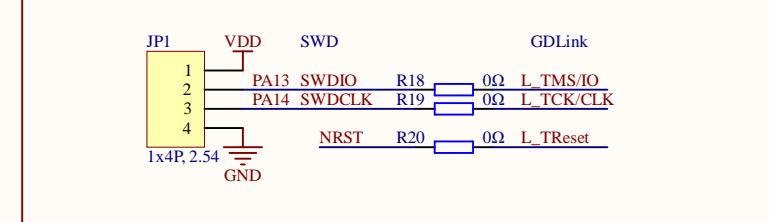
BOOT



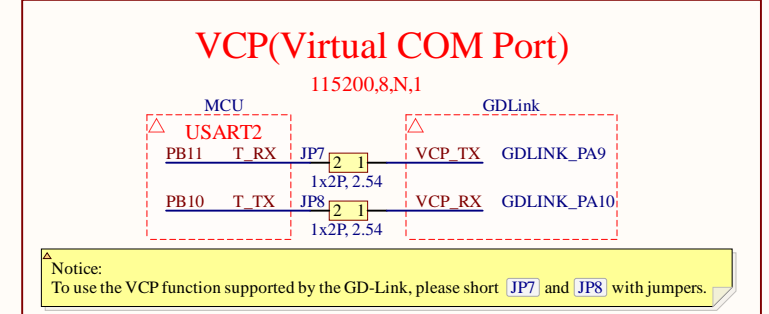
NRST



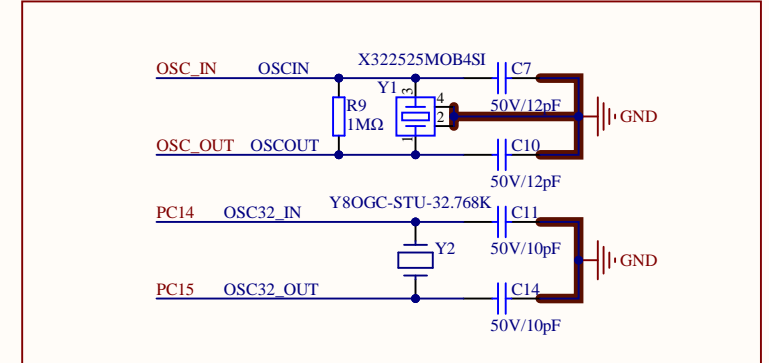
SWD



VCP(Virtual COM Port)



HXTAL&LXTAL



Project Title: GD32F5HCR-EVAL

Sheet Title: MCU

Size:
A4

Designer: boya.jiang

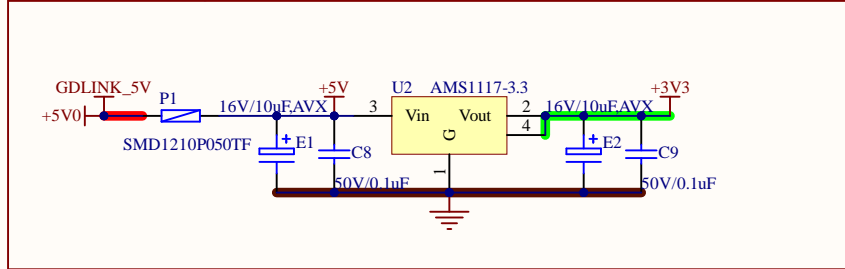
Revision: 1.0

Sheet: 1 of 11

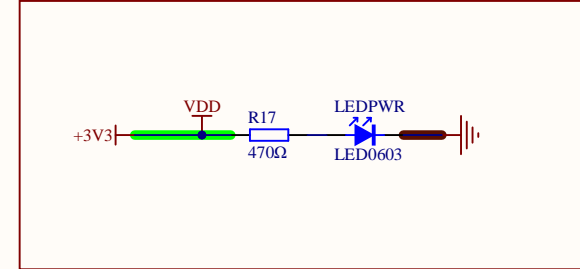
Date: 2025.07



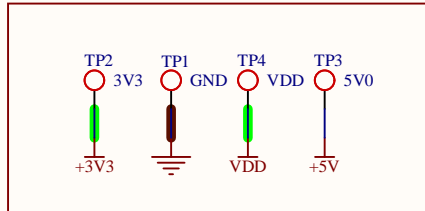
3V3 Power Supply



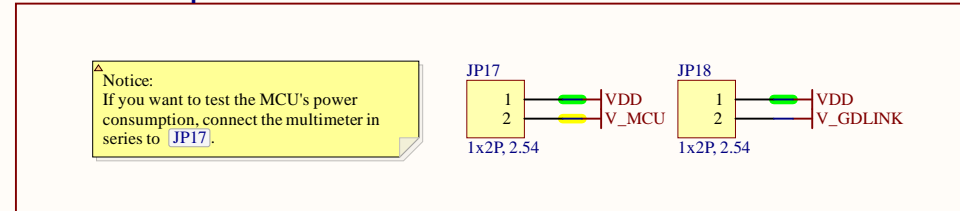
Power LED




Test Point

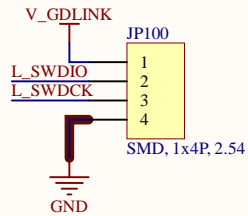


Power Consumption

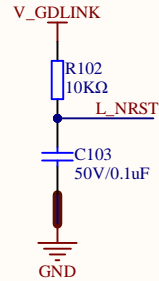


Project Title: GD32F5HCR-EVAL			
Sheet Title: Power			Size: A4
Designer: boya.jiang	Revision: 1.0	 GigaDevice	
Sheet: 2 of 11	Date: 2025.07		

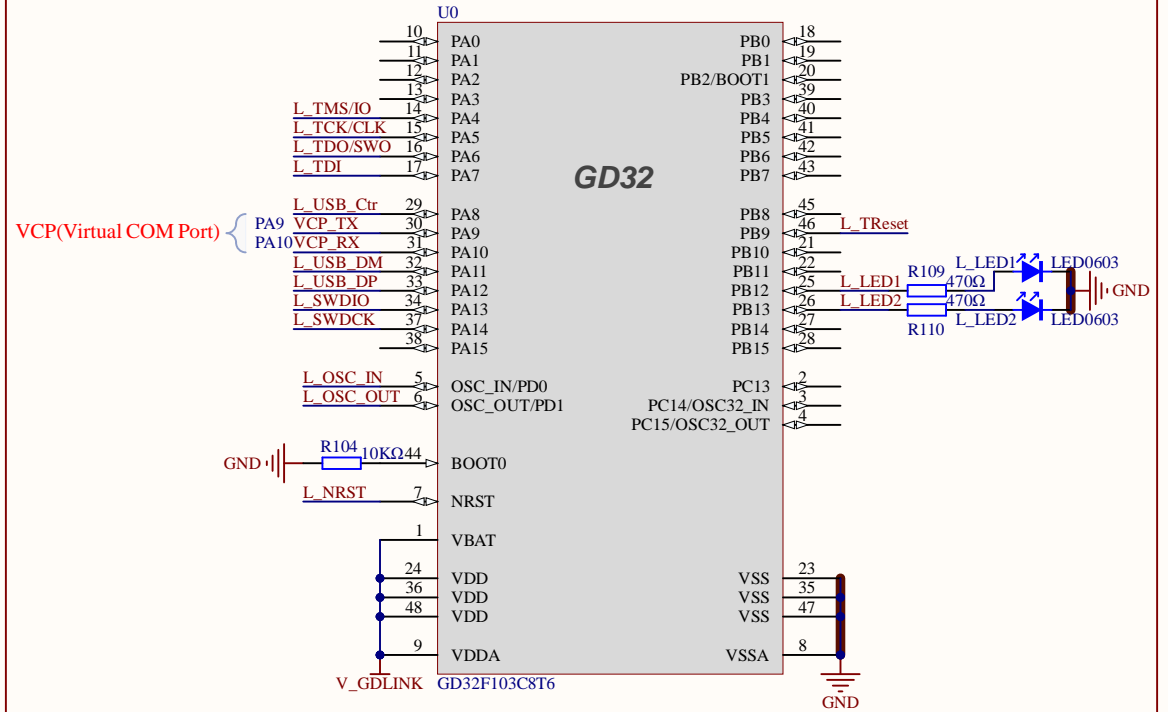
L_SWD



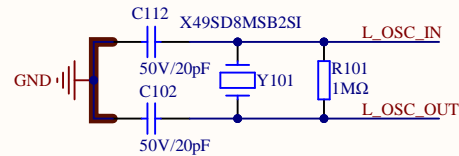
L_NRST



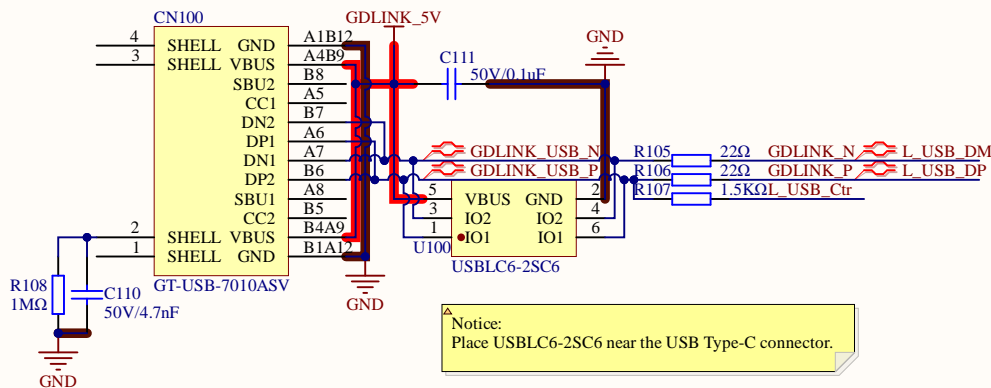
GD32F103C8T6



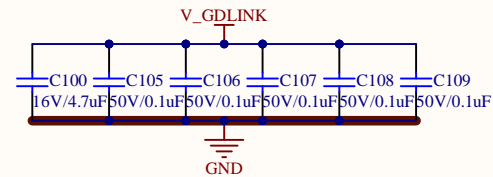
L_HXTAL



Type-C



Power Filtering



Notice:
The V_GDLINK power supply needs to be the first to pass through capacitor C100, other pins should be placed close to the MCU's VDD pins.

Project Title: **GD32F5HCR-EVAL**

Sheet Title: **GD-Link**

Size:
A4

Designer: **boya.jiang**

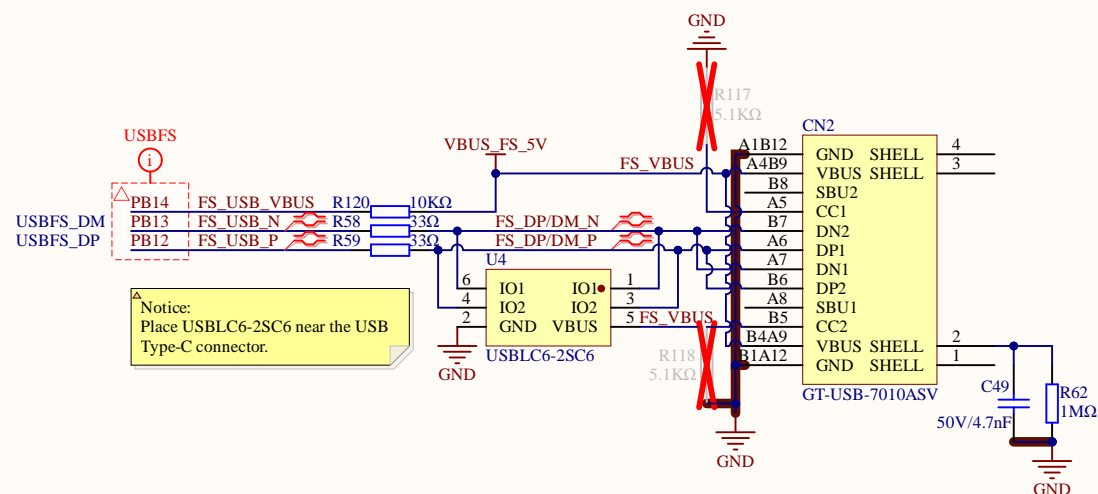
Revision: **1.0**

Sheet: **3 of 11**

Date: **2025.07**

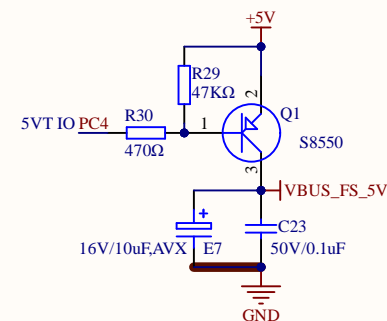


USB_FS



Notice:
By default, the two 5.1K Ω resistors are not installed. If using Type-C to Type-C cables, these two 5.1K Ω resistors need to be installed, and then the USB can be used as a slave device.

USB_FS VBUS Power Control



Project Title: **GD32F5HCR-EVAL**

Sheet Title: **USB_FS**

Size:
A4

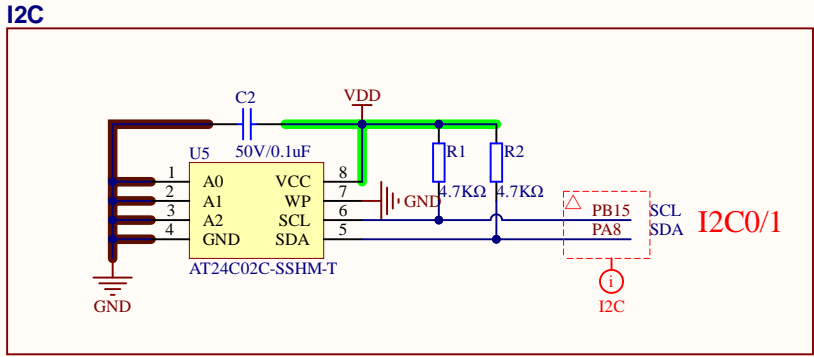
Designer: **boya.jiang**

Revision: **1.0**

Sheet: **4 of 11**

Date: **2025.07**





Project Title: **GD32F5HCR-EVAL**

Sheet Title: **I2C**

Size:
A4

Designer: **boya.jiang**

Revision: **1.0**

Sheet: **5 of 11**

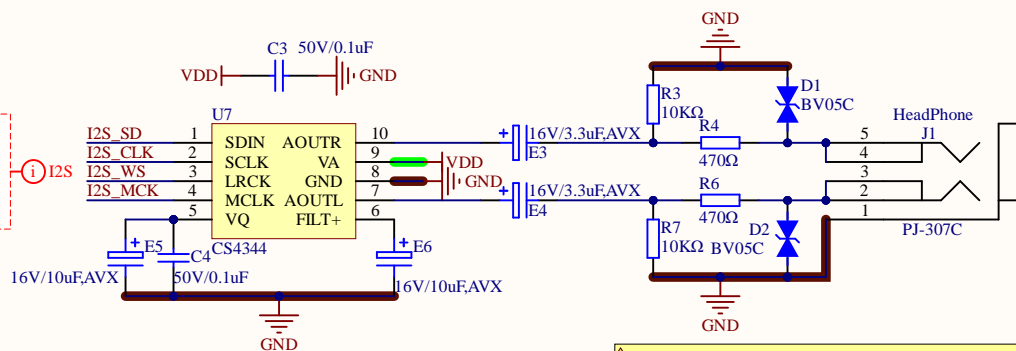
Date: **2025.07**



I2S

I2S

PA4	I2S_SD
PB8	I2S_CLK
PA7	I2S_WS
PA5	I2S_MCK



Notice:
Place BV05C near the PJ-307C Headphone connector.

Project Title: **GD32F5HCR-EVAL**

Sheet Title: **I2S**

Size:
A4

Designer: **boya.jiang**

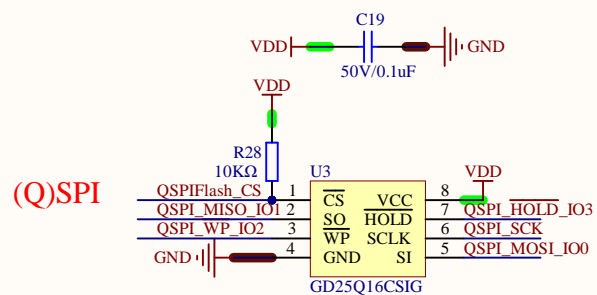
Revision: **1.0**

Sheet: **6 of 11**

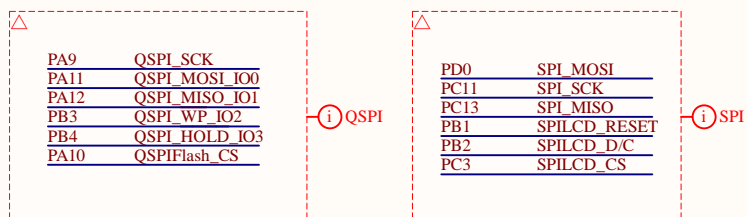
Date: **2025.07**



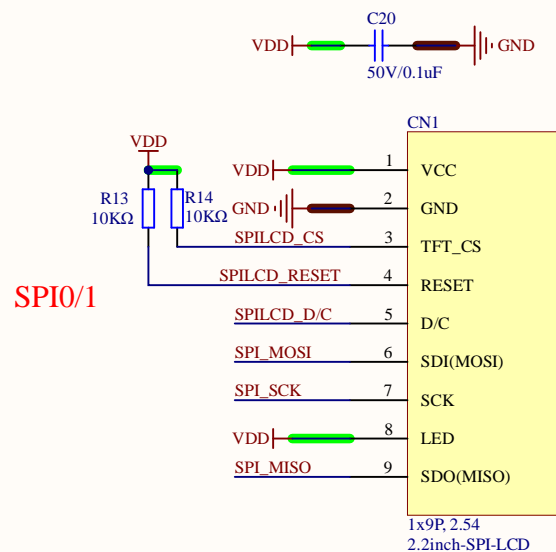
(Q)SPI Flash



Signal IO Map



SPI LCD



Project Title: **GD32F5HCR-EVAL**

Sheet Title: **SPI**

Size:
A4

Designer: **boya.jiang**

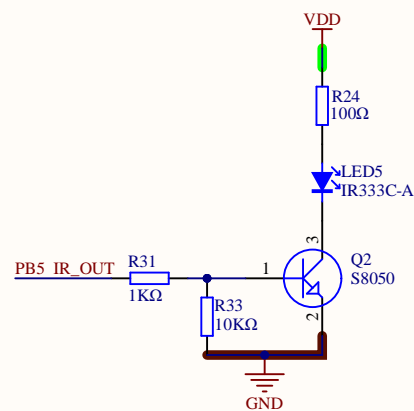
Revision: **1.0**

Sheet: **7 of 11**

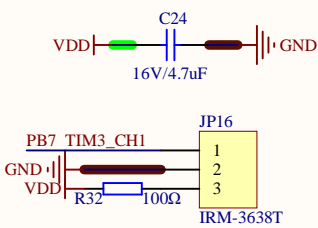
Date: **2025.07**



IFRP Infrared Emitter



IFRP Infrared Receiver



Project Title: **GD32F5HCR-EVAL**

Sheet Title: **IFRP**

Size:
A4

Designer: **boya.jiang**

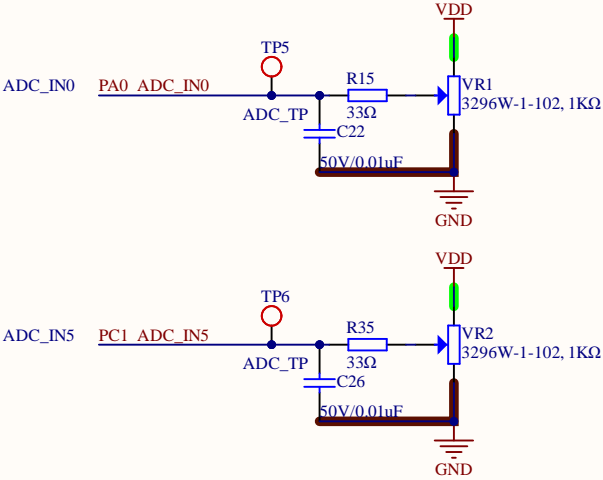
Revision: **1.0**

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Date: **2025.07**



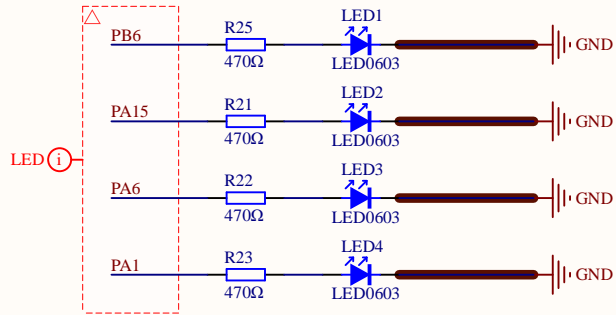
ADC



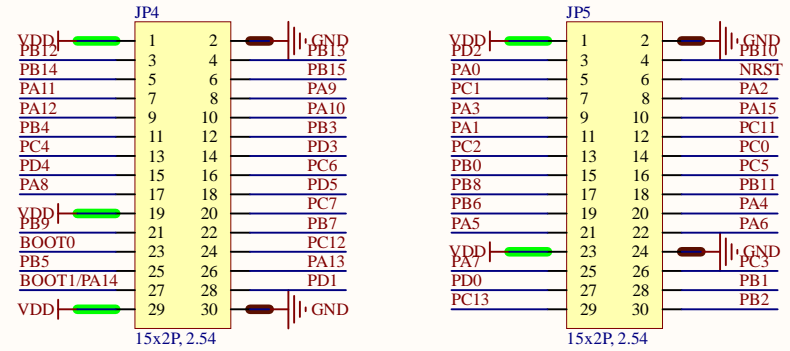
△ Notice:
The capacitors and resistors of the low-pass filter need to be placed close to the ADC input channel, especially the capacitors.

Project Title: GD32F5HCR-EVAL			
Sheet Title: ADC			Size: A4
Designer: boya.jiang	Revision: 1.0	 GigaDevice	
Sheet: 9 of 11	Date: 2025.07		

LED

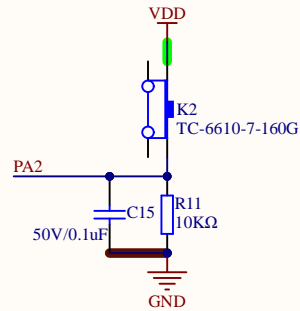


Extension Pin



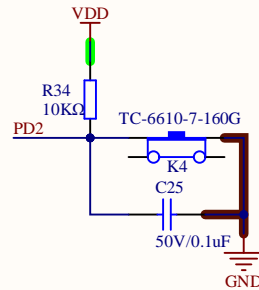
WKUP Key

WKUP & RTC_TAMP2

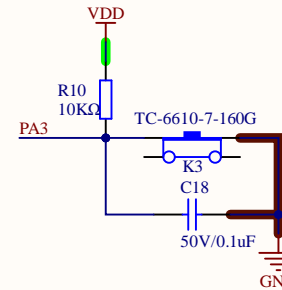


User Key

User Key1



User Key2



Project Title: **GD32F5HCR-EVAL**

Sheet Title: **Extension**

Size:
A4

Designer: **boya.jiang**

Revision: **1.0**

Sheet: **10 of 11**

Date: **2025.07**



LOGO & Precautions

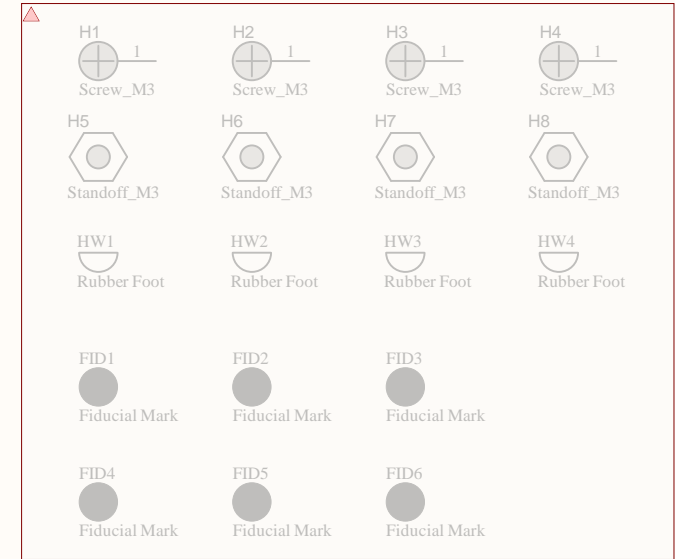
LG1
PCB
LOGO
GD32-LOGO

LG4

GD32-LOGO-No touching



Assembly Parts



Variants Notes

Variants Name	Description
Default	Default assembly options

Project Title: **GD32F5HCR-EVAL**

Sheet Title: **Assembly Parts**

Size:
A4

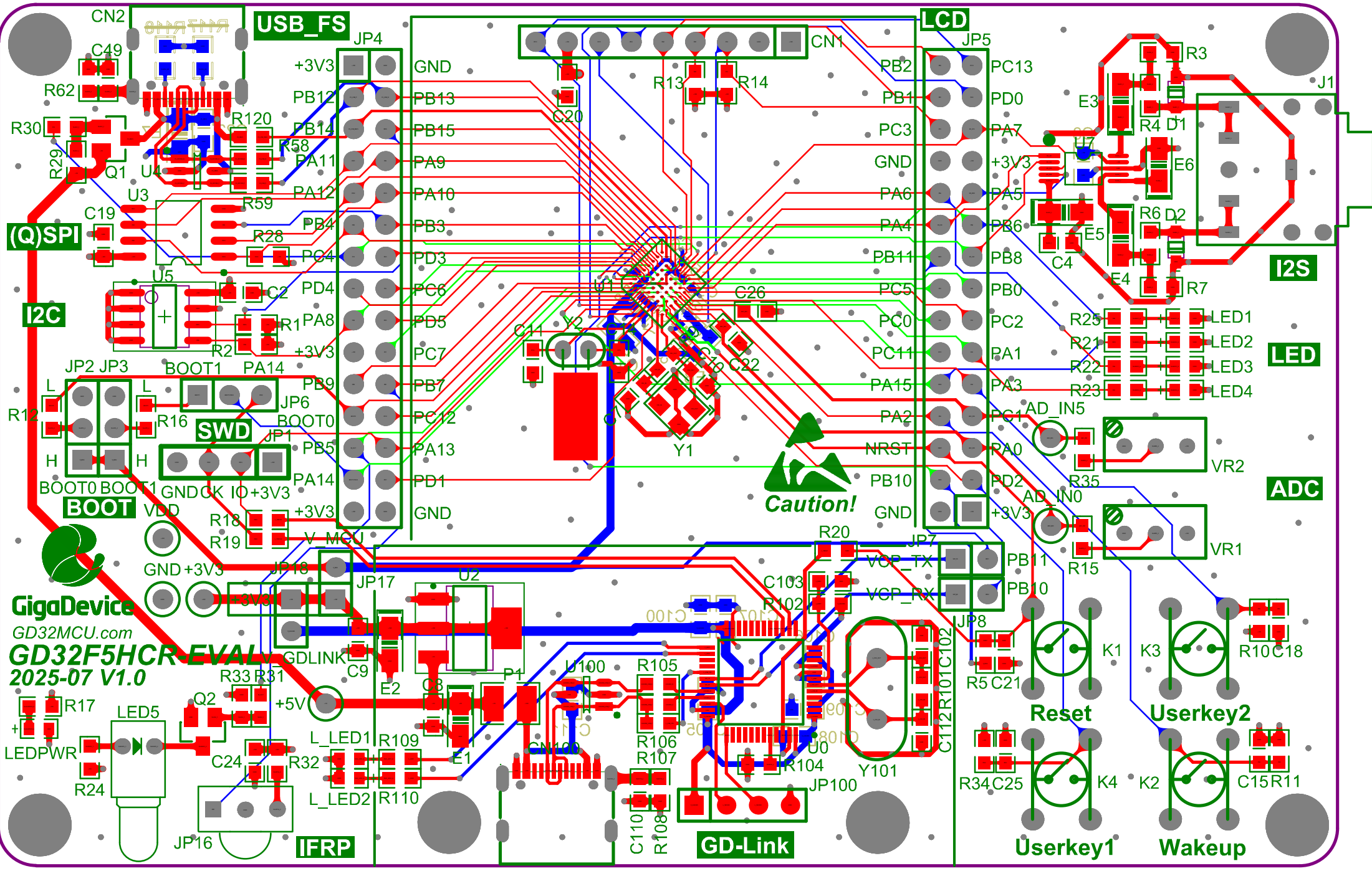
Designer: **boya.jiang**

Revision: **1.0**

Sheet: **11 of 11**

Date: **2025.07**





GigaDevice
GD32MCU.com
GD32F5HCR-EVAL
2025-07 V1.0

Caution!

Reset
Userkey1
Userkey2
Wakeup